



WANSEMI
万晶半导体

WX012N04LL

N-channel Enhancement Mode Power MOSFET

TOLL/NMOS/40V/ $\pm 20V$ /1.6V/298A/1.15m Ω

Rev0.5



40V, 1.15mΩ, 298A, N-channel MOSFET

1. Features

- ◆ $V_{DS}=40V$, $I_D=298A$
- ◆ $R_{DS(on)}_{typ}: 1.15m\Omega$ @ $V_{GS}=10V$
- ◆ Excellent $R_{DS(on)}$ and Low Gate Charge
- ◆ 100% RG Tested
- ◆ 100% UIS Tested

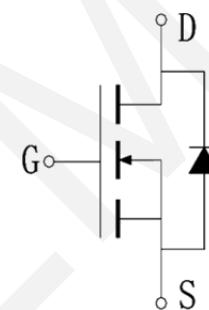
V_{DS}	$R_{DS(on)} \text{ Typ.}$	$I_D \text{ Max.}$
40V	1.15mΩ @10V	298A

2. Applications

- ◆ Load Switch
- ◆ PWM Application
- ◆ General Automotive Application



TOLL
Pin Description



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Package	Marking	PCS/Reel	PCS/CTN.
WX012N04LL	TOLL	012N04	2,000	16,000

4. Absolute Max Ratings at $T_a=25^\circ C$ (Note1)

Parameter		Symbol	Maximum	Units
Drain to Source Voltage		V_{DSS}	40	V
Gate to Source Voltage		V_{GSS}	± 20	V
Drain Current (DC)	$T_C = 25^\circ C$	I_D	298	A
	$T_C = 100^\circ C$	I_D	179	A
Drain Current (Pulse), $PW \leq 300\mu s$		I_{DM}	1192	A
Avalanche Energy, Single Pulsed		E_{AS}	900	mJ
Total Dissipation	$T_C = 25^\circ C$	P_D	310	W
Junction Temperature		T_j	150	°C
Storage Temperature		T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

**5.Thermal Resistance Ratings (Note 2)**

Parameter	Symbol	Value	Unit
Junction to case	R _{θJC}	0.5	°C/W
Junction to Ambient	R _{θJA}	36	°C/W

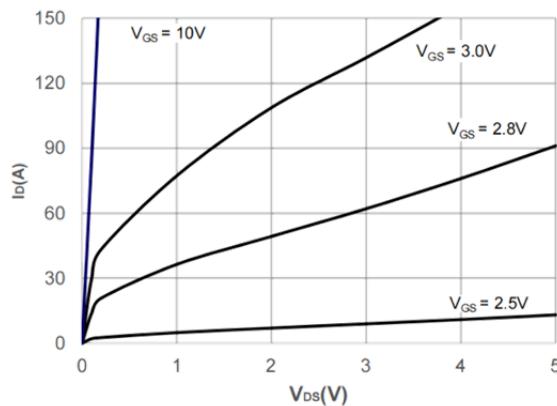
Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

6.Electrical Characteristics at Ta=25°C (Note 3)

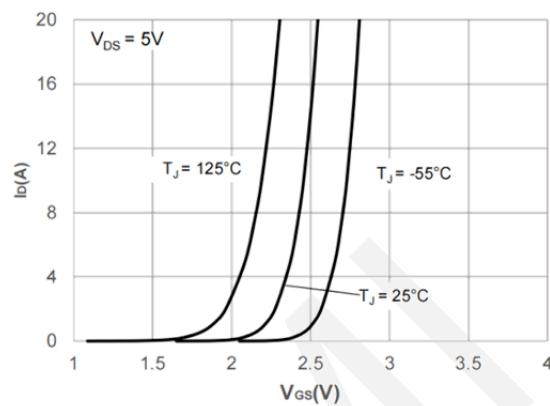
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	I _D = 250μA, V _{GS} = 0V	40	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40V, V _{GS} = 0V	-	-	1	μA
Gate to Source Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.3	1.6	2.0	V
Static Drain to Source On-State Resistance	R _{DS(on)}	I _D =15A, V _{GS} =10V	-	1.15	1.5	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =20V, Frequency=1.0MHz	-	5800	-	pF
Output Capacitance	C _{oss}		-	3200	-	pF
Reverse Transfer Capacitance	C _{rss}		-	240	-	pF
Turn-on Delay Time	t _{d(on)}	V _{DS} = 20V, V _{GS} = 10V, I _D =20A, R _{GEN} = 3Ω	-	17	-	ns
Rise Time	t _r		-	26	-	ns
Turn-off Delay Time	t _{d(off)}		-	75	-	ns
Fall Time	t _f		-	33	-	ns
Total Gate Charge	Q _g	V _{DS} = 20V, V _{GS} = 10V, I _D =20A	-	118	-	nC
	Q _{gs}		-	20	-	nC
	Q _{gd}		-	26	-	nC
Diode Forward Voltage	V _{FSD}	I _S = 20A, V _{GS} = 0	-	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

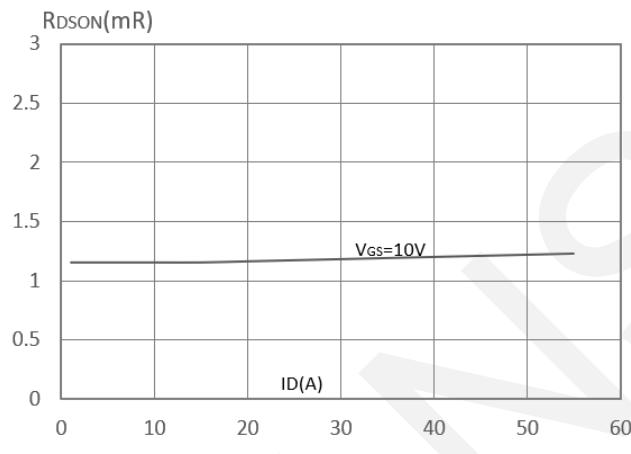
7.Typical electrical and thermal characteristics



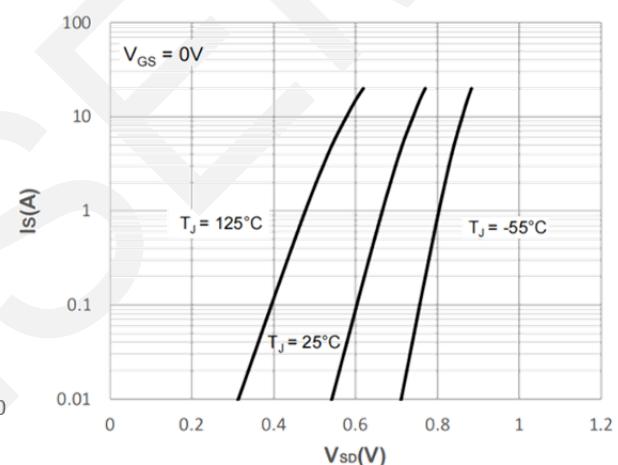
Saturation Characteristics



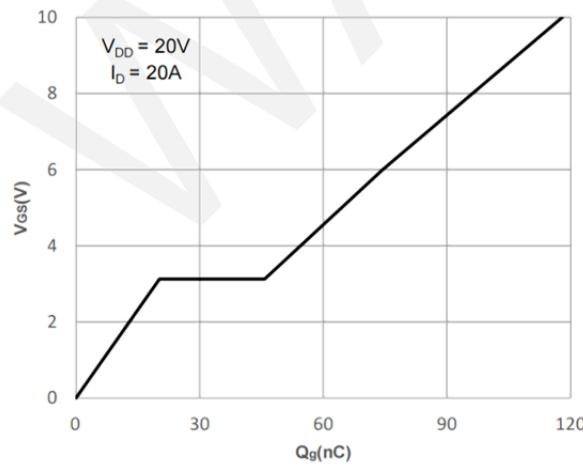
Transfer Characteristics



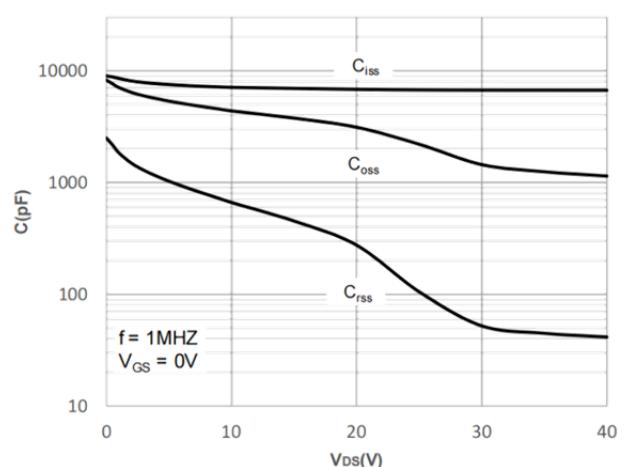
R_{DS(on)}vs.Drain Current



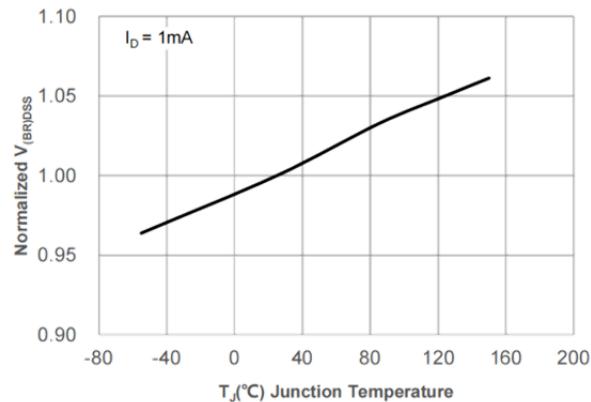
R_{DS(on)}vs.Junction Temperature



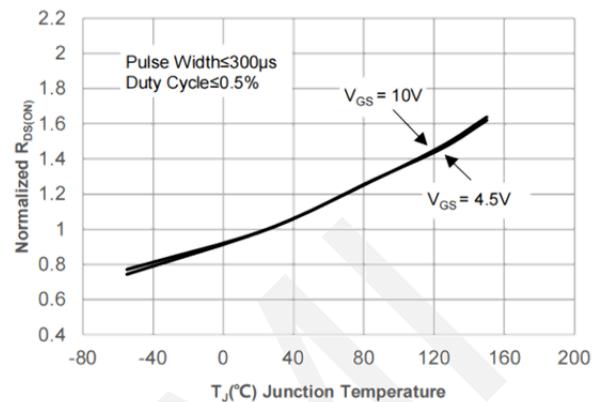
Gate Charge Characteristics



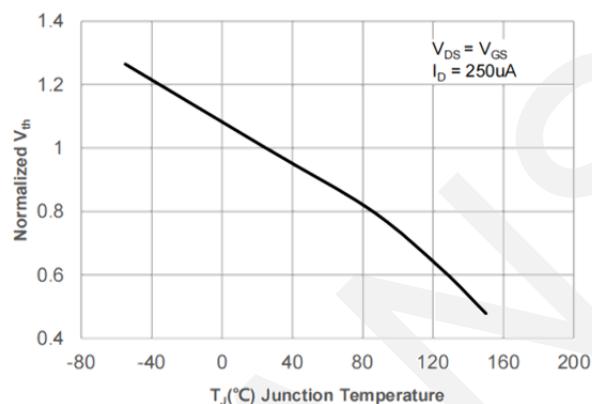
Capacitance Characteristics



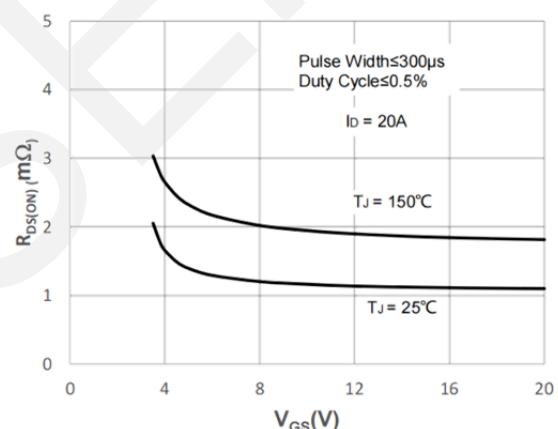
Normalized Breakdown voltage vs.
Junction Temperature



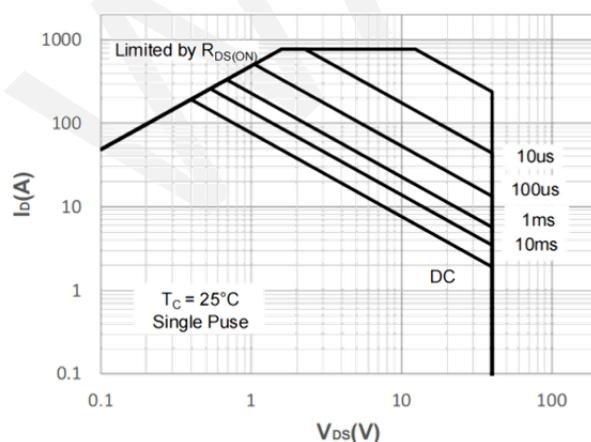
Normalized on Resistance vs.
Junction Temperature



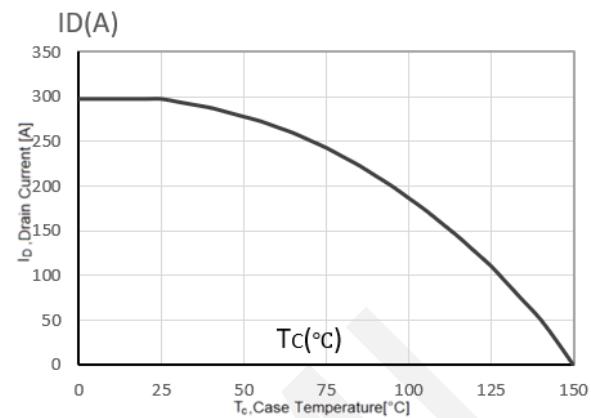
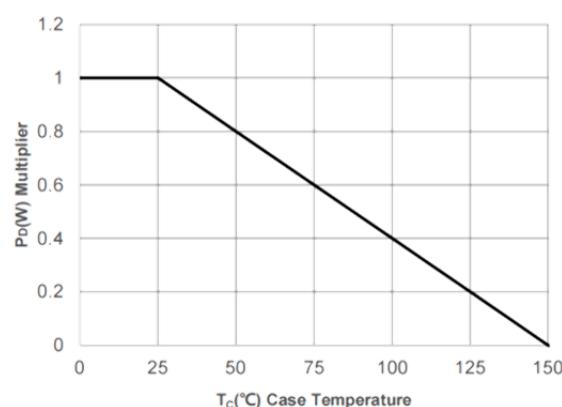
Normalized Threshold Voltage vs.
Junction Temperature



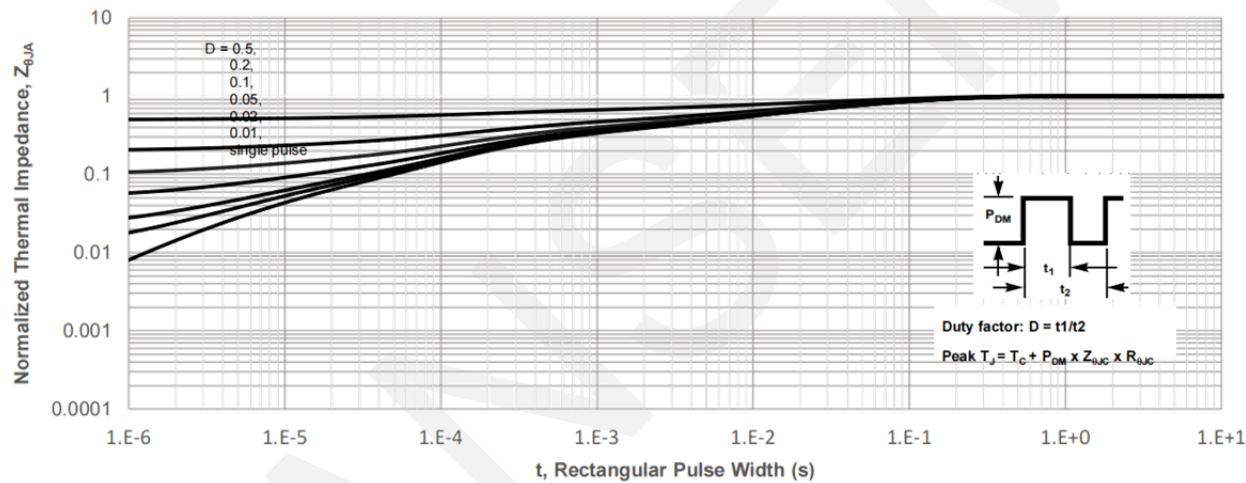
$R_{DS(ON)}$ vs. V_{GS}



Maximum Safe Operating Area



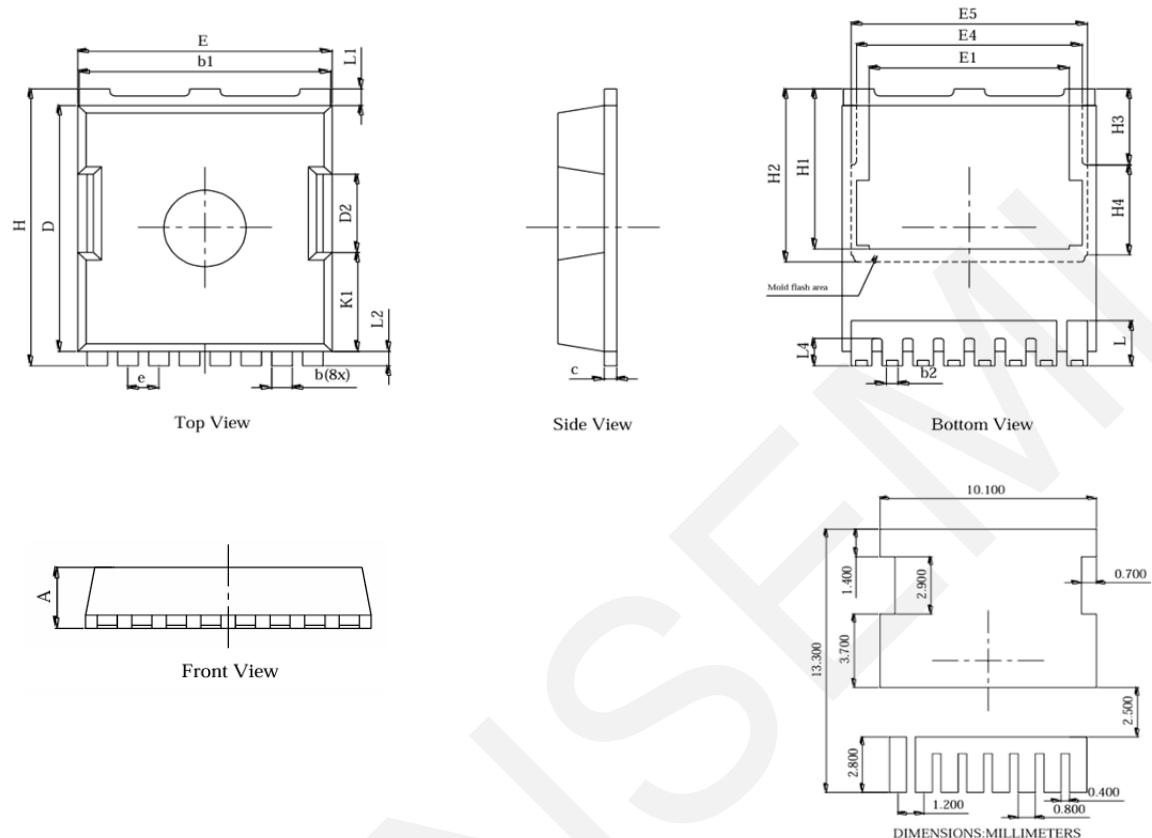
Power De-rating



Normalized Maximum Transient Thermal Impedance



8.Package Dimensions



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.42	0.46	0.50
c	0.40	0.50	0.60
D	10.28	10.38	10.58
D2		3.30	
E	9.70	9.90	10.10
E1		7.80	
E4		8.80	
E5		9.20	
e	1.20 (BSC)		
H	11.48	11.68	11.88
H1	6.55	6.75	6.85
H2		7.30	
H3		3.20	
H4		3.80	
K1		4.18	
L	1.70	1.90	2.10
L1		0.70	
L2		0.60	
L4	1.00	1.15	1.30

9. Important Notice

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