

# N-channel Enhancement Mode Power MOSFET

TOLL/NMOS/40V/ $\pm$ 20V/2.8V/275A/1.15m $\Omega$ 

Rev<sub>0.5</sub>



 $I_{\mathsf{D}}\, Max.$ 



# 40V, $1.15m\Omega$ , 275A, N-channel MOSFET

#### 1.Features

- ◆ Ultra-low ON-resistance, RDS(ON)
- ◆ Low Gate Charge
- ♦ 100% UIS Tested
- 100% ΔVds Tested
- ◆ Halogen-free; RoHS-compliant
- ◆ AEC-Q101 Qualified

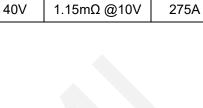
# 2.Applications

- ◆ Load Switch
- PWM Application
- ♦ General Automotive Application



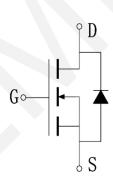


TOLL Pin Description



 $R_{DS(on)} \, Typ. \,$ 

 $V_{\text{DS}} \\$ 



Schematic Diagram

# 3. Package Marking and Ordering Information

Part no.	Package	Marking	PCS/Reel	PCS/CTN.
WX012AN04LL	TOLL	012AN04	2,000	16,000

# 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter		Symbol	Maximum	Units	
Drain to Source Voltage		V <sub>DSS</sub>	40	V	
Gate to Source Voltage		V <sub>GSS</sub>	±20	V	
Drain Current (DC)	T C = 25°C	ID	275	Α	
	T C = 100°C	ID	195	А	
Drain Current (Pulse), PW≤300μs		Ідм	1100	А	
Avalanche Energy, Single Pulsed		Eas	618	mJ	
Total Dissipation	T C = 25°C	P <sub>D</sub>	188	W	
Junction Temperature		Tj	175	°C	
Storage Temperature		$T_{stg}$	-55 to +175	°C	



Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

# 5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit	
Junction to case	Rejc	0.8	°C/W	
Junction to Ambient	Reja	41	°C/W	

Note 2: When mounted on 1 inch square copper board  $t \le 10$ sec The value in any given application depends on the user's specific board design.

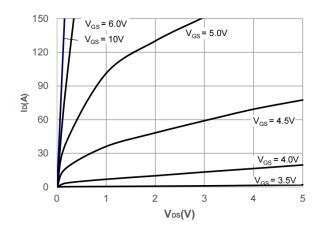
# 6.Electrical Characteristics at Ta=25°C (Note 3)

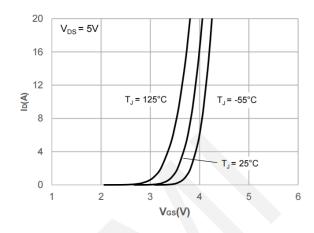
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$I_D = 250 \mu A, V_{GS} = 0 V$	40	•	-	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V	-	1	1	μΑ
Gate to Source Leakage Current	Igss	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA	1.8	2.8	3.4	V
Static Drain to Source On-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =15A, V <sub>GS</sub> =10V	-	1.15	1.6	mΩ
Input Capacitance	Ciss	V <sub>GS</sub> =0V,	3931	5504	7430	pF
Output Capacitance	Coss	V <sub>DS</sub> =20V,	2024	2833	3825	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	Frequency=1.0MHz	77	107	145	pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> = 20V,	-	20	-	ns
Rise Time	tr	$V_{GS} = 20V$ , $V_{GS} = 10V$ ,	-	30	-	ns
Turn-off Delay Time	t <sub>d(off)</sub>	I <sub>D</sub> =20A,	-	41	-	ns
Fall Time	t <sub>f</sub>	$R_{GEN} = 3\Omega$	-	14	-	ns
	Qg	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 10V, I <sub>D</sub> =20A	51	71	96	nC
Total Gate Charge	Qgs		16	23	31	nC
	$Q_{gd}$		9	13	17	nC
Diode Forward Voltage	V <sub>FSD</sub>	I <sub>S</sub> = 1A, V <sub>GS</sub> = 0	-	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

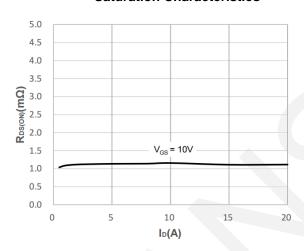


# 7. Typical electrical and thermal characteristics

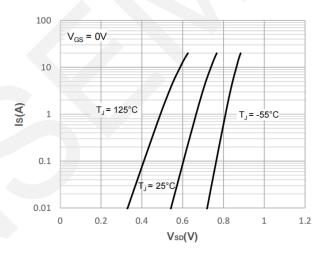




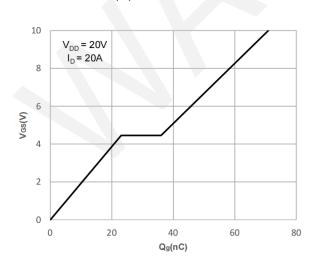
#### **Saturation Characteristics**



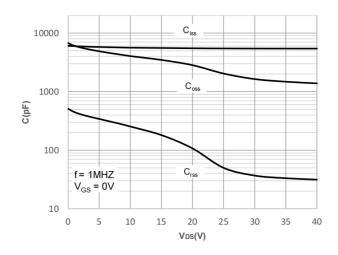
**Transfer Characteristics** 



R<sub>DS(on)</sub>vs.Drain Current



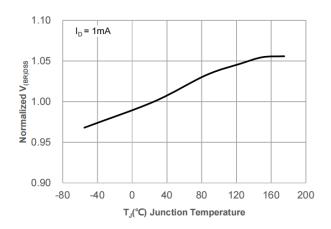
R<sub>DS(on)</sub>vs.Junction Temperature

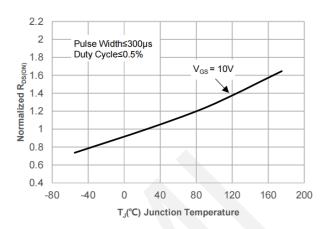


**Gate Charge Characteristics** 

**Capacitance Characteristics** 

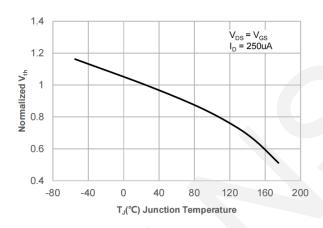






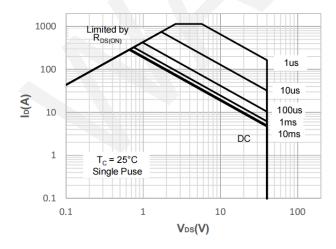
#### Normalized Breakdown voltage vs.

#### **Junction Temperature**



Normalized Threshold Voltage vs.

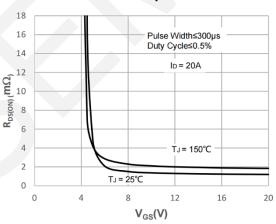
# **Junction Temperature**



**Maximum Safe Operating Area** 

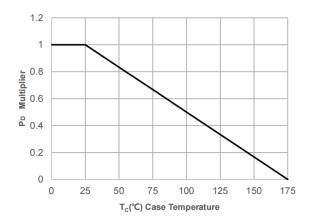
#### Normalized on Resistance vs.

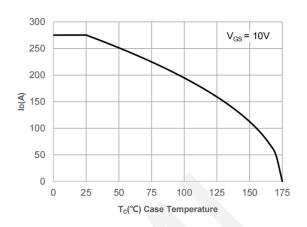
#### **Junction Temperature**



RDS(ON) vs. VGS

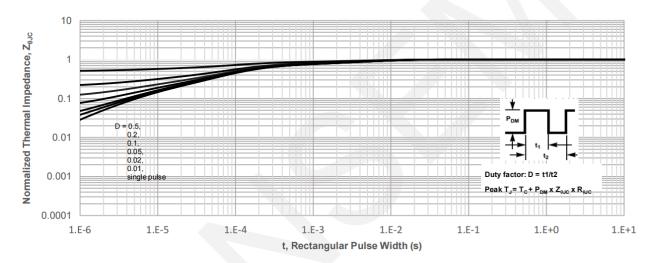






# **Power De-rating**

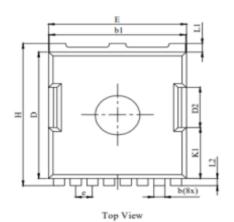
# **Current De-rating**

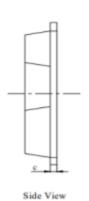


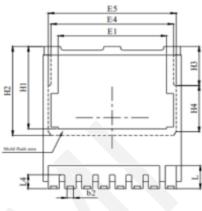
**Normalized Maximum Transient Thermal Impedance** 

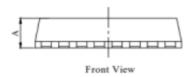


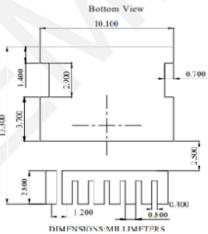
# 8. Package Dimensions











DIM.	MILLIMETER				
DIIVI.	MIN	NOM	MAX		
Α	2.20	2.30	2.50		
b	0.70	0.80	0.90		
bl	9.70	9.80	9.90		
b2	0.42	0.46	0.50		
C	0.40	0.50	0.65		
D	10.28	10.38	10.58		
D2		3.30			
E	9.70	9.70 9.90 10			
E1		7.80			
E4		8.80			
E5		9.20			
e		1.20(BSC)			
Н	11.48 11.68 11.8				
HI	6.55	6.75	6.85		
H2	7.30				
H3	3.20				
H4	3.80				
K1	4.18				
L	1.70	1.70 1.90 2.10			
L1	0.70				
L2	0.60				
L4	1.00	1.15	1.30		



#### 9. Important Notice

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