

Enhancement Mode P-Channel Power MOSFET

 $SOT23/PMOS/-40V/\pm 20V/-1.5V/-4A/100m\Omega$

Rev1.0





-40V,100mΩ, -4A, Single P-Channel

1.Features

- High power and current handing capability
- ◆ Lead free product is acquired
- Fast switching
- Surface mount package

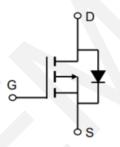
V _{DS}	R _{DS(on)} Typ.	Ι _D	
-40V	100mΩ @ -10V	4.0	
	125mΩ @ -4.5V	-4A	

2.Applications

- Power Switching Application
- Load Switching



SOT23 Pin Description



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	Package PCS/Reel	
WX02P04	2P04	SOT23	45,000	180,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter		Symbol	Maximum	Units
Drain to Source Voltage		V _{DSS}	-40	V
Gate to Source Voltage		V _{GSS}	±20	V
Dunin Comment (DC)	TA=25 °C	ID	-4	А
Drain Current (DC),	TA=70 °C	ID	-2.8	А
Drain Current (Pulse), PW≤300μs		Ідм	-16	А
Avalanche Energy, Single Pulsed		Eas	10.3	mJ
Total Dissipation TA=25 °C		P _D	0.6	W
Junction Temperature		Tj	150	°C
Storage Temperature		T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Maximum Junction-to-Ambient	Reja	192	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

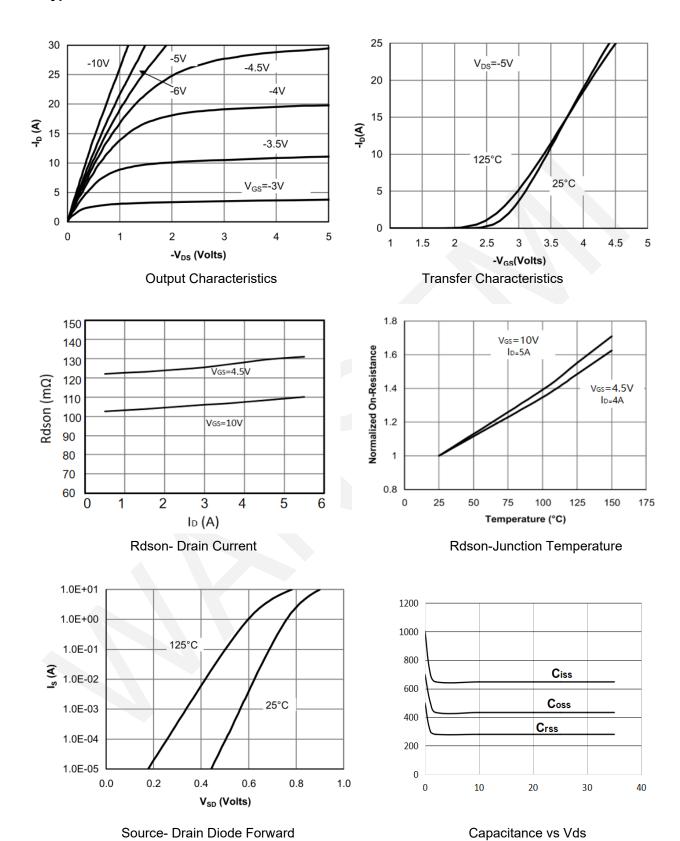
6.Electrical Characteristics at Ta=25°C (Note 3)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = -250 \mu A, V_{GS} = 0 V$	-40			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = -40V, V _{GS} = 0V			1	μΑ
Gate to Source Leakage Current	Igss	$V_{GS} = \pm 20V$, $V_{DS} = 0V$			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _{DS} =-250μA	-1.0	-1.5	-2.5	V
Static Drain to Source On-State	Б	I _D = -4A, V _{GS} = -10V		100	125	mΩ
Resistance	R _{DS(on)}	I _D = -4A, V _{GS} =-4.5V		125	150	mΩ
Input Capacitance	Ciss	V _{GS} =0V,		647	-	pF
Output Capacitance	Coss	V _{DS} =-15V,		433	-	pF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz		280	-	pF
Turn-ON Delay Time	t _{d(on)}	V _{DD} = -20V		6.5	-	ns
Rise Time	tr	$I_D = -5A$		14	-	ns
Turn-OFF Delay Time	t _{d(off)}	V _{GS} = -10V		34	-	ns
Fall Time	t _f	$R_{GEN} = 2.5\Omega$		18	-	ns
	Q_g	V _{DS} = -20V,		13.5		nC
Total Gate Charge	Qgs	V _{GS} = -10V,		1.5		nC
	Q _{gd}	I _D = -5A		1.8		nC
Diode Forward Voltage	V _{FSD}	I _S = -5A, V _{GS} = 0V		-0.9	-1.2	V

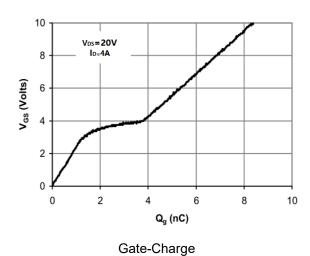
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different condit

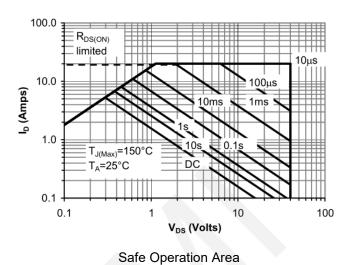


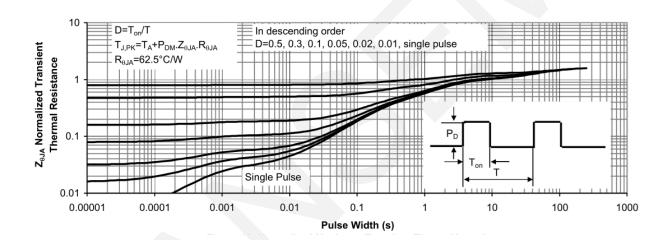
7. Typical electrical and thermal characteristics







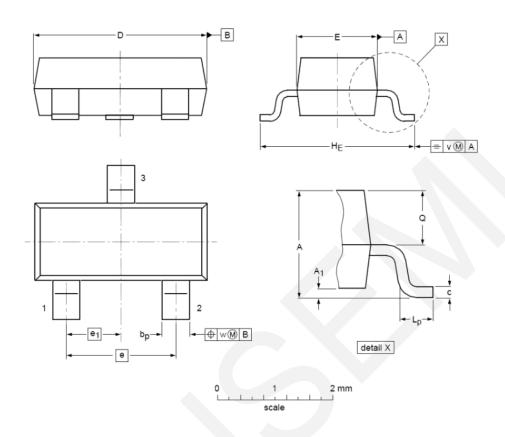




Normalized Maximum Transient Thermal Impedance



8.Package Dimensions



DIMENSIONS (unit : mm)

Symbol	Min	Тур	Max	Symbol	Min	Тур	Max
Α	0.90	1.01	1.15	A ₁	0.01	0.05	0.10
b _p	0.30	0.42	0.50	С	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
е		1.90		e ₁		0.95	
HE	2.25	2.40	2.55	L _p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v		0.20	
w	-	0.10					



9. Important Notice

WAN SEMICONDUCTOR (NINGBO) CO.,LTD reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services and to discontinue any product or service. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to WANSEMI's terms and conditions of sale supplied at the time of order acknowledgment.

WANSEMI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in WANSEMI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent WANSEMI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

WANSEMI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using WANSEMI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

No WANSEMI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Unless WANSEMI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use, WANSEMI will not be responsible for any failure of such components to meet such requirements.