

Enhancement Mode P-Channel Power MOSFET

PDFN3X3/PMOS/-30V/ \pm 20V/-1.65V/-10A/39m Ω

Rev_{0.6}





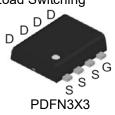
-30V, 39mΩ, -10A, P-Channel MOSFET

1.Features

- ◆ -30V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V
- ♦ 100% RG Tested
- ◆ 100% UIS Tested

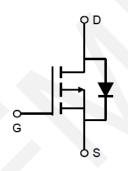
2.Applications

- Power Switching Application
- ◆ Load Switching



Pin Description

V _{DS}	R _{DS(on)} Typ.	I _D Max.	
-30V	39mΩ @ -10V	404	
	52mΩ @ -4.5V	-10A	



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX038P03P3	038P03	PDFN3X3	5,000	50,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	-30	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I _D	-10	Α
Drain Current (Pulse), PW≤300μs	I _{DP}	-40	А
Total Dissipation	P_{D}	36	W
Avalanche Energy, Single Pulsed	E _{AS}	25	mJ
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Junction to Case	R _{eJC}	1.8	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

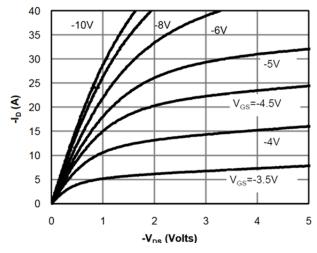
6.Electrical Characteristics at Ta=25°C (Note 3)

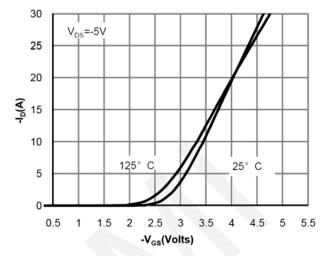
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A, V_{GS} = 0 V$	-30	-	-	٧
Zero-Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} = 0V	-	-	1	μΑ
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _{DS} =250μA	-1.0	-1.65	-2.2	٧
Static Drain to Source On-State	Б	I _D =-15A, V _{GS} = -10V	1	39	50	mΩ
Resistance	R _{DS(on)}	I _D = -10A, V _{GS} = -4.5V	-	52	70	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V,	-	513	-	pF
Output Capacitance	Coss	V _{DS} =-15V,	-	75	-	pF
Reverse Transfer Capacitance	C _{rss}	Frequency=1.0MHz	-	57	-	pF
Turn-ON Delay Time	t _{d(on)}	$V_{DS} = -15V, R_L = 3\Omega,$	ı	7.5	-	ns
Rise Time	t _r		-	5.5	-	ns
Turn-OFF Delay Time	$t_{\text{d(off)}}$	$V_{GS} = -10V, R_G = 3\Omega$	-	19	-	ns
Fall Time	t _f		-	7	-	ns
	Q_g	V _{DS} =-15V,	-	9.2	-	nC
Total Gate Charge	Q_{gs}	V _{GS} = -10V,	-	1.6	-	nC
	Q_{gd}	I _D =-15A	-	2.2	-	nC
Diode Forward Voltage	V_{FSD}	I _S =-15A, V _{GS} = 0V	-	-	-1.2	٧

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



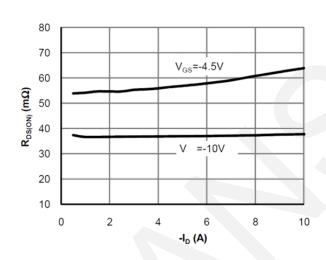
7. Typical electrical and thermal characteristics

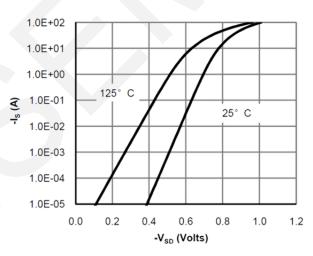




Output Characteristics

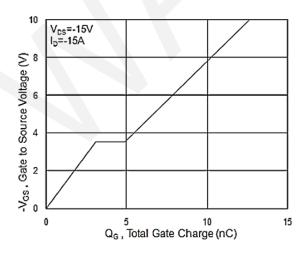


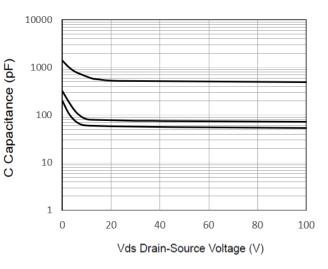




On-resistance vs. Drain Current

Body Diode Characteristics

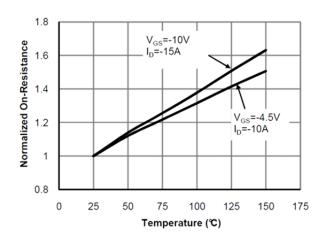


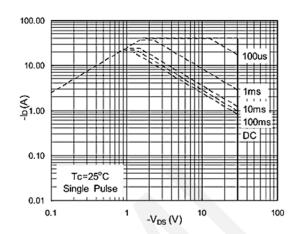


Gate Charge Characteristics

Capacitance Characteristics

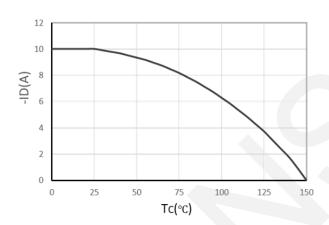






Normalized on Resistance vs.

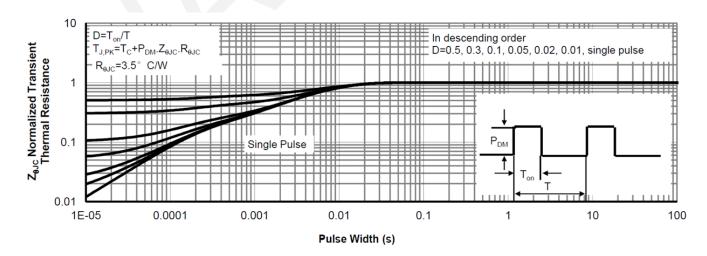
Junction Temperature



Maximum Safe Operating Area

Maximum Continuous Drain Current vs.

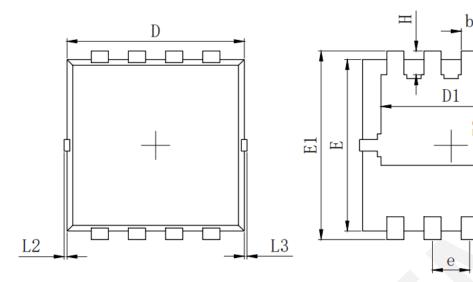
Case Temperature

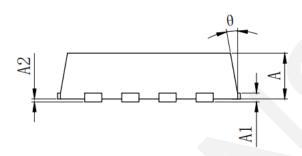


Maximum Effective Transient Thermal Impedance, Junction-to-Case



8.Package Dimensions





SYMBOL		MILLIMETER	
SIMBOL	MIN	Тур.	MAX
A	0. 700	0.800	0.900
A1		0. 152 REF.	
A2	0~0. 05		
D	3.000	3. 100	3. 200
D1	2. 300	2. 450	2.600
Е	2. 900	3.000	3. 100
E1	3. 150	3. 300	3. 450
E2	1. 320	1. 520	1.720
b	0. 200	0. 300	0.400
е	0. 550	0.650	0.750
L	0. 300	0.400	0.500
L1	0. 180	0. 330	0.480
L2	0~0. 100		
L3	0~0. 100		
Н	0. 315	0. 415	0. 515
θ	8°	10°	12°



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