

# **Enhancement Mode N-Channel Power MOSFET**

DFN3X3/NMOS/20V/ $\pm$ 12V/0.75V/9A/6.6m $\Omega$ 

Rev1.0





# 20V, 6.6mΩ, 9A, N-Channel MOSFET

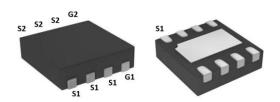
#### 1.Features

- High Power and current handing capability
- Lead free product is acquired
- ◆ Surface Mount Package

V <sub>DS</sub> Typ.	R <sub>DS(on)</sub> Typ.	I <sub>D</sub> Max.
201/	6.6mΩ @ 4.5V	0.4
20V	10mΩ @ 2.5V	9A

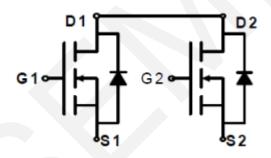
## 2.Applications

- Battery Protection
- Battery Powered Systems
- Power Management in Notebook Computer
- ◆ Portable Equipment



Pin Description

DFN3X3-8L



Schematic Diagram

## 3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX075N02DP	075N02	DFN3x3	5,000	50,000

#### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V <sub>DSS</sub>	20	V
Gate to Source Voltage	$V_{GSS}$	±12	V
Drain Current-Continuous	I <sub>D</sub>	9	Α
Drain Current (Pulse)	I <sub>DM</sub>	36	Α
Maximum Power Dissipation	P <sub>D</sub>	1.5	W
Operating Junction and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



## 5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Maximum Junction-to-Ambien	$R_{ hetaJA}$	64	°C/W

Note 2: When mounted on 1 inch square copper board  $t \le 10$ sec The value in any given application depends on the user's specific board design.

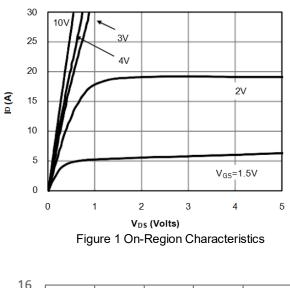
#### 6.Electrical Characteristics at Ta=25°C (Note 3)

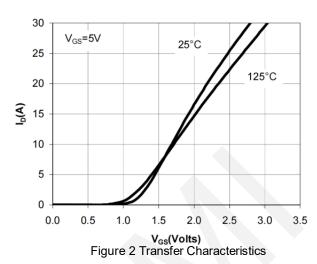
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250 \mu A, V_{GS} = 0 V$	20	-	-	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V	-	1	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 10V, V_{DS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{DS}=250\mu A$	0.5	0.75	1.2	V
Drain to Source On-State		$I_D = 8A, V_{GS} = 4.5V$		6.6	8.0	mΩ
Resistance	$R_{DS(on)}$	$I_D = 6A, V_{GS} = 2.5V$		10	17	mΩ
Input Capacitance	$C_{iss}$	V <sub>GS</sub> =0V,	-	290	-	pF
Output Capacitance	$C_{oss}$	V <sub>DS</sub> =10V,	-	120	-	pF
Reverse Transfer Capacitance	$C_{rss}$	Frequency=1.0MHz	1	40	-	pF
Turn-ON Delay Time	t <sub>d(on)</sub>		ı	280	-	ns
Turn-ON Rise Time	t <sub>r</sub>	$V_{DS} = 10V, V_{GS} = 5V,$	-	972	-	ns
Turn-OFF Delay Time	$t_{\text{d(off)}}$	$R_{GEN} = 3\Omega, R_L = 1.7\Omega$	-	2.4	-	ns
Turn-ON Fall Time	t <sub>f</sub>		-	2.2	-	ns
Total Gate Charge	$Q_g$	V <sub>DS</sub> = 10V,	-	5.2	-	nC
Gate-Source Charge	$Q_{gs}$	$V_{GS} = 4.5V$ ,	-	2	-	nC
Gate-Drain Charge	$Q_{gd}$	I <sub>D</sub> = 6A	-	1.9	-	nC
Diode Forward Voltage	$V_{\text{SD}}$	I <sub>S</sub> = 6A, V <sub>GS</sub> = 0V	1	0.8	1.2	٧

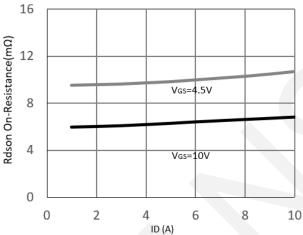
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



# 7. Typical electrical and thermal characteristics







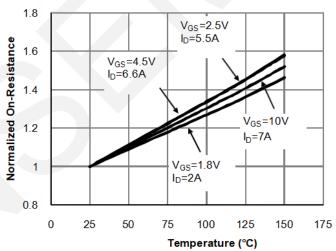
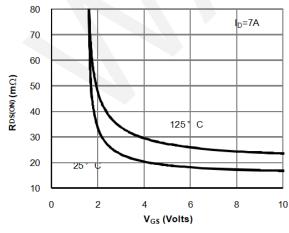


Figure 3 On-Resistance vs. Drain

Current and Gate Voltage

Figure 4 On-Resistance vs. Junction Temperature



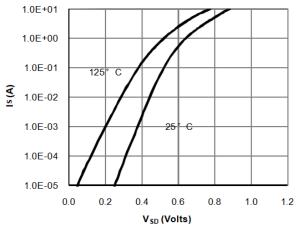
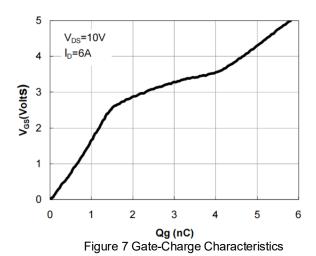
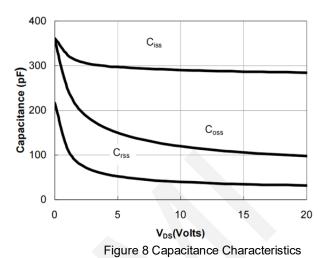


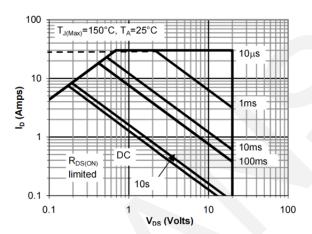
Figure 5 On-Resistance vs. Gate-Source Voltage

Figure 6 Body-Diode Characteristics









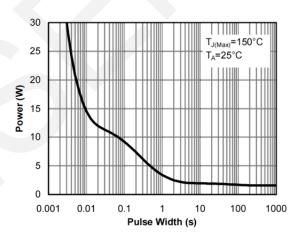


Figure 9 Safe Operating Area

Figure 10 Single Pulse Power Rating Junction-to- Ambient

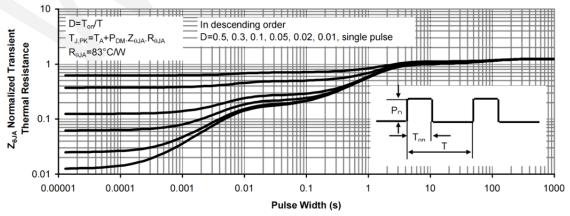
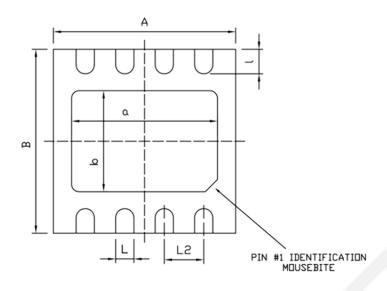
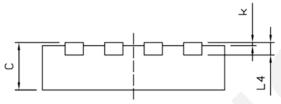


Figure 11 Maximum Transient Thermal Impedence



# 8.Package Dimensions





Dimensions In Millimeterer					
Symbol	MIN	MAX			
Α	2.95	3.00	3.05		
В	2.95	3.00	3.05		
С	0.70	0.75	0.80		
L	0.25	0.30	0.35		
ı	0.324	0.40	0.476		
L2	_	0.65	_		
L4	-	0.20	-		
۵	2.20	2.30	2.40		
b	1.40	1,50	1.60		
k	0.00	_	0.05		



#### 9.Important Notice

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