

# **Enhancement Mode N-Channel Power MOSFET**

PDFN3X3/NMOS/30V/ $\pm$ 20V/1.8V/120A/2.5m $\Omega$ 

Rev<sub>0.1</sub>





# 30V, 2.5mΩ, 120A, N-Channel MOSFET

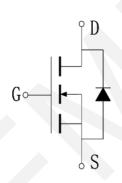
#### 1.Features

- ◆ 30V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V
- ◆ 100% RG Tested
- ◆ 100% UIS Tested

- Power Switching Application
- ◆ Load Switching



V <sub>DS</sub>	R <sub>DS(on)</sub> Typ.	I <sub>D</sub> Max.	
00)/	2.5mΩ @ 10V	4004	
30V	3.5mΩ @ 4.5V	120A	



Schematic Diagram

## 3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX025N03P3	025N03	PDFN3X3	5,000	50,000

#### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	$V_{ extsf{DSS}}$	30	V
Gate to Source Voltage	$V_{GSS}$	±20	٧
Drain Current (DC)	I <sub>D</sub>	120	А
Drain Current (Pulse), PW≤300μs	I <sub>DP</sub>	480	А
Total Dissipation	$P_{D}$	48	W
Avalanche Energy, Single Pulsed	E <sub>AS</sub>	110.25	mJ
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



## 5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	32	°C/W

Note 2: When mounted on 1 inch square copper board  $t \le 10$ sec The value in any given application depends on the user's specific board design.

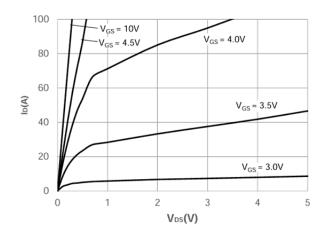
#### 6.Electrical Characteristics at Ta=25°C (Note 3)

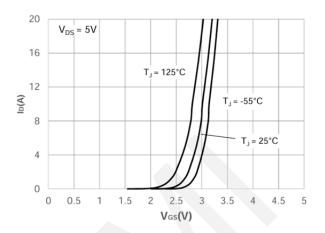
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$I_D = 250 \mu A, V_{GS} = 0 V$	30	-	-	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =30V, $V_{GS}$ = 0V	-	-	1	μA
Gate to Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	1.0	1.8	2.5	V
Static Drain to Source On-State	D	I <sub>D</sub> =30A, V <sub>GS</sub> = 10V		2.5	3.0	mΩ
Resistance	R <sub>DS(on)</sub>	$I_D = 20A, V_{GS} = 4.5V$	-	3.0	4.5	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V,	-	3089	-	pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =15V,	-	372	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	Frequency=1.0MHz	1	302	-	pF
Turn-ON Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 15V,	ı	11	-	ns
Rise Time	t <sub>r</sub>	$I_D = 30A$ ,	-	29	-	ns
Turn-OFF Delay Time	$t_{\text{d(off)}}$	V <sub>GS</sub> = 10V,	-	47	-	ns
Fall Time	t <sub>f</sub>	$R_{GEN} = 3\Omega$	-	18	-	ns
	$Q_g$	V <sub>DS</sub> = 15V,	-	58	-	nC
Total Gate Charge	$Q_{gs}$	V <sub>GS</sub> =0 to 10V,	-	12	-	nC
	$Q_{gd}$	I <sub>D</sub> = 30A	-	13	-	nC
Diode Forward Voltage	$V_{FSD}$	I <sub>S</sub> = 30A, V <sub>GS</sub> = 0V	0.5	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

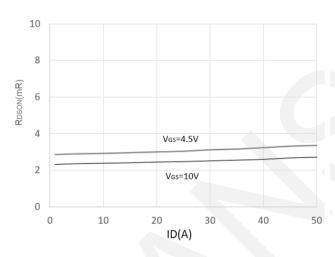


## 7. Typical electrical and thermal characteristics

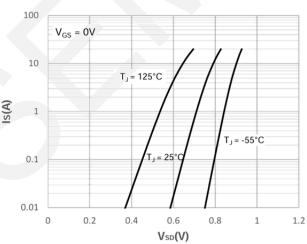




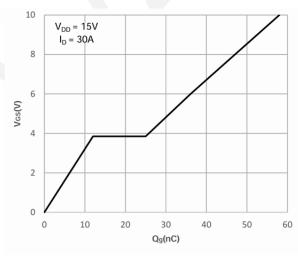
**Output Characteristics** 



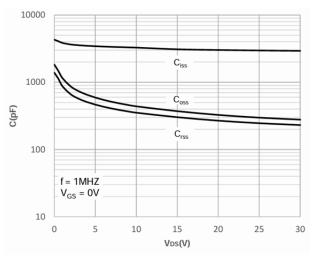
Typical Transfer Characteristics



On-resistance vs. Drain Current



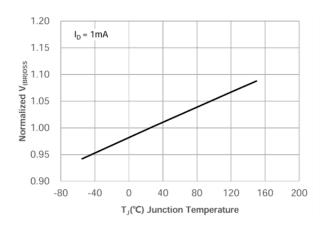
**Body Diode Characteristics** 

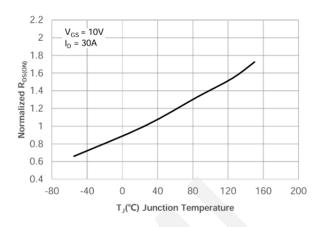


Gate Charge Characteristics

Capacitance Characteristics

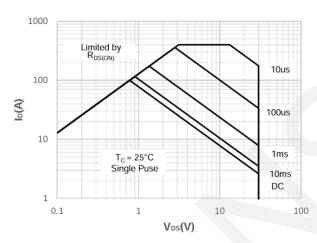






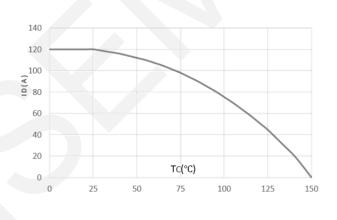
# Normalized Breakdown Voltage vs.

# **Junction Temperature**

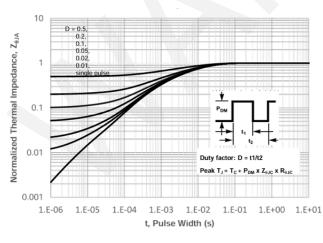


Normalized on Resistance vs.

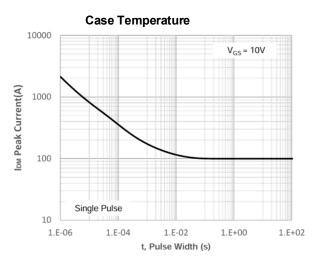
Junction Temperature



## **Maximum Safe Operating Area**



**Maximum Continuous Drain Current vs.** 



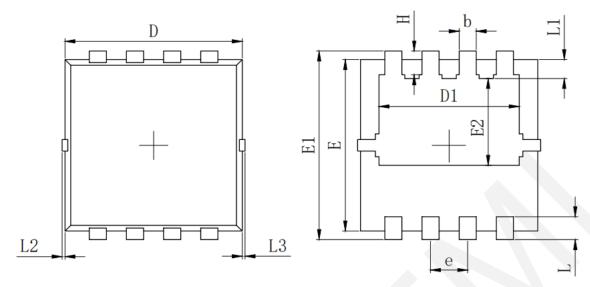
#### **Normalized Maximum Transient**

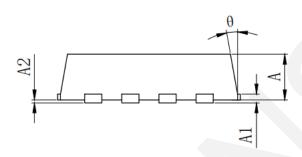
Thermal Impedance

**Peak Current Capacity** 



# 8.Package Dimensions





SYMBOL	1	MILLIMETER	
SIMBOL	MIN	Тур.	MAX
A	0. 700	0.800	0.900
A1		0. 152 REF.	
A2		0~0.05	
D	3.000	3. 100	3. 200
D1	2. 300	2. 450	2.600
Е	2. 900	3.000	3. 100
E1	3. 150	3. 300	3. 450
E2	1. 320	1.520	1.720
b	0. 200	0.300	0.400
е	0. 550	0.650	0.750
L	0. 300	0.400	0.500
L1	0. 180	0. 330	0.480
L2	0~0. 100		
L3	0~0. 100		
Н	0. 315	0. 415	0. 515
θ	8°	10°	12°



#### 9. Important Notice

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