

Enhancement Mode N-Channel Power MOSFET

PDFN3X3/NMOS/20V/ \pm 12V/0.75V/30A/5.5m Ω

Rev_{0.6}



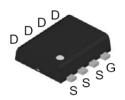


20V, 5.5mΩ, 30A, Single N-Channel

1.Features

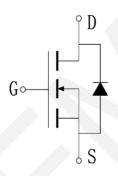
- ◆ 20V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±12V

- ◆ Power Switching Application
- Load Switching



PDFN3X3 Pin Description

V_{DS}	R _{DS(on)} Typ.	I _D Max.	
20V	5.5mΩ @ 4.5V	20.4	
	7.5mΩ @ 2.5V	30A	



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP2030KP3	WP2030KP3	PDFN3x3	5,000	50,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	$V_{ extsf{DSS}}$	20	V
Gate to Source Voltage	V _{GSS}	±12	V
Drain Current (DC)	I _D	30	А
Drain Current (Pulse), PW≤300μs	I _{DP}	120	А
Total Dissipation	P _D	34	W
Avalanche Energy, Single Pulsed	E _{AS}	64	mJ
Junction Temperature	Tj	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5.Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to case	$R_{ heta JC}$	1.8	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

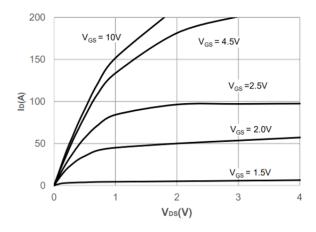
6.Electrical Characteristics at Ta=25°C (Note 3)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250 \mu A, V_{GS} = 0 V$	20	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20V, V_{GS} = 0V$	-	ı	100	nA
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{DS}=250\mu A$	0.5	0.75	1.2	V
Static Drain to Source On-State	В	$I_D = 20A, V_{GS} = 4.5V$	ı	5.5	7.5	mΩ
Resistance	R _{DS(on)}	$I_D = 10A, V_{GS} = 2.5V$	1	7.5	10	mΩ
Forward Transconductance	G_{FS}	$I_D = 20A, V_{DS} = 5V$	20	-	-	S
Input Capacitance	C _{iss}	V _{GS} =0V,	-	1703	-	pF
Output Capacitance	C _{oss}	V _{DS} =10V,	-	210	-	pF
Reverse Transfer Capacitance	C _{rss}	Frequency=1.0MHz	-	188	-	pF
Turn-ON Delay Time	t _{d(on)}		-	12	-	ns
Rise Time	t _r	$V_{DD} = 10V, I_D = 20A,$	-	33	-	ns
Turn-OFF Delay Time	$t_{d(off)}$	$V_{GS} = 4.5V,$ $R_G = 3\Omega,$	-	48	-	ns
Fall Time	t _f	, ,	-	95	-	ns
	Q_g	$V_{DS} = 10V,$ $V_{GS} = 0 \text{ to } 4.5V,$	-	23	-	nC
Total Gate Charge	Q_{gs}		-	4	-	nC
	Q_{gd}	I _D = 20A	-	7	-	nC
Diode Forward Voltage	V _{FSD}	I _S = 25A, V _{GS} = 0V	0.5	0.8	1.2	V

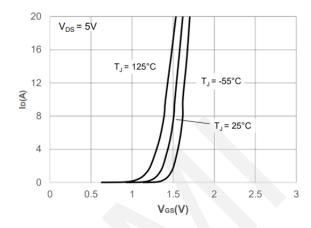
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



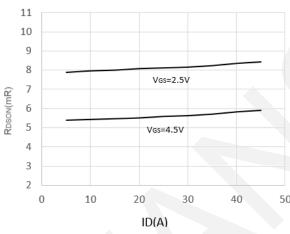
7. Typical electrical and thermal characteristics



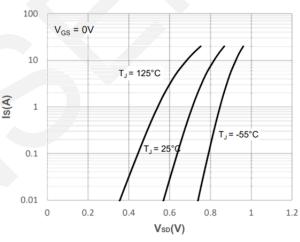




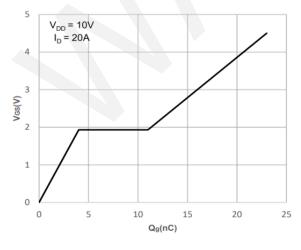
Typical Transfer Characteristics



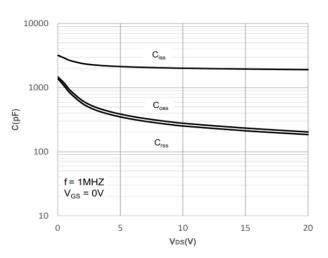
On-resistance vs. Drain Current



Body Diode Characteristics

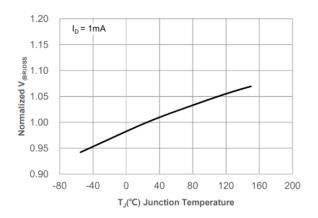


Gate Charge Characteristics



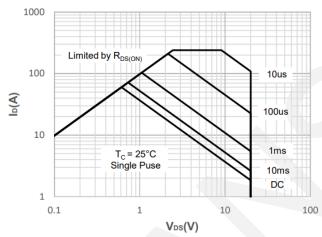
Capacitance Characteristics



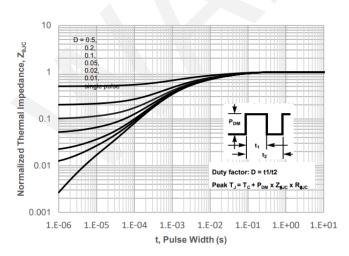


Normalized Breakdown Voltage vs.

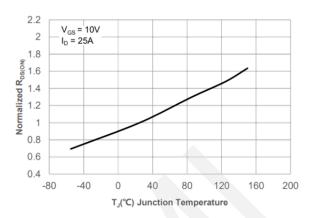
Junction Temperature



Maximum Safe Operating Area

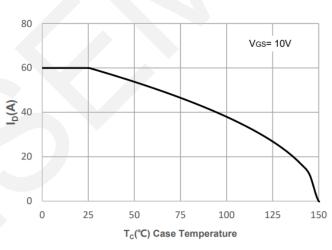


Normalized Maximum Transient
Thermal Impedance

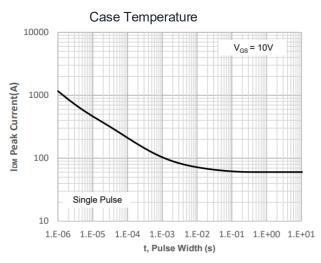


Normalized on Resistance vs.

Junction Temperature



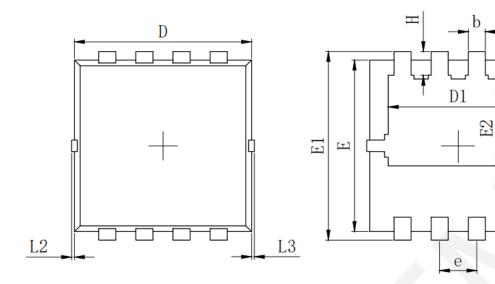
Maximum Continuous Drain Current vs.

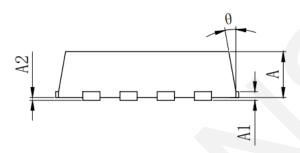


Peak Current Capacity



8.Package Dimensions





SYMBOL		MILLIMETER			
SIMDUL	MIN	Тур.	MAX		
A	0. 700	0.800	0.900		
A1		0. 152 REF.			
A2		0~0.05			
D	3.000	3,000 3,100 3,20			
D1	2. 300	2. 450	2.600		
Е	2. 900	3.000	3. 100		
E1	3. 150	3. 300	3. 450		
E2	1. 320	1.520	1.720		
b	0. 200	0.300	0.400		
е	0. 550	0.650	0.750		
L	0. 300	0.400	0.500		
L1	0. 180	0. 330	0.480		
L2	0~0. 100				
L3	0~0. 100				
Н	0. 315	0. 415	0. 515		
θ	8°	10°	12°		



9. Important Notice

WAN SEMICONDUCTOR (NINGBO) CO.,LTD reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services and to discontinue any product or service. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to WANSEMI's terms and conditions of sale supplied at the time of order acknowledgment.

WANSEMI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in WANSEMI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent WANSEMI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

WANSEMI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using WANSEMI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

No WANSEMI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Unless WANSEMI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use, WANSEMI will not be responsible for any failure of such components to meet such requirements.