

# N-channel Enhancement Mode Power MOSFET

 $TOLL/100V/\pm20V/3.0V/467A/0.98m\Omega$ 

Rev1.2





### 100V, 0.98 mΩ, 467A, N-channel MOSFET

#### 1.Features

- Excellent R DS(ON) and Low Gate Charge
- 100% UIS Tested
- 100% ΔVds Tested
- 100% DVDS Tested
- 100% Rg Tested
- Halogen-free; RoHS-compliant

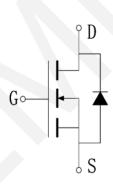
2.A	pp	licati	ons
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- Load Switch
- **PWM Application**
- **Power Management**



#### **TOLL** Pin Description 3. Package Marking and Ordering Information

$V_{DS}$	$R_{DS(on)}$ Typ.	I <sub>D</sub> Max.
100V	0.98mΩ @10V	467A



Schematic Diagram

Part no.	Package	Marking	PCS/Reel	PCS/CTN.
WX012N10LL	TOLL	012N10	2,000	16,000

#### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter		Symbol	Maximum	Units	
Drain to Source Voltage		$V_{DSS}$	100	V	
Gate to Source Voltage		V <sub>GSS</sub>	±20	V	
Durain Commant (DC)	T C = 25°C	I <sub>D</sub>	467	Α	
Drain Current (DC)	T C = 100°C	I <sub>D</sub>	295	Α	
Drain Current (Pulse), PW≤300μs		I <sub>DM</sub>	1868	Α	
Avalanche Energy, Single Pulsed		E <sub>AS</sub>	1987	mJ	
Total Dissipation	T C = 25°C	$P_{D}$	500	W	
Total Dissipation	T C = 100°C	$P_{D}$	200	W	
Junction Temperature		$T_{j}$	150	°C	
Storage Temperature		$T_{stg}$	-55 to +150	°C	

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



#### 5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit	
Junction to Ambient	$R_{ hetaJA}$	33	°C/W	
Junction to case	R <sub>eJC</sub>	0.35	°C/W	

Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

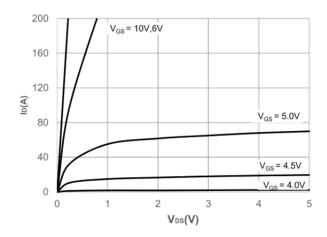
#### 6.Electrical Characteristics at Ta=25°C (Note 2)

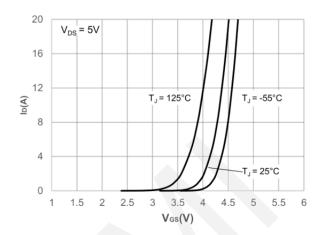
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$I_D = 250 \mu A, V_{GS} = 0 V$	100	-	-	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 90V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate to Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	2.0	3.0	4.0	V
Static Drain to Source On-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	0.98	1.2	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V,	9543	13360	18036	pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =50V,	3652	5113	6902	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	Frequency=1.0MHz	87	122	164	pF
Turn-on Delay Time	t <sub>d(on)</sub>		-	41	-	ns
Rise Time	t <sub>r</sub>	$V_{DS} = 50V, I_{D} = 20A$	-	69	-	ns
Turn-off Delay Time	$t_{d(off)}$	$V_{GS} = 10V,$ $R_{GEN} = 3\Omega$	-	157	-	ns
Fall Time	t <sub>f</sub>	SEN -	-	92	-	ns
	$Q_g$	V <sub>DS</sub> = 50V,	155	217	293	nC
Total Gate Charge	Q <sub>gs</sub>	$V_{GS} = 10V$ ,	47	65	88	nC
	$Q_{gd}$	I <sub>D</sub> =20A	41	57	77	nC
Diode Forward Voltage	$V_{FSD}$	I <sub>S</sub> = 20A, V <sub>GS</sub> = 0	0.4	0.85	1.2	V

Note 2: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



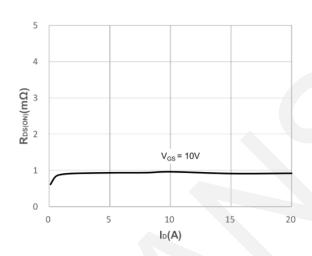
#### 7. Typical electrical and thermal characteristics

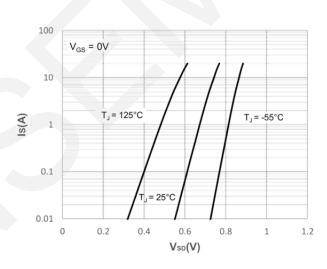




#### **Saturation Characteristics**

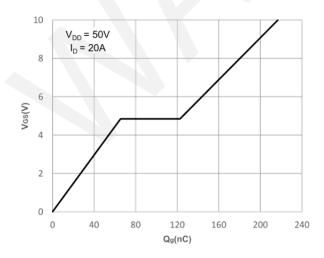
**Transfer Characteristics** 

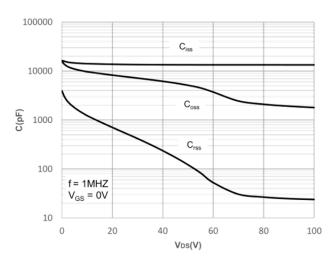




#### R<sub>DS(on)</sub>vs.Drain Current

**Body-Diode Characteristics** 

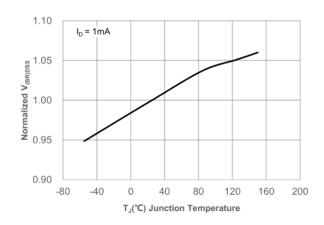


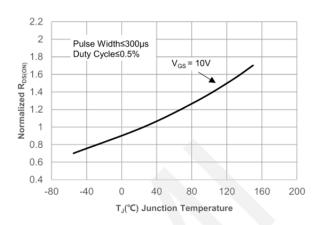


**Gate Charge Characteristics** 

**Capacitance Characteristics** 





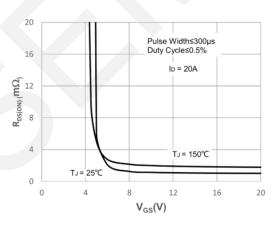


# Normalized Breakdown voltage vs. Junction Temperature

1.4  $V_{DS} = V_{GS}$  $I_D = 250uA$ 1.2 Normalized V<sub>th</sub> 1 0.8 0.6 0.4 -80 -40 40 80 120 160 200 T<sub>J</sub>(°C) Junction Temperature

Normalized on Resistance vs.

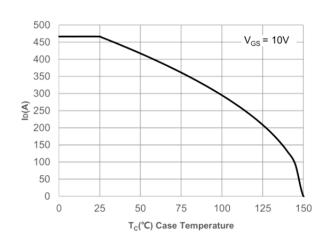
Junction Temperature



## Normalized Threshold Voltage vs.

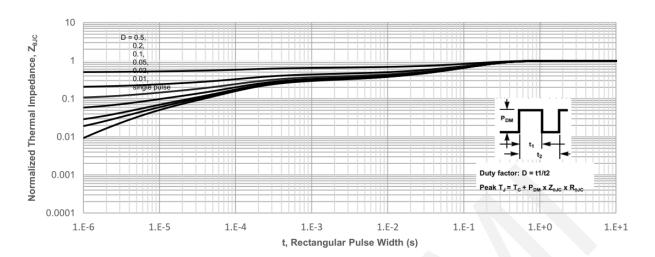


R DS(ON) vs. V GS

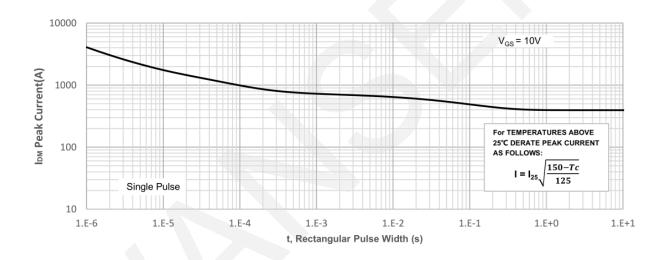


**Current De-rating** 





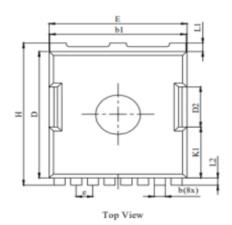
#### **Normalized Maximum Transient Thermal Impedance**

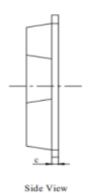


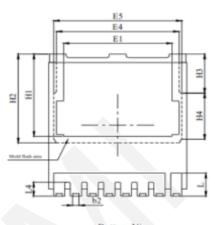
**Peak Current Capacity** 

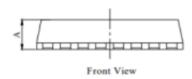


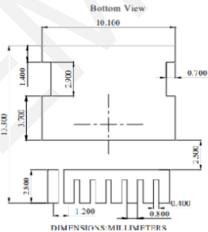
#### 8.Package Dimensions











DIM.	MILLIMETER				
DIIVI.	MIN	NOM	MAX		
Α	2.20	2.30	2.50		
b	0.70	0.80	0.90		
bl	9.70	9.80	9.90		
b2	0.42	0.46	0.50		
С	0.40	0.50	0.65		
D	10.28	10.38	10.58		
D2		3.30			
E	9.70	9.90	10.10		
E1		7.80			
E4		8.80			
E5	9.20				
e		1.20(BSC)			
Н	11.48	11.68	11.88		
HI	6.55	6.75	6.85		
H2	7.30				
Н3	3.20				
H4	3.80				
K1	4.18				
L	1.70	1.90	2.10		
L1	0.70				
L2	0.60				
L4	1.00 1.15 1.30				



#### 9. Important Notice

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