

Enhancement Mode Dual N-Channel Power MOSFET

PDFN3x3/NMOS/30V/ \pm 20V/1.8V/22A/10m Ω

Rev0.6





30V, 10mΩ, 22A, Dual N-Channel

1.Features

- ◆ 30V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V

V _{DS}	R _{DS(on)} Typ.	I _D Max.	
30V	10mΩ @ 10V	004	
	14.5mΩ @ 4.5V	22A	

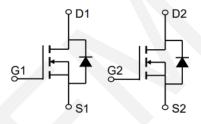
2.Applications

- ◆ Power Switching Application
- ◆ Load Switching





PDFN3x3
Pin Description



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP3040DP3	WP3040DP3	PDFN3x3	5,000	50,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	$V_{ extsf{DSS}}$	30	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I _D	22	А
Drain Current (Pulse), PW≤300μs	I _{DP}	88	А
Total Dissipation	P_{D}	34	W
Avalanche Energy, Single Pulsed	E _{AS}	30	mJ
Junction Temperature	Tj	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Junction to case	R _{eJC}	3.7	°C/W
Junction to Ambient	$R_{ hetaJA}$	59	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

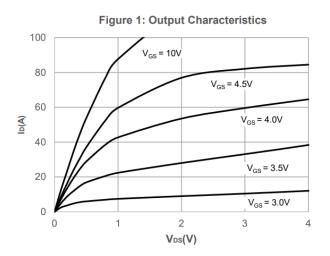
6.Electrical Characteristics at Ta=25°C (Note 3)

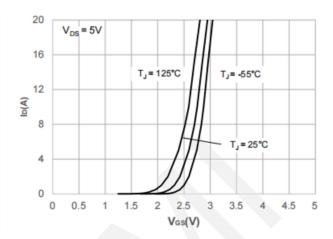
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A, V_{GS} = 0 V$	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30V, V_{GS} = 0V$			1.0	μA
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{DS}=250\mu A$	1.0	1.8	2.5	V
Static Drain to Source On-State	Б	I _D = 20A, V _{GS} = 10V		10	13	mΩ
Resistance	R _{DS(on)}	$I_D = 20A, V_{GS} = 4.5V$		14.5	17	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V,		1002		pF
Output Capacitance	C _{oss}	V _{DS} =15V,		131		pF
Reverse Transfer Capacitance	C_{rss}	Frequency=1.0MHz		105		pF
Turn-ON Delay Time	t _{d(on)}			6		ns
Rise Time	t _r	V _{DD} = 15V, I _{DS} = 30A,		9		ns
Turn-OFF Delay Time	$t_{d(off)}$	$V_{GS} = 10V, R_G = 3\Omega$		22		ns
Fall Time	t _f			5		ns
	Q_g	Q _g V _{DS} = 15V,		20		nC
Total Gate Charge	Q_{gs}	$V_{GS} = 0 \text{ to } 10V,$		4		nC
	Q_{gd}	I _{DS} = 30A		5		nC
Diode Forward Voltage	V _{FSD}	I _S = 20A, V _{GS} = 0V		0.85	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

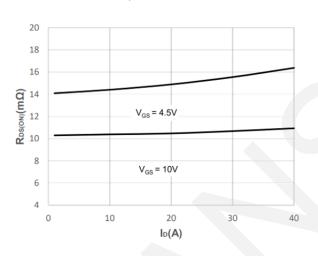


7. Typical electrical and thermal characteristics

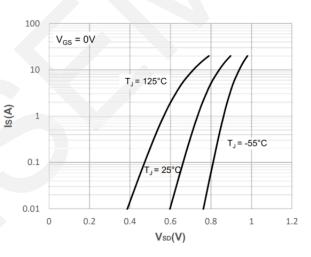




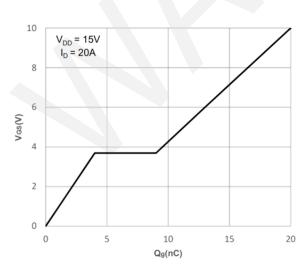
Output Characteristics



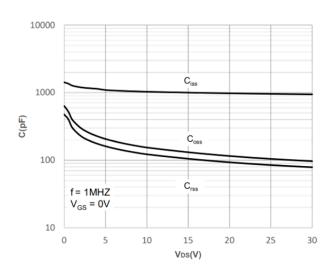
Typical Transfer Characteristics



On-resistance vs. Drain Current



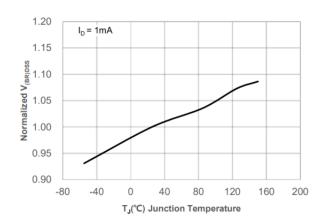
Body Diode Characteristics



Gate Charge Characteristics

Capacitance Characteristics



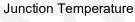


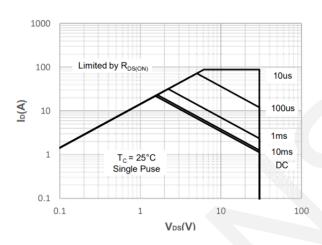
2.2 V_{GS} = 10V I_D = 15A 2 1.8 Normalized R_{DS(ON)} 1.6 1.4 1.2 8.0 0.6 0.4 -80 -40 40 80 120 160 200 T_J(°C) Junction Temperature

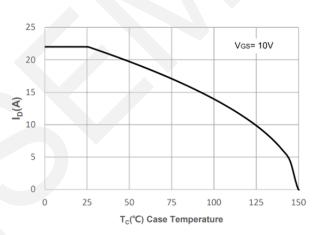
Normalized Breakdown Voltage vs.

Normalized on Resistance vs.



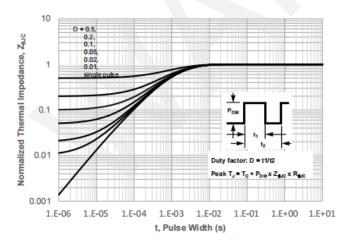


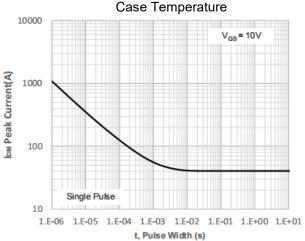




Maximum Safe Operating Area

Maximum Continuous Drain Current vs.



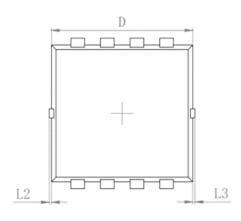


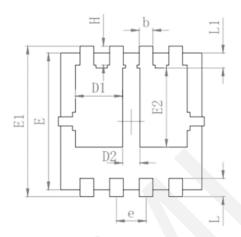
Normalized Maximum Transient Thermal Impedance

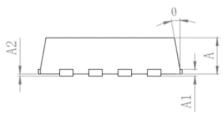
Peak Current Capacity



8.Package Dimensions







SYMBOL	MILLI	METER	
	MIN	MAX	
A	0.700	0. 900	
A1	0.152 REF.		
A2	0~0.05		
D	3.000	3. 200	
D1	0.935	1. 135	
D2	0.280	0. 480	
Е	2.900	3. 100	
E1	3.150	3. 450	
E2	1.535	1. 935	
b	0.200	0. 400	
e	0.550	0. 750	
L	0.300	0. 500	
L1	0.180	0. 480	
L2	0~0.100		
L3	0~0.100		
Н	0.315	0. 515	
θ	8°	12°	



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