

Enhancement Mode N-Channel Power MOSFET

 $TO-252/NMOS/100V/\pm20V/1.5V/35A/22m\Omega$

Rev0.6





100V, 22mΩ, 35A, Single N-Channel

1.Features

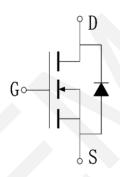
- ◆ 100V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V
- ◆ 100% UIS Tested
- ◆ 100% RG Tested

- Power Switching Application
- ◆ Load Switching



Pin Description

V _{DS}	R _{DS(on)} Typ.	I _D Max.
100V	22mΩ @ 10V	054
	23.5mΩ @ 4.5V	35A



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.	
WX024N10KD	024N10	TO-252	2,500	25,000	

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	$V_{ extsf{DSS}}$	100	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I_D	35	А
Drain Current (Pulse), PW≤300μs	I _{DP}	140	А
Total Dissipation	P_{D}	104.2	W
Avalanche Energy, Single Pulsed	E _{AS}	121	mJ
Junction Temperature	T _j	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Junction to case	R _{eJC}	1.2	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

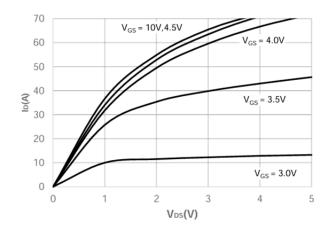
6.Electrical Characteristics at Ta=25°C (Note 3)

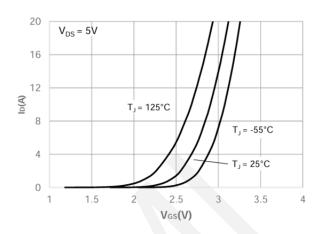
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A, V_{GS} = 0 V$	100	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA
Gate to Source Leakage Current	I _{GSS1}	$V_{GS} = \pm 20V, V_{SS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _{DS} =250μA	1.0	1.5	2.5	V
Static Drain to Source On-State		$I_D = 20A, V_{GS} = 10V$		22	28	mΩ
Resistance	R _{DS(on)}	$I_D = 10A, V_{GS} = 4.5V$	-	23.5	33	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V,	-	2810	-	pF
Output Capacitance	C _{oss}	V _{DS} =40V, Frequency=1.0MHz	-	96	-	pF
Reverse Transfer Capacitance	C _{rss}		1	85	-	pF
Turn-ON Delay Time	t _{d(on)}	V _{DD} = 50V, I _D = 20A, V _{GS} = 10V,	ı	10	-	ns
Rise Time	t _r		-	24	-	ns
Turn-OFF Delay Time	t _{d(off)}		-	63	-	ns
Fall Time	t _f	$R_{GEN} = 3\Omega$	-	45	-	ns
	Q_g	V _{DS} = 50V, V _{GS} =0 to 10V,	-	72	-	nC
Total Gate Charge	Q _{gs}		-	11	-	nC
	Q_{gd}	I _D = 20A	-	19	-	nC
Diode Forward Voltage	V_{FSD}	I _S = 20A, V _{GS} = 0V	0.5	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

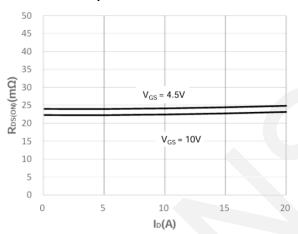


7. Typical electrical and thermal characteristics

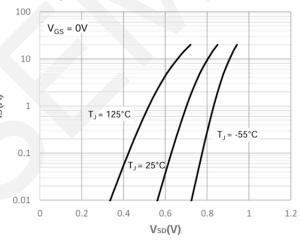




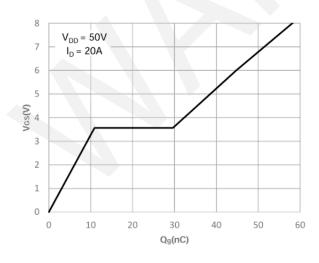
Output Characteristics



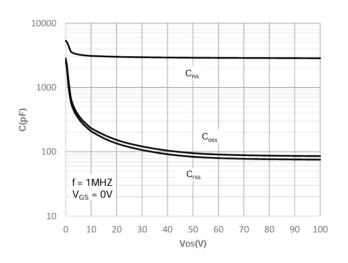
Typical Transfer Characteristics



On-resistance vs. Drain Current



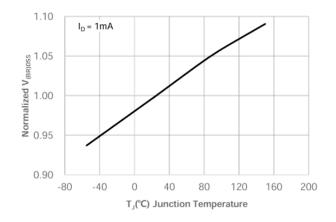
Body Diode Characteristics

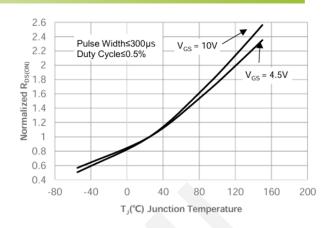


Gate Charge Characteristics

Capacitance Characteristics

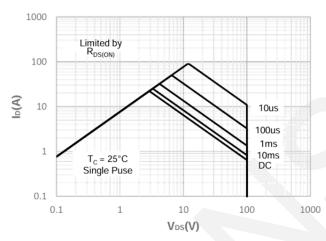






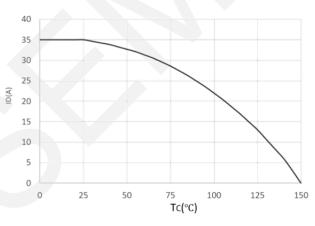
Normalized Breakdown voltage vs.

Junction Temperature



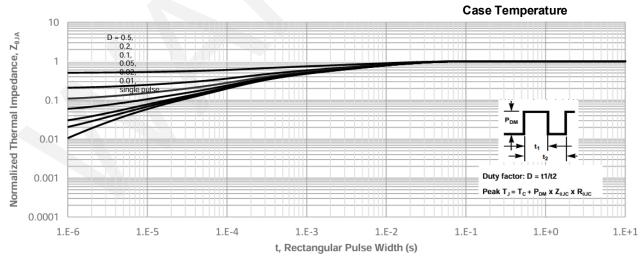
Normalized on Resistance vs.

Junction Temperature



Maximum Safe Operating Area

Maximum Continuous Drain Current vs.

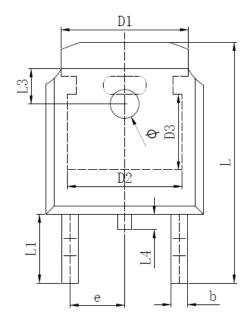


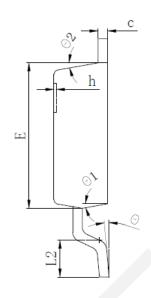
Normalized Maximum Transient

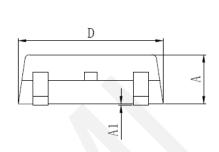
Thermal Impedance



8.Package Dimensions







SYMBOL	MILLIMETER			
SIMDUL	MIN	Тур.	MAX	
A	2. 200	2.300	2. 400	
A1	0.000		0. 127	
b	0. 640	0.690	0.740	
c(电镀后)	0. 460	0.520	0. 580	
D	6. 500	6. 600	6. 700	
D1	5.334 REF			
D2	4.826 REF			
D3	3. 166 REF			
E	6. 000	6. 100	6. 200	
е	2.286 TYP			
h	0.000	0.100	0. 200	
L	9. 900	10. 100	10. 300	
L1	2.888 REF			
L2	1. 400	1.550	1. 700	
L3	1.600 REF			
L4	0.600	0.800	1. 000	
ф	1. 100	1. 200	1. 300	
θ	0°		8°	
θ1	9° TYP			
θ2	9° TYP			



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