

Enhancement Mode N-Channel Power MOSFET

 $TO-263/NMOS/70V/\pm 20V/3.0V/51A/6.1m\Omega$

Rev_{0.1}





70V, 6.1mΩ, 51A, N-Channel Enhancement MOSFET

1.Features

- ◆ 70V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V
- ◆ 100% RG Tested
- ◆ 100% UIS Tested

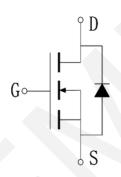
2.Applications

- Power Switching Application
- ◆ Load Switching



Pin Description

V _{DS}	R _{DS(on)} Typ.	I _D Max.
70V	6.1mΩ @ 10V	51A



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX060N07KF	060N07	TO-263	800	4,800

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	$V_{ extsf{DSS}}$	70	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I _D	51	А
Drain Current (Pulse), PW≤300μs	I _{DP}	204	А
Total Dissipation	P_{D}	46.3	W
Avalanche Energy, Single Pulsed	E _{AS}	206	mJ
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Case	R _{eJC}	2.7	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

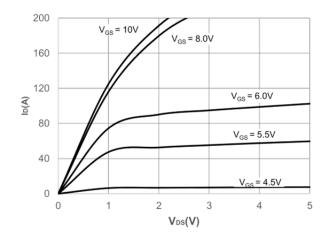
6.Electrical Characteristics at Ta=25°C (Note 3)

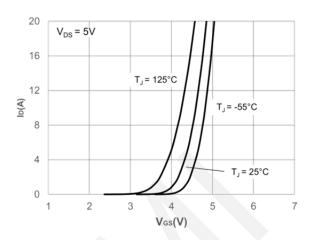
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A, V_{GS} = 0 V$	70	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 68V, V_{GS} = 0V$	-	1	1	μΑ
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _{DS} =250μA	2.0	3.0	4.0	٧
Static Drain to Source On-State Resistance	R _{DS(on)}	I _D =30A, V _{GS} = 10V	1	6.1	7.9	mΩ
Input Capacitance	C_{iss}	V _{GS} =0V,	-	5463	1	nF
Output Capacitance	C _{oss}	V _{DS} =35V,	-	250	-	pF
Reverse Transfer Capacitance	C _{rss}	Frequency=1.0MHz	-	199	-	pF
Turn-ON Delay Time	t _{d(on)}		-	20	-	ns
Rise Time	t _r	$V_{DS} = 35V, V_{GS} = 10V,$	1	30	1	ns
Turn-OFF Delay Time	t _{d(off)}	$I_D = 30A, R_{GEN} = 3\Omega$	ı	45	1	ns
Fall Time	t _f		ı	14	ı	ns
	Q_g	$V_{DS} = 35V,$ $V_{GS} = 0 \text{ to } 10V,$ $I_{D} = 30A$	ı	94	ı	nC
Total Gate Charge	Q_{gs}		-	30	-	nC
	Q_{gd}		-	24	-	nC
Diode Forward Voltage	V_{FSD}	I _S =30A, V _{GS} = 0	-	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

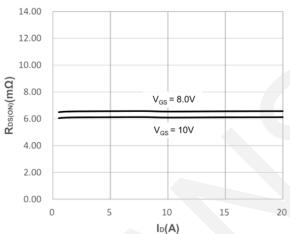


7. Typical electrical and thermal characteristics

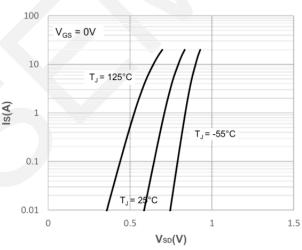




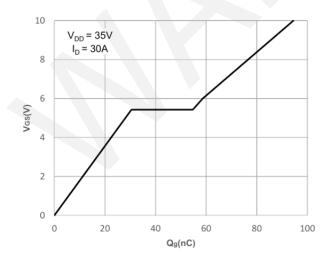
Output Characteristics



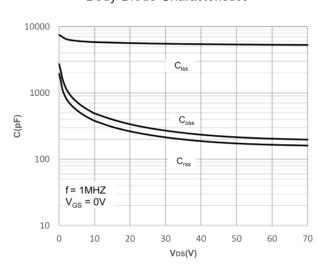
Typical Transfer Characteristics



On-resistance vs. Drain Current



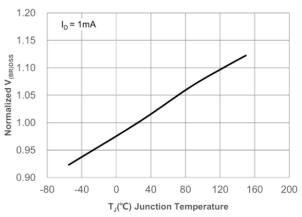
Body Diode Characteristics



Gate Charge Characteristics

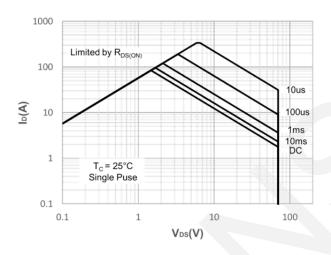
Capacitance Characteristics



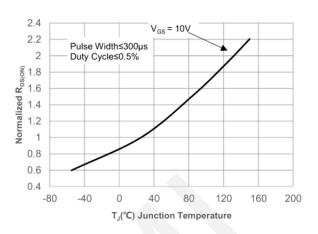


Normalized Breakdown Voltage vs.

Junction Temperature

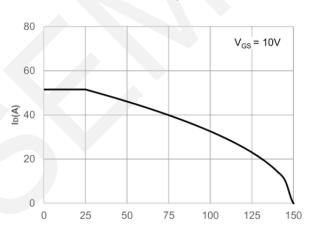


Maximum Safe Operating Area



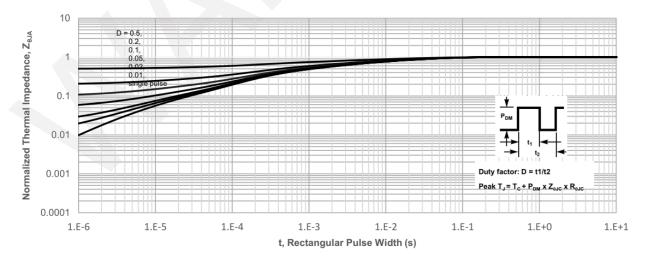
Normalized on Resistance vs.

Junction Temperature



Maximum Continuous Drain Current vs.

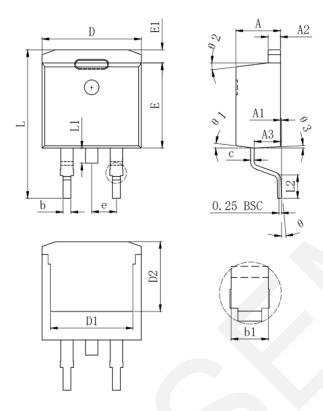
Case Temperature



Maximum Effective Transient Thermal Impedance, Junction-to-Case



8.Package Dimensions



SYMBOL		MILLIMETER	
	MIN	Тур.	MAX
A	4. 370	4. 570	4. 770
A1	0.000		0. 250
A2	1. 220	1. 270	1. 420
A3	2. 490	2.690	2. 890
b	0.700	0.810	0.960
b1	1, 170	1. 270	1. 470
С	0.300	0.380	0. 530
D	9.860	10. 160	10. 360
D1	8. 400 REF		
D2		7.073 REF	
Е	8. 500	8. 700	8. 900
E1	1.070	1. 270	1. 470
е	2.540 TYP		
L	14. 700	15. 100	15. 500
L1	1. 400	1.550	1. 700
L2	2.000	2. 300	2.600
θ	0°		9°
θ 1	7° TYP		
θ2	7° TYP		
θ3	3° TYP		



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