

Enhancement Mode N-Channel Power MOSFET

PDFN3X3/NMOS/30V/ \pm 20V/1.6V/80A/4.2m Ω

Rev1.1





30V, 4.2mΩ, 80A, Single N-Channel

1.Features

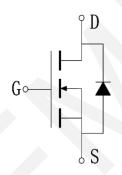
- ◆ 30V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V

2.A	pp	lica	tio	ns
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- ◆ Power Switching Application
- Load Switching



V _{DS}	R _{DS(on)} Typ.	I _D Max.
201/	4.2mΩ @ 10V	004
30V	6.7mΩ @ 4.5V	80A



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP3080AP3	WP3080AP3	PDFN3x3	5,000	50,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	30	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	l _D	80	А
Drain Current (Pulse), PW≤300μs	I DP	320	А
Total Dissipation	P _D	35	W
Avalanche Energy, Single Pulsed	Eas	144	mJ
Junction Temperature	Tj	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Junction to case	Rejc	3.6	°C/W
Junction to Ambient	Reja	43	°C/W

Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

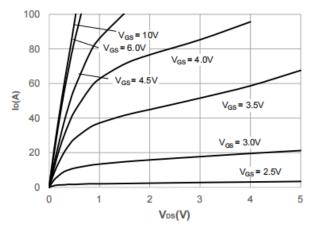
6.Electrical Characteristics at Ta=25°C (Note 3)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A$, $V_{GS} = 0 V$	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V			1.0	μA
Gate to Source Leakage Current	Igss	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _{DS} =250µA	1.0	1.6	2.5	V
Static Drain to Source On-State	D	I _D = 20A, V _{GS} = 10V		4.2	5.5	mΩ
Resistance	R _{DS(on)}	$I_D = 20A, V_{GS} = 4.5V$		6.7	8.5	mΩ
Input Capacitance	Ciss	V _{GS} =0V,		1790		pF
Output Capacitance	Coss	V _{DS} =15V,		225		pF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz		180		pF
Turn-ON Delay Time	Furn-ON Delay Time t _{d(on)}			7		ns
Rise Time	t _r	V _{DD} = 15V, I _{DS} = 30A,		15		ns
Turn-OFF Delay Time	t _{d(off)}	$V_{GS} = 10V$, $R_G = 3\Omega$		34		ns
Fall Time	t _f			10		ns
	Qg	V _{DS} = 15V,		34		nC
Total Gate Charge	Qgs	$V_{GS} = 0 \text{ to } 10V,$		6.5		nC
	Q_{gd}	I _{DS} = 30A		7.5		nC
Diode Forward Voltage	V _{FSD}	I _S = 20A, V _{GS} = 0V		0.85	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



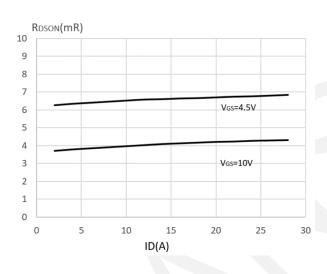
7. Typical electrical and thermal characteristics

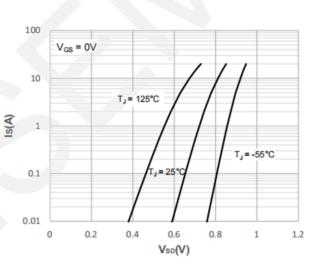


20 V_{DS} = 5V 16 T_J= -55°C 12 I_D(A) 8 T_J = 25°C 4 0 0.5 2 2.5 3 3.5 Vgs(V)

Output Characteristics

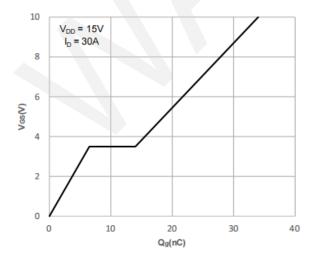


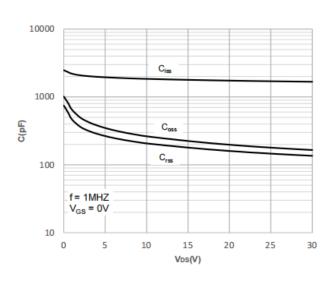




On-resistance vs. Drain Current

Body Diode Characteristics

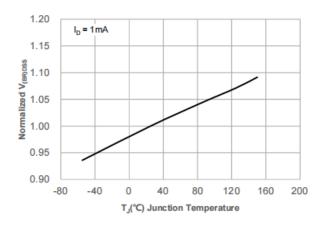


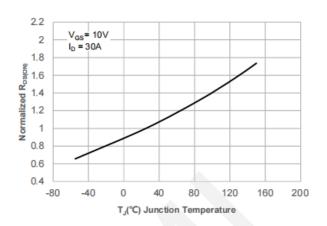


Gate Charge Characteristics

Capacitance Characteristics







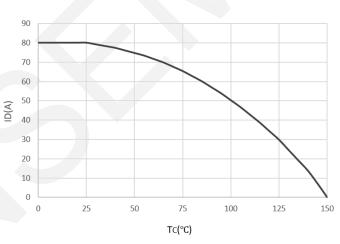
Normalized Breakdown Voltage vs.

Junction Temperature

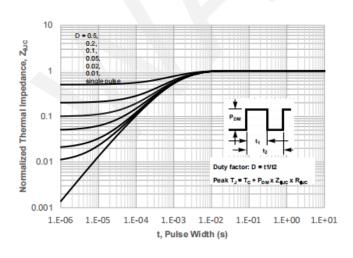
1000 Limited by R_{DS(ON)} 100 10us 10 100us 1ms 10ms 1 T_C= 25°C DC Single Puse 0.1 0.1 10 100 1 V_{DS}(V)

Normalized on Resistance vs.

Junction Temperature

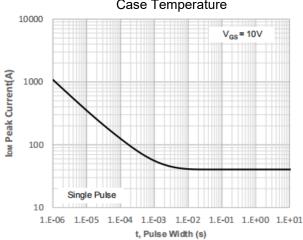


Maximum Safe Operating Area



Normalized Maximum Transient Thermal Impedance

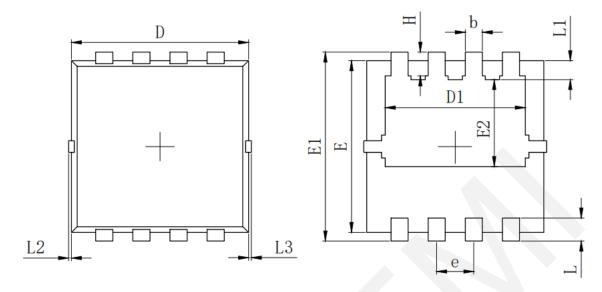
Maximum Continuous Drain Current vs.
Case Temperature

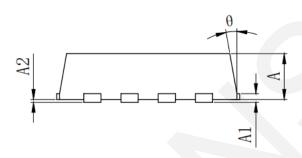


Peak Current Capacity



8.Package Dimensions





SYMBOL	MILLIMETER				
SYMBOL	MIN	Тур.	MAX		
A	0. 700	0.800	0.900		
A1		0. 152 REF.			
A2		0~0.05			
D	3. 000	3. 100	3. 200		
D1	2. 300	2. 450	2.600		
Е	2. 900	3.000	3. 100		
E1	3. 150	3. 300	3. 450		
E2	1. 320	1.520	1. 720		
b	0. 200	0.300	0.400		
е	0. 550	0.650	0.750		
L	0. 300	0.400	0.500		
L1	0. 180	0. 330	0.480		
L2	0~0. 100				
L3	0~0. 100				
Н	0. 315	0. 415	0. 515		
θ	8°	10°	12°		



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