

Enhancement Mode N-Channel Power MOSFET

 $TO-220/NMOS/40V/\pm20V/1.7V/90A/4.0m\Omega$

Rev_{0.1}





40V, 4.0mΩ, 90A, N-Channel MOSFET

1.Features

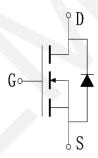
- Advanced Trench Technology
- ◆ Excellent R_{DS(ON)} and Low Gate Charge
- ◆ Lead Free
- ◆ 100% RG Tested
- ♦ 100% UIS Tested

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- ◆ Load Switch
- PWM Application
- Power Management



V_{DS}	R _{DS(on)} Typ.	I _D Max.
40)/	4.0mΩ @ 10V	004
40V	5.4mΩ @ 4.5V	90A



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Tube	PCS/CTN.
WP4090KFB	WP4090	TO-220	50	5,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	40	V
Gate to Source Voltage	V _{GSS}	±20	V
Drain Current (DC)	ID	90	А
Drain Current (Pulse), PW≤300μs	I _{DP}	360	А
Total Dissipation	P _D	26	W
Avalanche Energy, Single Pulsed	Eas	106	mJ
Junction Temperature	Tj	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Ambient	Reja	56	°C/W
Junction to case	Rejc	4.8	°C/W

Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

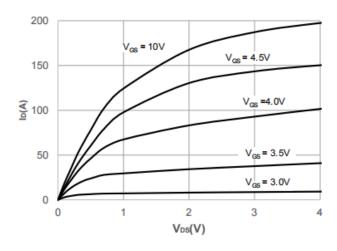
6.Electrical Characteristics at Ta=25°C (Note 3)

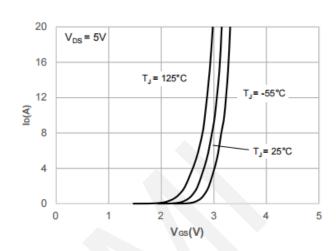
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A$, $V_{GS} = 0 V$	40	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V, V _{GS} = 0V	1	-	1	μA
Gate to Source Leakage Current	I _{GSS1}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$,	-	±100	nA
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} =V _{GS} , I _{DS} =250µA	1.0	1.7	2.5	V
Static Drain to Source On-State		I _D =30A, V _{GS} = 10V	-	4.0	5.1	mΩ
Resistance	R _{DS(on)}	I _D =20A, V _{GS} = 4.5V		5.3	7.0	mΩ
Input Capacitance	Ciss	V _{GS} =0V,	-	3031	-	pF
Output Capacitance	Coss	V _{DS} =20V,	-	213	-	pF
Reverse Transfer Capacitance	C _{rss}	Frequency=1.0MHz	-	179	-	pF
Turn-on Delay Time	t _{d(on)}		-	12	-	ns
Rise Time	t _r	$V_{DD} = 20V, R_L = 1\Omega$	-	11	-	ns
Turn-off Delay Time	t _{d(off)}	V_{GS} = 10V, R_{G} = 3 Ω	-	39	-	ns
Fall Time	t _f		-	12	-	ns
	Qg	V _{DS} = 20V,	-	59	-	nC
Total Gate Charge	Qgs	$V_{GS} = 20V$, $V_{GS} = 10V$,	-	12	_	nC
	Q _{gd}	I _D = 30A	-	12	-	nC
Diode Forward Voltage	V_{FSD}	I _S = 30A, V _{GS} = 0	0.4	0.8	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

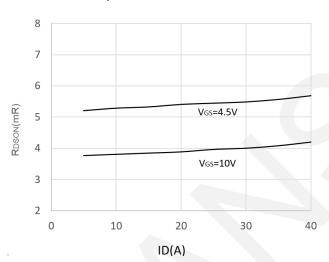


7. Typical electrical and thermal characteristics

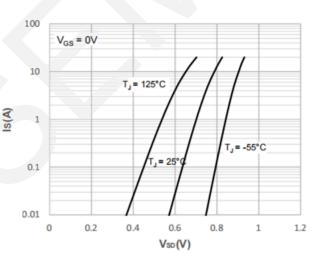




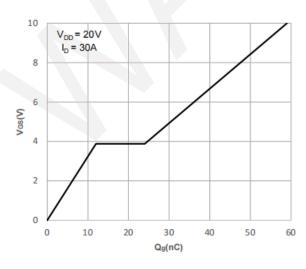
Output Characteristics



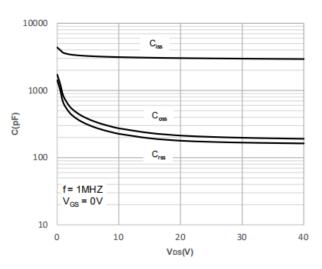
Typical Transfer Characteristics



On-resistance vs. Drain Current



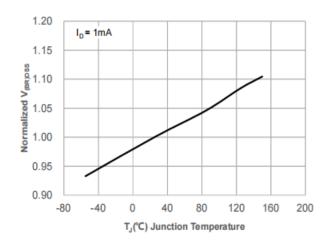
Body Diode Characteristics

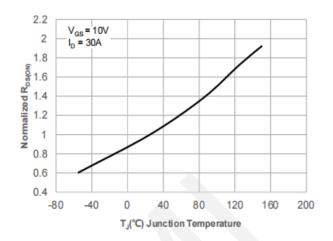


Gate Charge Characteristics

Capacitance Characteristics



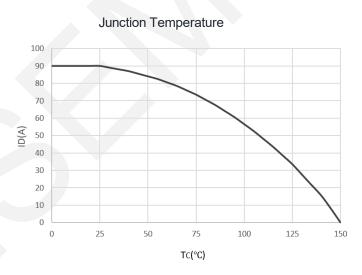




Normalized Breakdown Voltage vs.

Junction Temperature 1000 Limited by RDS(ON) 10us 100 100us 10 1 ms DC T_C = 25°C Single Puse 1 0.1 0.1 10 100 1

Normalized on Resistance vs.

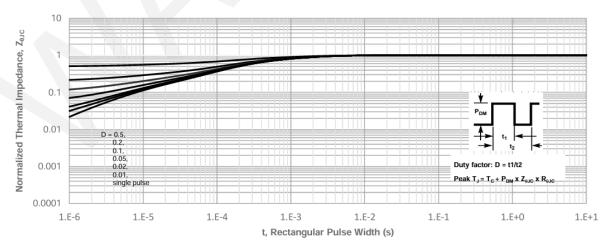


Maximum Safe Operating Area

V_{DS}(V)

Maximum Continuous Drain Current vs.

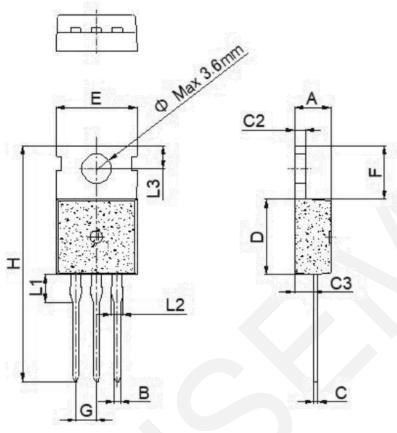
Case Temperature



Normalized Maximum Transient Thermal Impedance



8.Package Dimensions



	Dimensions							
Ref.		Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.		
Α	4.40		4.60	0.173		0.181		
В	0.70		0.90	0.028		0.035		
С	0.45		0.60	0.018		0.024		
C2	1.23		1.32	0.048		0.052		
C3	2.20		2.60	0.087	~	0.102		
D	8.90		9.90	0.350		0.390		
Е	9.90		10.3	0.390		0.406		
F	6.30		6.90	0.248		0.272		
G		2.54			0.1			
Н	28.0		29.8	1.102		1.173		
L1		3.39			0.133	2 -1 (6 -10		
L2	1.14		1.70	0.045		0.067		
L3	2.65		2.95	0.104		0.116		
Φ		3.6			0.142			



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