

Enhancement Mode P-Channel Power MOSFET

DFN2X2/PMOS/-20V/ \pm 12V/-0.7V/-9A/18m Ω

Rev_{0.2}





-20V, 18mΩ, -9A, P-Channel MOSFET

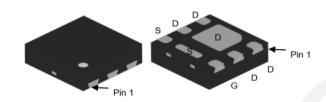
1.Features

- ◆ High Power and current handing capability
- Lead free product is acquired
- ◆ Surface Mount Package

V _{DS} Typ.	R _{DS(on)} Typ.	I _D Max.	
-20V	18mΩ @ -4.5V	0.4	
	21mΩ @ -2.5V	-9A	

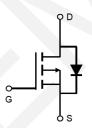
2.Applications

- Battery Protection
- Battery Powered Systems
- Power Management in Notebook Computer
- Portable Equipment



Pin Description

DFN2X2-6L



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX018P02N2	018P02	DFN2X2	3,000	180,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	-20	V
Gate to Source Voltage	V _{GSS}	±12	V
Drain Current-Continuous	ID	-9	Α
Drain Current (Pulse)	I _{DM}	-36	Α
Maximum Power Dissipation	P _D	1.1	W
Operating Junction and Storage Temperature Range	Tj, Tstg	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient	ReJA	114	°C/W

Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

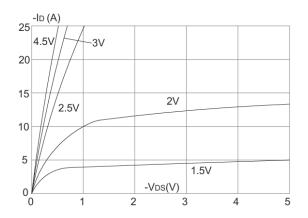
6.Electrical Characteristics at Ta=25°C (Note 3)

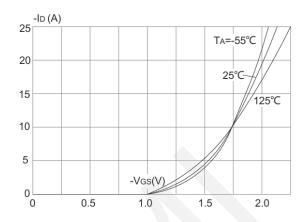
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = -250 \mu A$, $V_{GS} = 0 V$	-20	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V	-		-1	μΑ
Gate-Body Leakage Current	Igss	$V_{GS} = \pm 12V, V_{DS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _{DS} =-250µA	-0.3	-0.7	-1.5	V
Drain to Source On-State	Б	I _D =-5A, V _{GS} =-4.5V	-	18	23	mΩ
Resistance	R _{DS(on)}	I _D =-4A, V _{GS} = -2.5V	-	21	30	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V,	-	1200	-	pF
Output Capacitance	Coss	V _{DS} =-10V,	-	191	-	pF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz	-	168	-	pF
Turn-ON Delay Time	t _{d(on)}	V_{DD} =-10V, I_{D} =-5A, R_{G} = 10 Ω , V_{GS} =-4.5V	ı	11	-	ns
Turn-ON Rise Time	tr		-	35	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	30	-	ns
Turn-ON Fall Time	t _f		ı	10	-	ns
Total Gate Charge	Qg	V _{DS} = -10V, V _{GS} = -4.5V,	-	33.7	-	nC
Gate-Source Charge	Qgs		-	3.5	-	nC
Gate-Drain Charge	Q_{gd}	I _D = -5A	-	10.5	-	nC
Diode Forward Voltage	V _{SD}	$I_{SD} = -5A$, $V_{GS} = 0$	-0.5	-	-1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

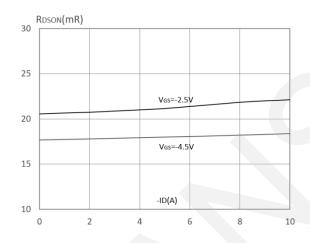


7. Typical electrical and thermal characteristics

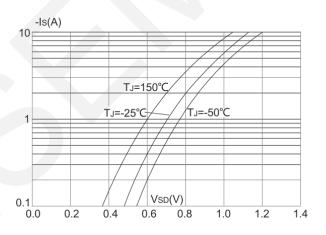




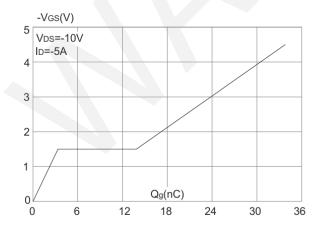
Typical Output Characteristics



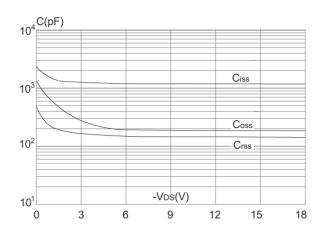
Typical Transfer Characteristics



On-resistance vs. Drain Current



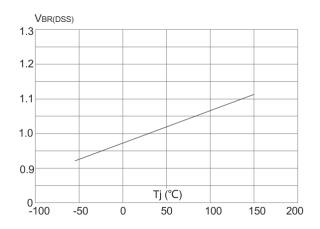
Body Diode Characteristics

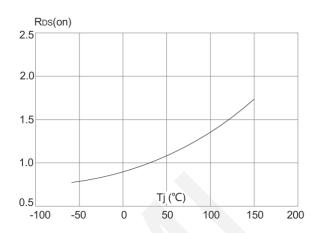


Gate Charge Characteristics

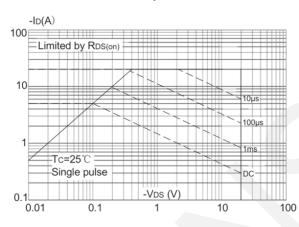
Capacitance Characteristics



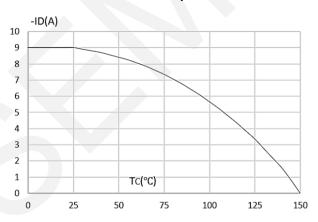




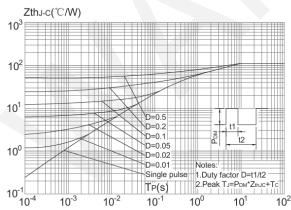
Normalized Breakdown voltage vs. Junction Temperature



Normalized on Resistance vs. Junction Temperature



Normalized Breakdown voltage vs. Junction Temperature

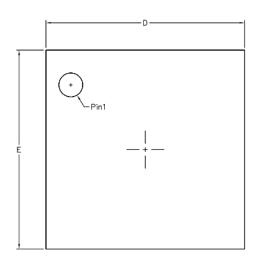


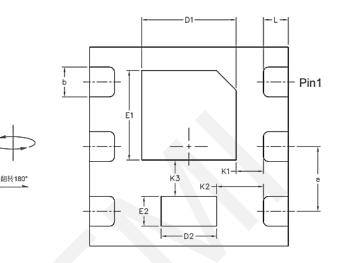
Current De-rating

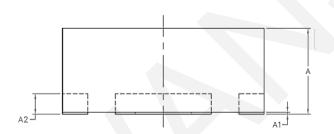
Normalized Maximum Transient Thermal Impedance



8.Package Dimensions







SYMBOL	MILLIMETER			
O TABOB	MIN	NOM	MAXO	
A	80	0.85	0. 900.	
A1	00	0.02	0.05	
A2	(). 203REF	`.	
b	0. 27	0.30	0. 33	
D	1.95	2.00	2.05	
Е	1.95	2.00	2.05	
D1	0. 92	0. 95	0.98	
E1	0.87	0.90	0. 93	
D2	0. 53	0. 56	0. 59	
E2	0. 27	0.30	0. 33	
e	0. 60	0.65	0.70	
K1	0. 25	0. 28	0.31	
K2	0.44	0.47	0.50	
К3	0.34	0. 37	0.40	
L	0. 22	0. 25	0. 28	



9.Important Notice

WAN SEMICONDUCTOR (NINGBO) CO.,LTD reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services and to discontinue any product or service. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to WANSEMI's terms and conditions of sale supplied at the time of order acknowledgment.

WANSEMI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in WANSEMI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent WANSEMI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

WANSEMI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using WANSEMI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

No WANSEMI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Unless WANSEMI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use, WANSEMI will not be responsible for any failure of such components to meet such requirements.