

Enhancement Mode P-Channel Power MOSFET

SOT23/PMOS/-16V/ \pm 12V/-0.6V/-12A/18m Ω

Rev_{0.1}





-16V, 18mΩ, -12A, P-Channel MOSFET

1.Features

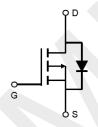
- Advanced Trench Technology
- ◆ Surface mount package

- ◆ Power Management
- Load Switching



Pin Description





Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.	
WX018P02SS	018P02	SOT23	3,000	180,000	

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	-16	V
Gate to Source Voltage	V _{GSS}	±12	V
Drain Current (DC)	l _D	-12	А
Drain Current (Pulse), PW≤300μs	I _{DP}	-48	Α
Total Dissipation	P _D	3.9	W
Junction Temperature	Tj	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	Reja	30	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.



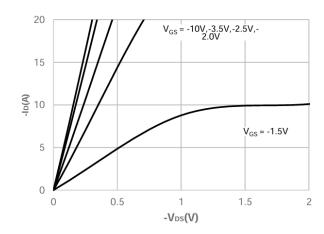
6.Electrical Characteristics at Ta=25°C (Note 3)

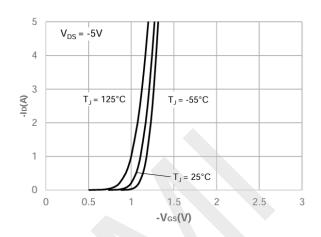
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = -250 \mu A$, $V_{GS} = 0 V$	-16	ı	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = -16V, V _{GS} = 0V	-	ı	-1	μΑ
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$	-	ı	±100	nA
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V_{DS} = V_{GS} , I_{DS} =-250 μ A	-0.4	-0.6	-1.0	V
Static Drain to Source On-State	I _D =-4.1A, V _{GS} =-4.5V	-	18	24	mΩ	
Resistance	R _{DS(on)}	I _D =-3A, V _{GS} = -2.5V	-	23	30	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V,	-	920	-	pF
Output Capacitance	Coss	V _{DS} =-8V, Frequency=1.0MHz	-	127	-	pF
Reverse Transfer Capacitance	Crss		-	108	-	pF
Turn-ON Delay Time	t _{d(on)}		-	7.2	-	ns
Rise Time	t _r	V_{DD} =-10V, I_{D} =-3A, R_{G} = 3 Ω , V_{GS} = -4.5V	-	17.5	-	ns
Turn-OFF Delay Time	t _{d(off)}		-	62	-	ns
Fall Time	t _f		-	45	-	ns
	Qg	V _{DS} = -8V,	-	14	-	nC
Total Gate Charge	Qgs	$V_{GS} = 0 \text{ to } -4.5V,$	-	2.2	-	nC
	Qgd	I _D = -3A	-	2.9	-	nC
Diode Forward Voltage	V _{FSD}	I _{SD} = -4.1A, V _{GS} = 0	-0.5	-	-1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

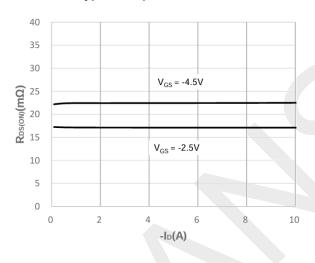


7. Typical Electrical and Thermal Characteristics

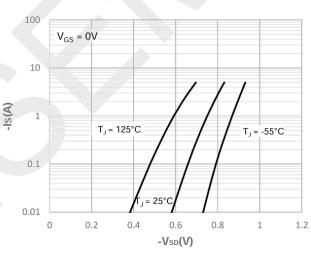




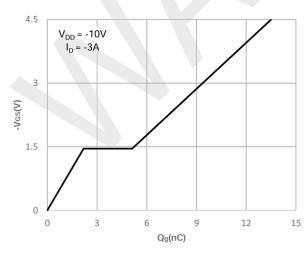
Typical Output Characteristics



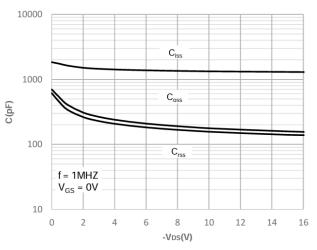
Typical Transfer Characteristics



On-resistance vs. Drain Current



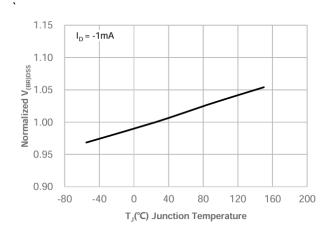
Body Diode Characteristics

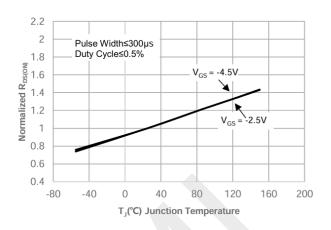


Gate Charge Characteristics

Capacitance Characteristics



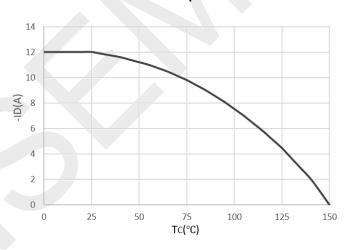




Normalized Breakdown voltage vs. Junction Temperature

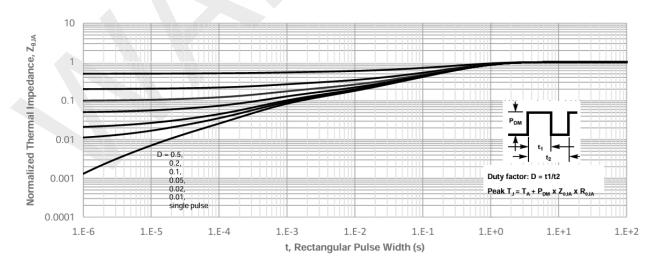
100 Limited by $R_{DS(ON)}$ 1us 10us 10 100us 1ms 10ms DC 0.1 $T_A = 25^{\circ}C$ Single Puse 0.01 0.1 10 100 -V_{DS}(V)

Normalized on Resistance vs. Junction Temperature



Normalized Breakdown voltage vs. Junction Temperature

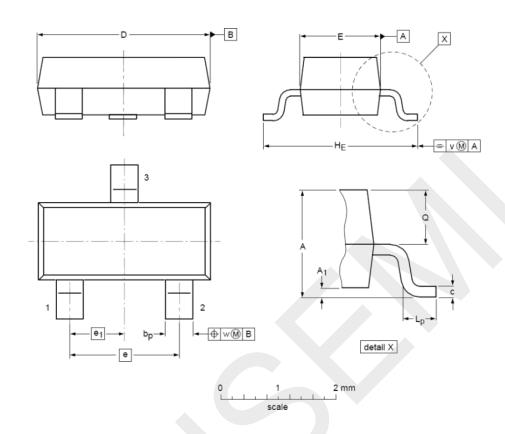
Current De-rating



Normalized Maximum Transient Thermal Impedance



8.Package Dimensions



DIMENSIONS (unit : mm)

Symbol	Min	Тур	Max	Symbol	Min	Тур	Max
Α	0.90	1.01	1.15	A ₁	0.01	0.05	0.10
b _p	0.30	0.42	0.50	С	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
е		1.90		e ₁		0.95	
HE	2.25	2.40	2.55	Lp	0.30	0.42	0.50
Q	0.45	0.49	0.55	٧		0.20	
w		0.10					



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