

Enhancement Mode N-Channel Power MOSFET

 $TO-252/NMOS/68V/\pm20V/3.0V/80A/6.3m\Omega$

Rev1.0





68V, 6.3mΩ, 80A, Single N-Channel

1.Features

- ♦ 68V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V
- ♦ 100% RG Tested
- ◆ 100% UIS Tested

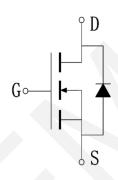
2.Applications

- Power Switching Application
- Load Switching



Pin Description

V_{DS} R_{DS(on)} Typ. I_D Max. 68V 6.3mΩ @ 10V 80A



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP6888A	<u>6</u> 888	TO-252	2,500	25,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units	
Drain to Source Voltage	V _{DSS}	68	V	
Gate to Source Voltage	V_{GSS}	±20	V	
Drain Current (DC)	ID	80	А	
Drain Current (Pulse), PW≤300μs	I _{DP}	320	А	
Total Dissipation	P _D	46	W	
Avalanche Energy, Single Pulsed	Eas	361	mJ	
Junction Temperature	Tj	150	°C	
Storage Temperature	T_{stg}	-55 to +150	°C	

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to case	Rejc	2.7	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

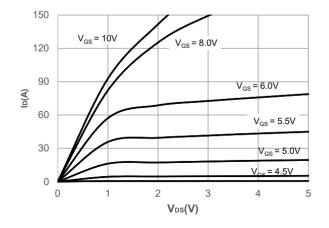
6.Electrical Characteristics at Ta=25°C (Note 3)

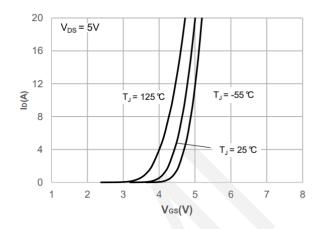
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A$, $V_{GS} = 0 V$	68	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = 68V, V _{GS} = 0V	-	ı	1	μA
Gate to Source Leakage Current	I _{GSS1}	$V_{GS} = \pm 20V$, $V_{SS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _{DS} =250µA	2.0	3.0	4.0	٧
Static Drain to Source On-State Resistance	R _{DS(on)}	I _D = 30A, V _{GS} = 10V	-	6.3	7.8	mΩ
Input Capacitance	Ciss	V _{GS} =0V,	1	4385	-	pF
Output Capacitance	Coss	V _{DS} =30V,	-	225	-	pF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz	-	175	-	pF
Turn-ON Delay Time	t _{d(on)}		1	19	-	ns
Rise Time	t _r	$V_{DD} = 34V, I_D = 30A,$ $V_{GS} = 10V, R_G = 3\Omega$	-	27	-	ns
Turn-OFF Delay Time	t _{d(off)}		ı	37	-	ns
Fall Time	t _f		ı	10	-	ns
	Q_g	$V_{DS} = 34V,$ $V_{GS} = 0 \text{ to } 10V,$ $I_{D} = 30A$	ı	70	-	nC
Total Gate Charge	Q _{gs}		-	24	-	nC
	Q _{gd}		-	17	-	nC
Diode Forward Voltage	V _{FSD}	I _S = 30A, V _{GS} = 0	0.5	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

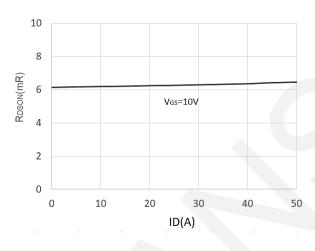


7. Typical electrical and thermal characteristics

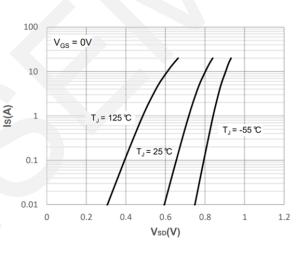




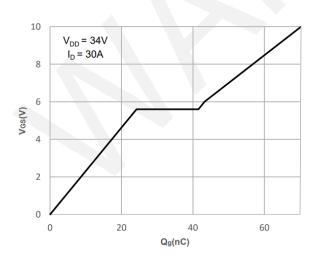
Output Characteristics



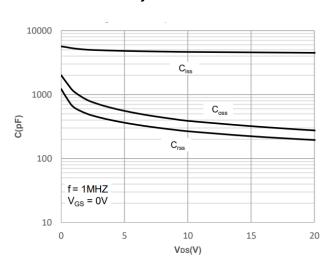
Typical Transfer Characteristics



On-resistance vs. Drain Current



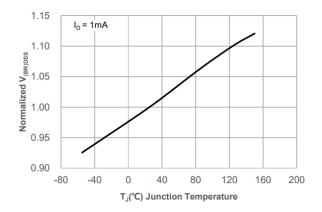
Body Diode Characteristics

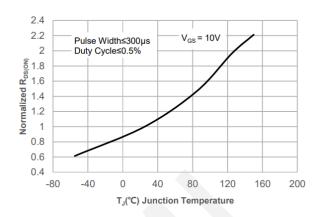


Gate Charge Characteristics

Capacitance Characteristics

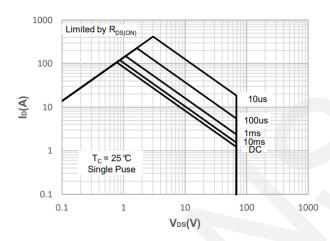






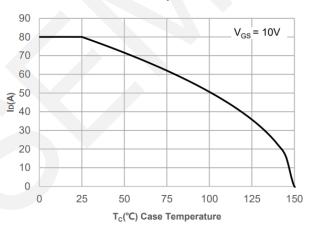
Normalized Breakdown Voltage vs.

Junction Temperature



Normalized on Resistance vs.

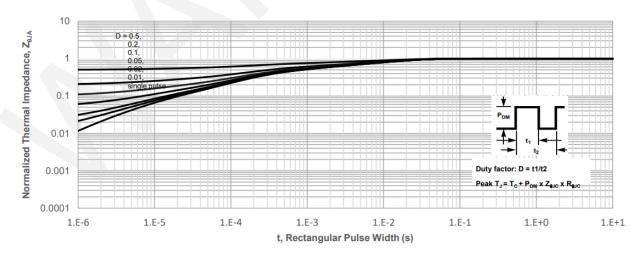
Junction Temperature



Maximum Safe Operating Area

Maximum Continuous Drain Current vs.

Case Temperature

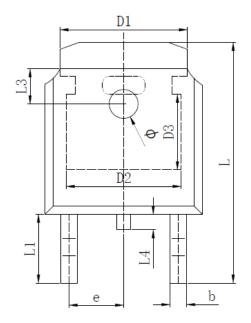


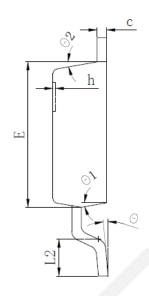
Normalized Maximum Transient

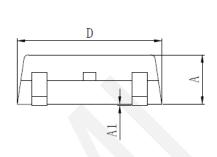
Thermal Impedance



8.Package Dimensions







SYMBOL	MILLIMETER				
	MIN	Тур.	MAX		
A	2. 200	2. 300	2. 400		
A1	0.000		0. 127		
b	0. 640	0.690	0. 740		
c(电镀后)	0. 460	0. 520	0. 580		
D	6. 500	6. 600	6. 700		
D1	5. 334 REF				
D2	4.826 REF				
D3	3.166 REF				
E	6. 000	6. 100	6. 200		
е	2.286 TYP				
h	0.000	0.100	0. 200		
L	9. 900	10. 100	10. 300		
L1	2.888 REF				
L2	1. 400	1. 550	1. 700		
L3	1.600 REF				
L4	0.600	0.800	1. 000		
ф	1. 100	1. 200	1. 300		
θ	0°		8°		
θ1	9° TYP				
θ2	9° TYP				



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