

Enhancement Mode P-Channel Power MOSFET

DFN2X2/PMOS/-16V/ \pm 12V/-0.65V/-20A/15.5m Ω

Rev1.0





-16V, 15.3mΩ, -20A, P-Channel MOSFET

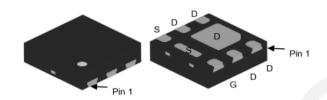
1.Features

- ◆ High Power and current handing capability
- ◆ Lead free product is acquired
- ◆ Surface Mount Package

V _{DS} Typ.	R _{DS(on)} Typ.	l _□ Max.
-16V	12.7mΩ @ -10V	
	15.5mΩ @ -4.5V	-20A
	20.9mΩ @ -2.5V	

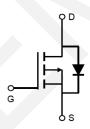
2.Applications

- Battery Protection
- Battery Powered Systems
- Power Management in Notebook Computer
- Portable Equipment



Pin Description

DFN2X2-6L



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX012BP02N2	0 <u>1</u> 2P02	DFN2X2	3,000	180,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	-16	٧
Gate to Source Voltage	V _{GSS}	±12	V
Drain Current-Continuous	ID	-12	Α
Drain Current (Pulse)	Ірм	-48	Α
Maximum Power Dissipation	P _D	1.9	W
Operating Junction and Storage Temperature Range	Tj, Tstg	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Maximum Junction-to-Ambien	ReJA	64	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

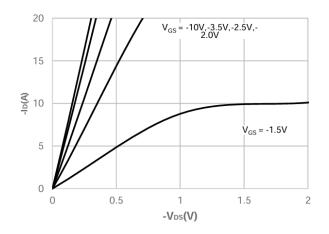
6.Electrical Characteristics at Ta=25°C (Note 3)

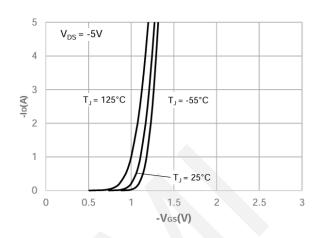
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = -250 \mu A$, $V_{GS} = 0 V$	-16	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = -16V, V _{GS} = 0V	-	-	-1	μΑ
Gate-Body Leakage Current	Igss	$V_{GS} = \pm 12V, V_{DS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _{DS} =-250µA	-0.4	-0.65	-1.0	V
	R _{DS(on)}	I _D =-6A, V _{GS} =-10V	1	12.7	17	mΩ
Drain to Source On-State Resistance		I _D =-4.1A, V _{GS} =-4.5V	-	15.5	20	mΩ
redistance		I _D =-3A, V _{GS} = -2.5V		20.9	27	mΩ
Input Capacitance	Ciss	V _{GS} =0V, V _{DS} =-8V, Frequency=1.0MHz	-	1014	-	pF
Output Capacitance	Coss		-	167	-	pF
Reverse Transfer Capacitance	Crss		-	135	-	pF
Turn-ON Delay Time	t _{d(on)}	V_{DD} =-10V, I_{D} =-3A, R_{G} = 3 Ω , V_{GS} = -4.5V	-	7.2	-	ns
Turn-ON Rise Time	tr		-	17.5	-	ns
Turn-OFF Delay Time	t _{d(off)}		-	62	-	ns
Turn-ON Fall Time	t _f		-	45	-	ns
Total Gate Charge	Qg	V _{DS} = -8V, V _{GS} = 0 to -4.5V,	-	14	-	nC
Gate-Source Charge	Qgs		-	2.2	-	nC
Gate-Drain Charge	Q_{gd}	I _D = -3A	-	2.9	-	nC
Diode Forward Voltage	VsD	I _{SD} = -4.1A, V _{GS} = 0	-0.5	-	-1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

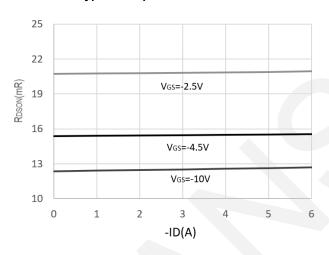


7. Typical electrical and thermal characteristics

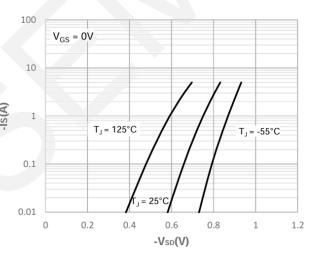




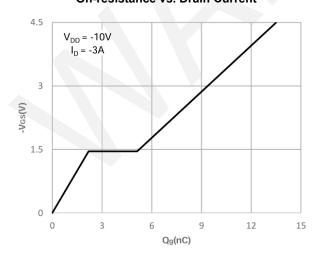
Typical Output Characteristics



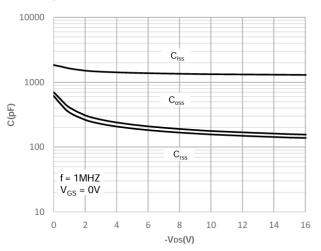
Typical Transfer Characteristics



On-resistance vs. Drain Current



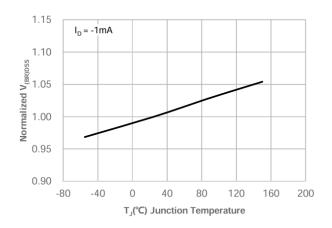
Body Diode Characteristics

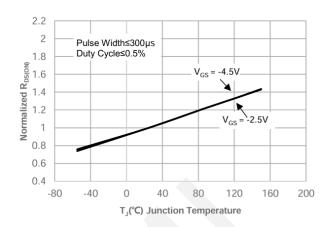


Gate Charge Characteristics

Capacitance Characteristics

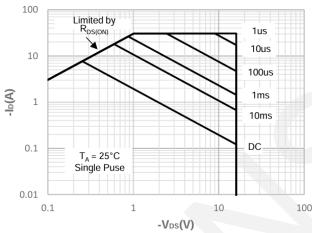




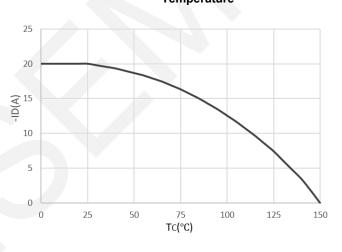


Normalized Breakdown voltage vs.

Junction Temperature

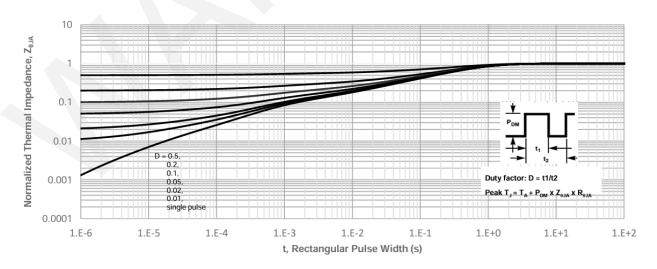


Normalized on Resistance vs. Junction **Temperature**



Normalized Breakdown voltage vs. **Junction Temperature**

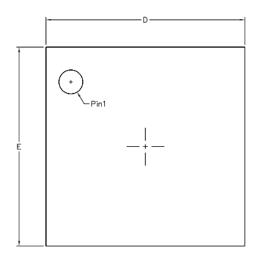
Current De-rating

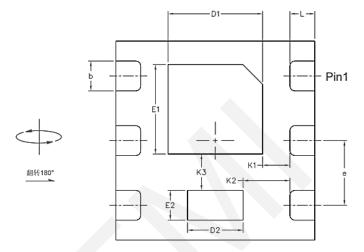


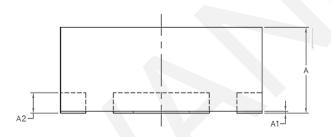
Normalized Maximum Transient Thermal Impedance



8.Package Dimensions







SYMBOL	MILLIMETER			
OTMBOD.	MIN	NOM	MAXO.	
A	80	0.85	0. 900.	
A1	00	0.02	0.05	
A2	0. 203REF.			
b	0. 27	0.30	0. 33	
D	1.95	2.00	2.05	
Е	1.95	2.00	2.05	
D1	0. 92	0. 95	0.98	
E1	0.87	0.90	0. 93	
D2	0. 53	0. 56	0. 59	
E2	0. 27	0.30	0.33	
е	0.60	0.65	0. 70	
K1	0. 25	0. 28	0.31	
K2	0. 44	0.47	0.50	
К3	0.34	0. 37	0.40	
L	0. 22	0. 25	0. 28	



9.Important Notice

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