

N-channel Enhancement Mode Power MOSFET

TOLL/NMOS/60V/ \pm 20V/1.6V/250A/1.6m Ω

Rev_{0.1}





60V, 1.6mΩ, 250A, N-channel MOSFET

1.Features

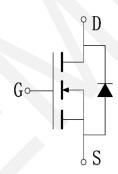
- ◆ Excellent RDS(ON) and Low Gate Charge
- ◆ 100% UIS Tested
- 100% ΔVds Tested
- ♦ 100% RG Tested
- ◆ Halogen-free; RoHS-compliant

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- ◆ Load Switch
- PWM Application
- Power Management



V _{DS}	R _{DS(on)} Typ.	I _D Max.	
60V	1.6mΩ @10V	0504	
	2.1mΩ @4.5V	250A	



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Package	Marking	PCS/Reel	PCS/CTN.
WX012AN06LL	TOLL	<u>0</u> 12N06	2,000	16,000

4. Absolute Max Ratings at Ta=25°C (Note1)

Parameter		Symbol	Maximum	Units
Drain to Source Voltage		V _{DSS}	60	V
Gate to Source Voltage		V _{GSS}	±20	V
	T C = 25°C	ID	250	Α
Drain Current (DC)	T C = 100°C	I _D	159	Α
Drain Current (Pulse), PW≤300μs		I _{DM}	1000	Α
Avalanche Energy, Single Pulsed		Eas	489	mJ
Takal Disabatian	T C = 25°C	P _D	156	W
Total Dissipation	T C = 100°C	P _D	63	W
Junction Temperature		Tj	150	°C
Storage Temperature		T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may



be affected.

5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Junction to Ambient	Reja	41	°C/W
Junction to case	Rejc	0.8	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

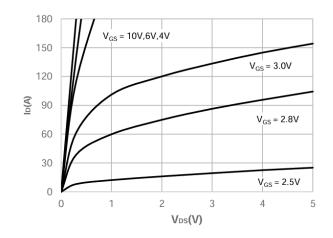
6.Electrical Characteristics at Ta=25°C (Note 2)

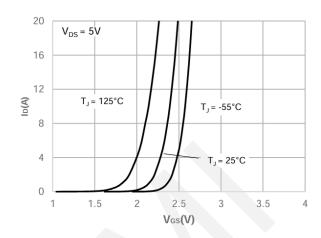
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A$, $V_{GS} = 0 V$	60	-	-	٧
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} = 0V	-	-	1	μΑ
Gate to Source Leakage Current	Igss	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _{DS} =250μA	1.3	1.6	2.0	V
Static Drain to Source On-State	_	I _D = 15A, V _{GS} = 10V	-	1.6	2.1	mΩ
Resistance	R _{DS(on)}	I _D = 10A, V _{GS} = 4.5V		2.1	3.1	mΩ
Input Capacitance	Ciss	V _{GS} =0V,	-	5584	-	nF
Output Capacitance	Coss	V _{DS} =30V,	-	2623	-	pF
Reverse Transfer Capacitance	C _{rss}	Frequency=1.0MHz	-	135	-	pF
Turn-on Delay Time	t _{d(on)}		-	13	-	ns
Rise Time	tr	$V_{DD} = 30V, I_{D}=20A,$	-	30	-	ns
Turn-off Delay Time	t _{d(off)}	$V_{GS} = 10V,$ $R_{GEN} = 3\Omega$	-	83	-	ns
Fall Time	t _f		-	67	-	ns
	Qg	$V_{DS} = 30V,$ $V_{GS} = 0 \text{ to } 10V,$ $I_{D} = 20A$	-	96	-	nC
Total Gate Charge	Qgs		-	15	-	nC
	Q _{gd}		-	21	-	nC
Diode Forward Voltage	V _{FSD}	I _S =15A, V _{GS} = 0	0.5	-	1.2	V

Note 2: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

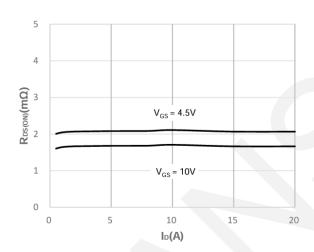


7. Typical electrical and thermal characteristics

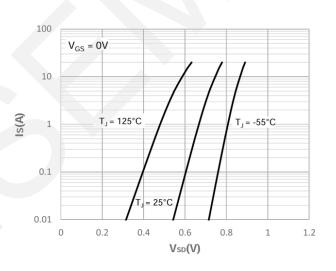




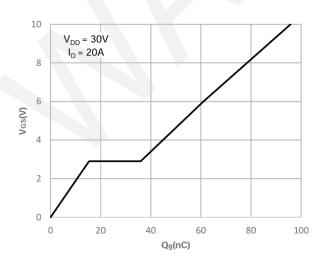
Saturation Characteristics



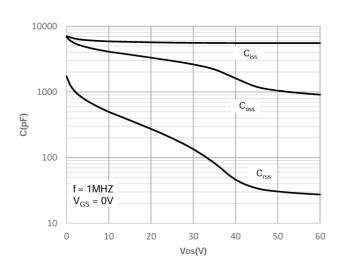
Transfer Characteristics



R_{DS(on)}vs.Drain Current



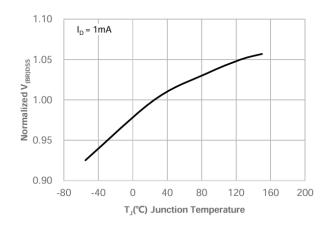
Body-Diode Characteristics

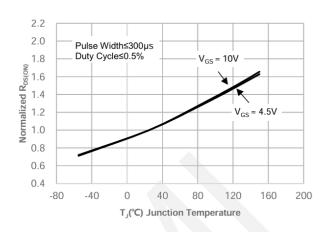


Gate Charge Characteristics

Capacitance Characteristics



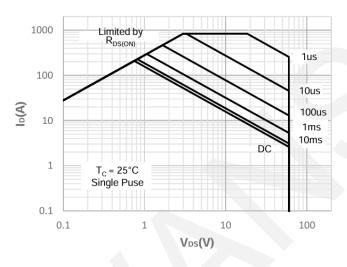


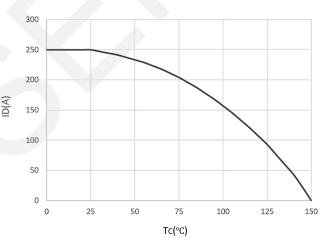


Normalized Breakdown voltage vs. Junction Temperature

Normalized on Resistance vs.

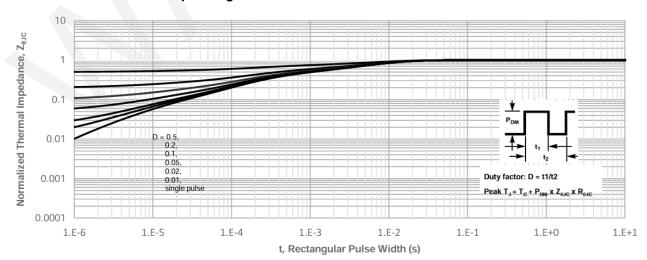
Junction Temperature





Maximum Safe Operating Area

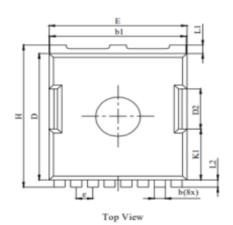
Current De-rating

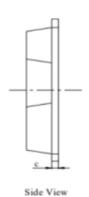


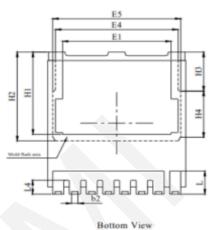
Normalized Maximum Transient Thermal Impedance

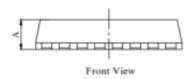


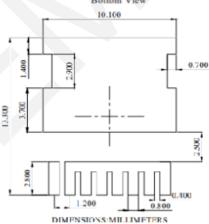
8.Package Dimensions











DIM	MILLIMETER				
DIM.	MIN	NOM	MAX		
Α	2.20	2.30	2.50		
b	0.70	0.80	0.90		
bl	9.70	9.80	9.90		
b2	0.42	0.46	0.50		
С	0.40	0.50	0.65		
D	10.28	10.38	10.58		
D2		3.30			
E	9.70	9.90	10.10		
E1		7.80			
E4	8.80				
E5	9.20				
e		1.20(BSC)			
Н	11.48	11.68	11.88		
HI	6.55	6.75	6.85		
H2	7.30				
Н3	3.20				
H4	3.80				
K1	4.18				
L	1.70	1.90	2.10		
L1	0.70				
L2	0.60				
L4	1.00	1.15	1.30		



9. Important Notice

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