

# **Enhancement Mode N-Channel Power MOSFET**

PDFN5X6/NMOS/68V/ $\pm$ 20V/3.0V/80A/6.1m $\Omega$ 

Rev<sub>0.5</sub>





## 68V, 6.1mΩ, 80A, Single N-Channel

#### 1.Features

- ♦ 68V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V
- ♦ 100% RG Tested
- ◆ 100% UIS Tested

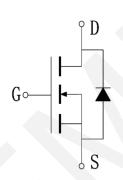
#### 2.Applications

- Power Switching Application
- Load Switching



Pin Description

V <sub>DS</sub>	R <sub>DS(on)</sub> Typ.	I <sub>D</sub> Max.
68V	6.1mΩ @ 10V	80A



Schematic Diagram

#### 3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX080N07PA	080N07	PDFN5X6	5,000	50,000

#### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V <sub>DSS</sub>	68	V
Gate to Source Voltage	$V_{GSS}$	±20	V
Drain Current (DC)	lσ	80	А
Drain Current (Pulse), PW≤300μs	I <sub>DP</sub>	320	А
Total Dissipation	P <sub>D</sub>	103	W
Avalanche Energy, Single Pulsed	Eas	361	mJ
Junction Temperature	Tj	175	°C
Storage Temperature	$T_{stg}$	-55 to +175	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



#### 5. Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to case	Rejc	1.46	°C/W

Note 2: When mounted on 1 inch square copper board  $t \le 10$ sec The value in any given application depends on the user's specific board design.

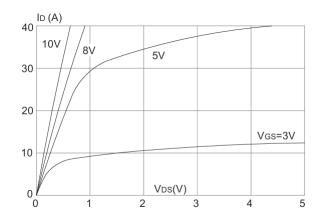
#### 6.Electrical Characteristics at Ta=25°C (Note 3)

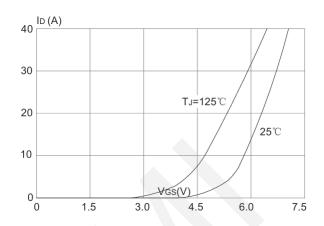
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$I_D = 250 \mu A$ , $V_{GS} = 0 V$	68	-	-	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 68V, V <sub>GS</sub> = 0V	-	1	1	μΑ
Gate to Source Leakage Current	Igss	$V_{GS} = \pm 20V$ , $V_{SS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA	2.0	3.0	4.0	V
Static Drain to Source On-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> = 30A, V <sub>GS</sub> = 10V	-	6.1	7.5	mΩ
Input Capacitance	Ciss	V <sub>GS</sub> =0V,	1	4244	-	pF
Output Capacitance	Coss	V <sub>DS</sub> =30V,	-	210	-	pF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz	-	170	-	pF
Turn-ON Delay Time	t <sub>d(on)</sub>		1	15	-	ns
Rise Time	tr	$V_{DS} = 30V, I_{D} = 30A,$	ı	90	-	ns
Turn-OFF Delay Time	$t_{\sf d(off)}$	$V_{GS} = 10V$ , $R_G = 3\Omega$	-	45	-	ns
Fall Time	t <sub>f</sub>		-	30	-	ns
	$Q_g$	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 30A	-	35	-	nC
Total Gate Charge	Q <sub>gs</sub>		-	10	-	nC
	Qgd		-	9	-	nC
Diode Forward Voltage	V <sub>FSD</sub>	I <sub>S</sub> = 30A, V <sub>GS</sub> = 0	0.5	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

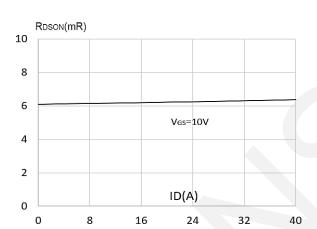


#### 7. Typical electrical and thermal characteristics

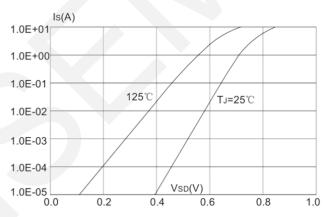




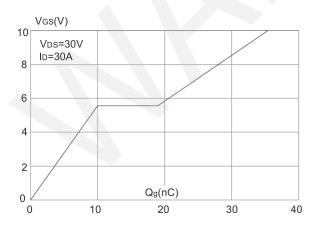
#### **Output Characteristics**



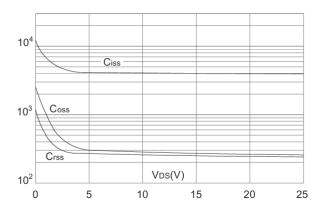
**Transfer Characteristics** 



**Rdson-Drain Current** 



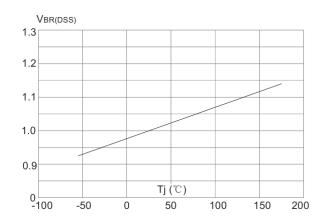
**Body Diode Characteristic** 

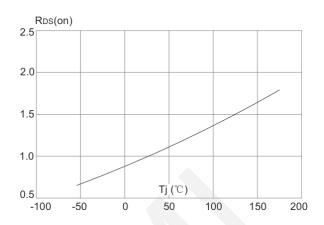


**Gate Charge** 

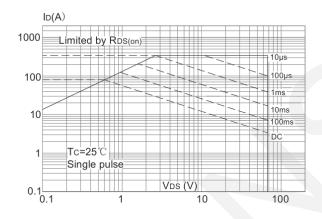
**Capacitance Characteristics** 





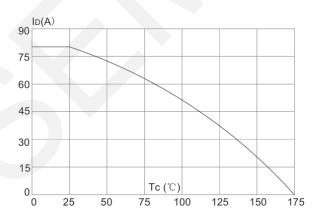


# Normalized Breakdown Voltage vs. Junction Temperature

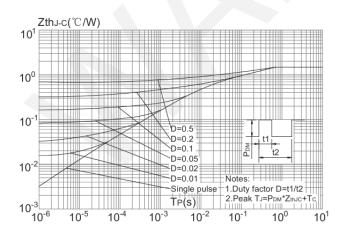


Normalized on Resistance vs.

#### **Junction Temperature**



#### **Maximum Safe Operating Area**

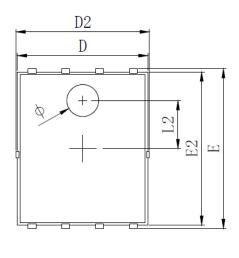


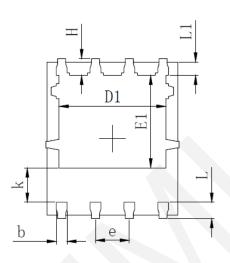
Maximum Continuous Drain Current vs. Case Temperature

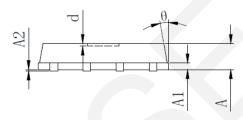
**Maximum Effective Transient Thermal Impedance, Junction-to-Case** 



### 8.Package Dimensions







SYMBOL		MILLIMETER	
	MIN	Typ.	MAX
A	0. 900	1. 000	1. 100
A1		0. 254 REF.	
A2		0~0.05	
D	4. 824	4. 900	4.976
D1	3. 910	4. 010	4. 110
D2	4. 924	5. 000	5. 076
Е	5. 924	6. 000	6.076
E1	3. 375	3. 475	3. 575
E2	5. 674	5. 750	5.826
b	0. 350	0. 400	0.450
е	1.270 TYP.		
L	0. 534	0. 610	0.686
L1	0. 424	0. 500	0. 576
L2	1.800 REF.		
k	1. 190	1. 290	1.390
Н	0. 549	0. 625	0. 701
θ	8°	10°	12°
ф	1. 100	1. 200	1.300
d			0. 100



#### 9. Important Notice

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