



WANSEMI
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WX050N10KD

Enhancement Mode N-Channel Power MOSFET

TO-252/NMOS/100V/±20V/1.7V/100A/5.0mΩ

Rev0.2



100V, 5.0mΩ, 100A, N-Channel Enhancement MOSFET

1. Features

- ◆ 100V MOSFET technology
- ◆ Low on-state resistance
- ◆ Fast switching
- ◆ $V_{GS} \pm 20V$
- ◆ 100% RG Tested
- ◆ 100% UIS Tested

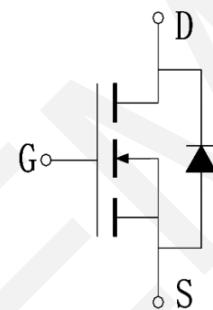
V_{DS}	$R_{DS(on)}$ Typ.	I_D Max.
100V	5.0mΩ @ 10V	100A
	6.5mΩ @ 4.5V	

2. Applications

- ◆ Power Switching Application
- ◆ Load Switching



TO-252
Pin Description



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX050N10KD	050N10	TO-252	2,500	25,000

4. Absolute Max Ratings at $T_a=25^\circ C$ (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	100	V
Gate to Source Voltage	V_{GSS}	± 20	V
Drain Current (DC)	I_D	100	A
Drain Current (Pulse), $PW \leq 300\mu s$	I_{DP}	400	A
Total Dissipation	P_D	121	W
Avalanche Energy, Single Pulsed	E_{AS}	125	mJ
Junction Temperature	T_j	175	°C
Storage Temperature	T_{stg}	-55 to +175	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5.Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Case	R _{θJC}	0.95	°C/W

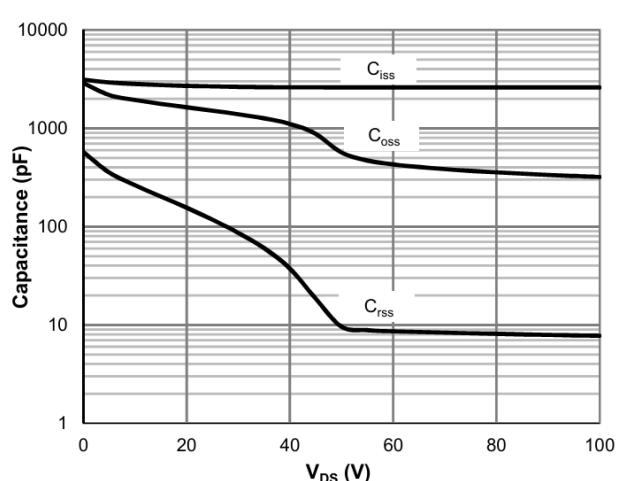
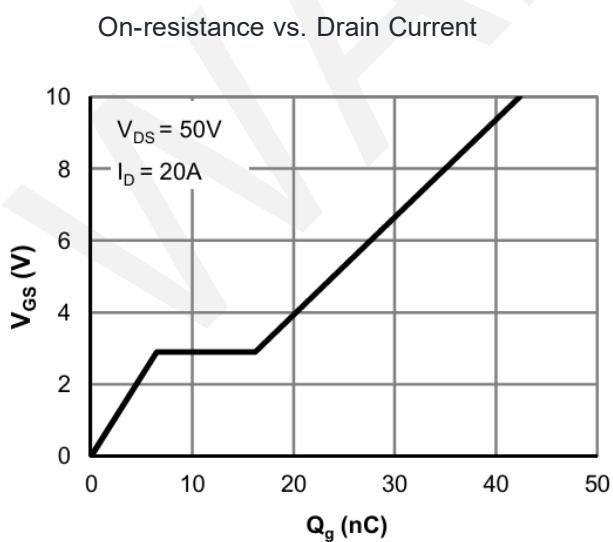
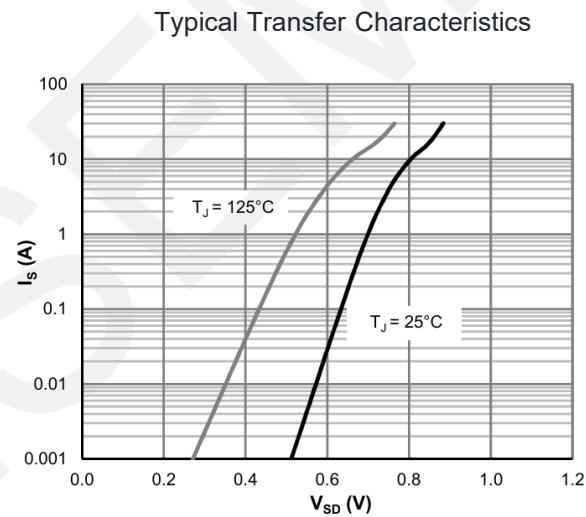
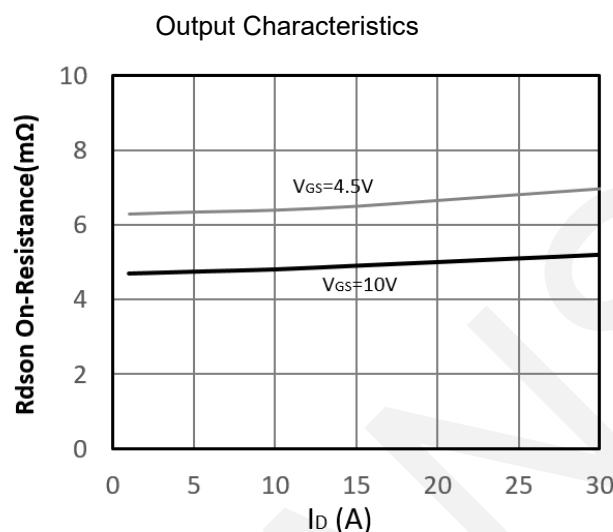
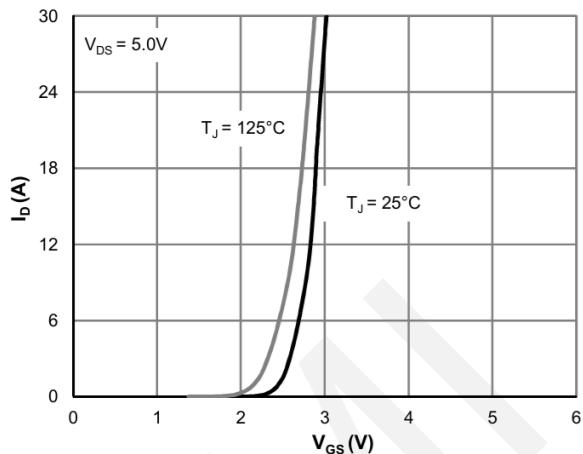
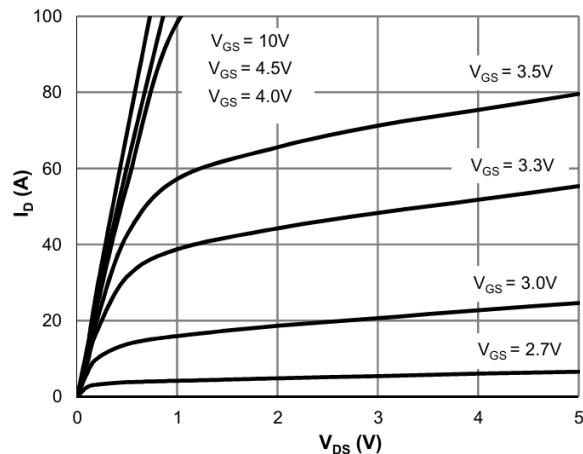
Note 2: When mounted on 1 inch square copper board $t \leq 10\text{sec}$ The value in any given application depends on the user's specific board design.

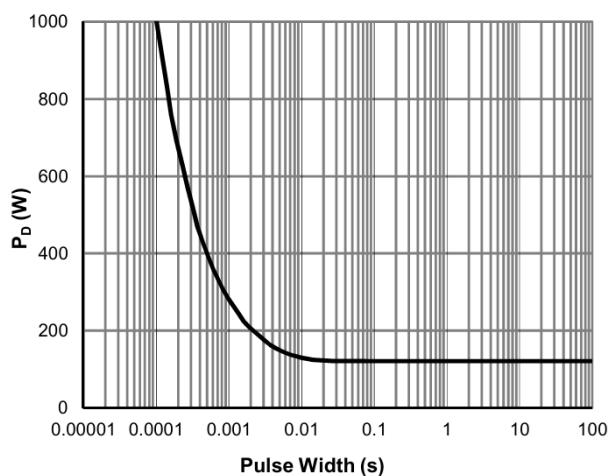
6.Electrical Characteristics at $T_a=25^\circ\text{C}$ (Note 3)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	I _D = 250μA, V _{GS} = 0V	100	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA
Gate to Source Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _{Ds} =250μA	1.2	1.7	2.2	V
Static Drain to Source On-State Resistance	R _{DS(on)}	I _D = 20A, V _{GS} = 10V	-	5.0	6.3	mΩ
		I _D = 15A, V _{GS} = 4.5V	-	6.5	8.0	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =50V, Frequency=1.0MHz	-	2604	-	pF
Output Capacitance	C _{oss}		-	567	-	pF
Reverse Transfer Capacitance	C _{rss}		-	9.6	-	pF
Turn-ON Delay Time	t _{d(on)}	V _{DS} = 50V, V _{GS} = 10V, R _L = 2.5Ω, R _{GEN} = 6Ω	-	11.3	-	ns
Rise Time	t _r		-	17.6	-	ns
Turn-OFF Delay Time	t _{d(off)}		-	46	-	ns
Fall Time	t _f		-	36	-	ns
Total Gate Charge	Q _g	V _{DS} = 50V, V _{GS} = 0 to 10V, I _D = 20A	-	42	-	nC
	Q _{gs}		-	6.5	-	nC
	Q _{gd}		-	9.7	-	nC
Diode Forward Voltage	V _{FSD}	I _S = 20A, V _{GS} = 0	-	-	1.2	V

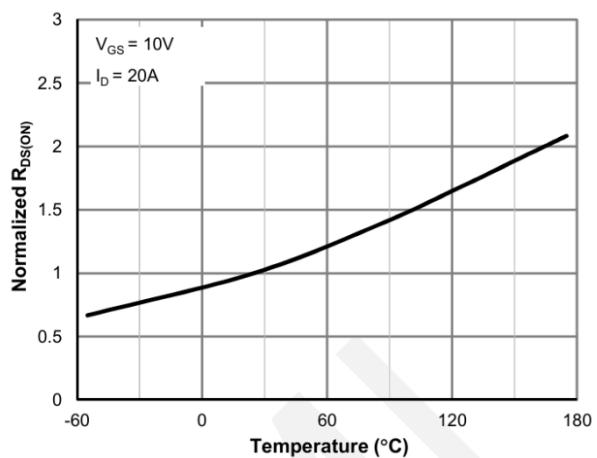
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

7.Typical electrical and thermal characteristics

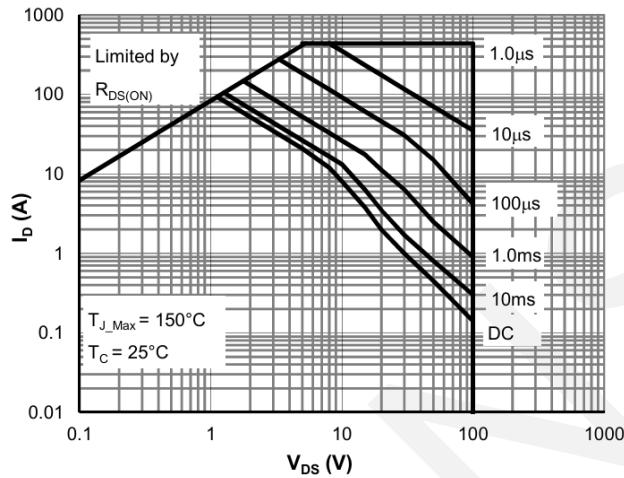




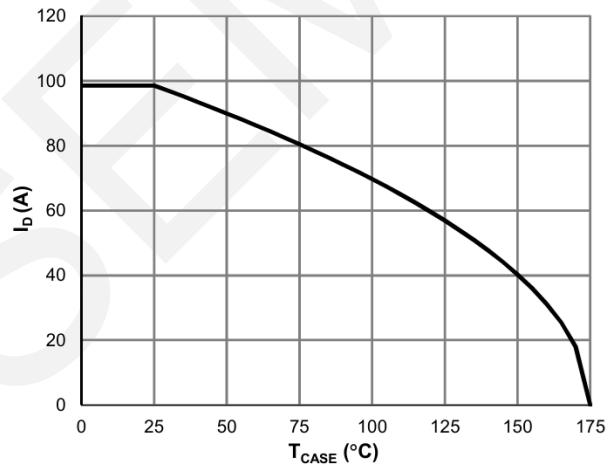
Single Pulse Power Rating, Junction-to-Case



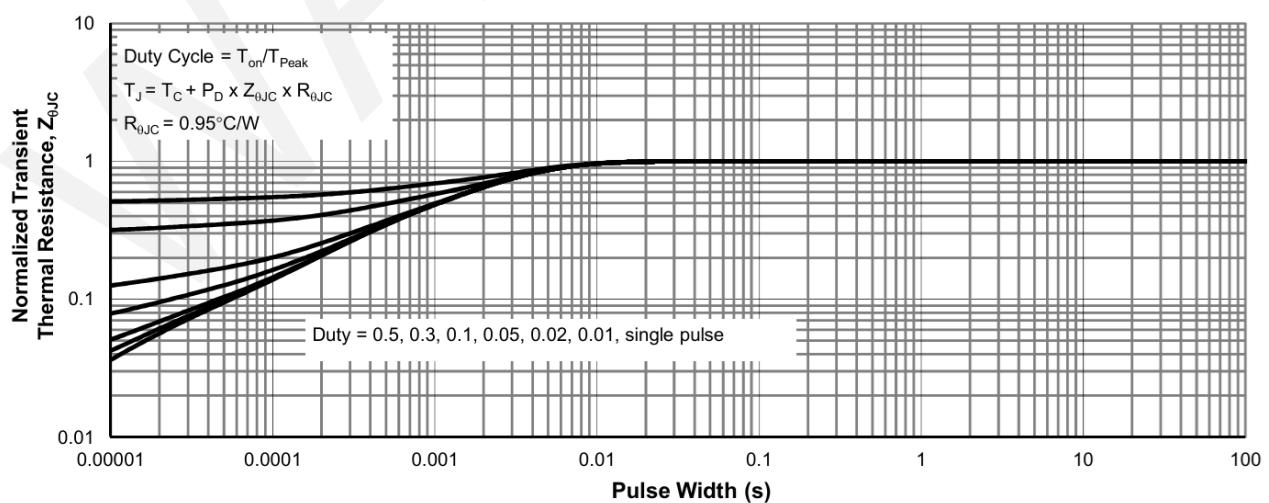
Normalized on Resistance vs. Junction Temperature



Maximum Safe Operating Area



Maximum Continuous Drain Current vs. Case Temperature

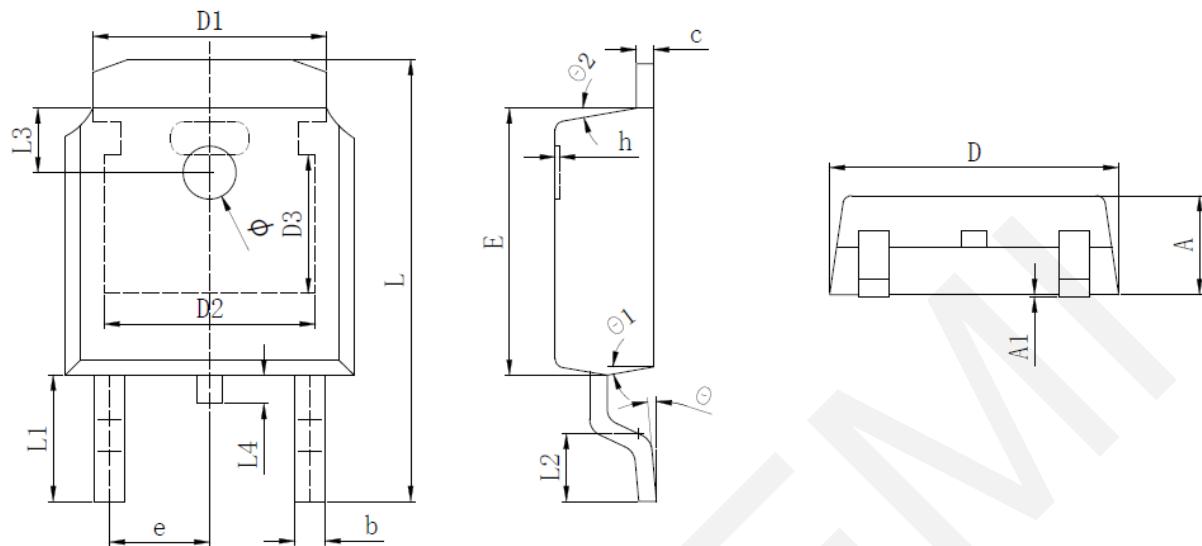


Maximum Effective Transient

Thermal Impedance, Junction-to-Case



8.Package Dimensions



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ 1	9° TYP		
θ 2	9° TYP		

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