



**WANSEMI**  
万芯半导体

**WP4953A**

# **Enhancement Mode N-Channel Power MOSFET**

**SOP8/PMOS/-20V/ $\pm 12$ V/-0.7V/-5A/46m $\Omega$**

**Rev0.6**

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## -20V,46mΩ, -5A, P-Channel MOSFET

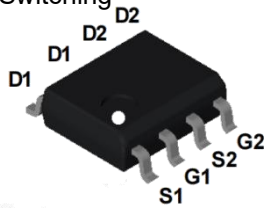
### 1.Features

- ◆ Fast switching speed
- ◆ High power and current handling capability

$V_{DS}$	$R_{DS(on)}$ Typ.	$I_D$
-20V	46mΩ @ -4.5V	-5A
	57mΩ @ -2.5V	

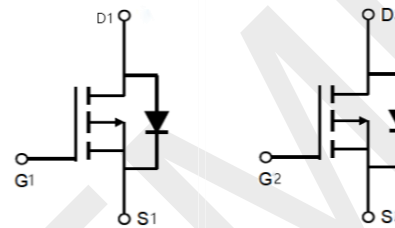
### 2.Applications

- ◆ Power Management
- ◆ Load Switching



SOP8

Pin Description



Schematic Diagram

### 3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP4953A	4953	SOP8	4,000	48,000

### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	$V_{DSS}$	-20	V
Gate to Source Voltage	$V_{GSS}$	±12	V
Drain Current (DC)	$I_D$	-5	A
Drain Current (Pulse), $PW \leq 300\mu s$	$I_{DP}$	-15	A
Total Dissipation	$P_D$	2	W
Junction Temperature	$T_j$	175	°C
Storage Temperature	$T_{stg}$	-55 to +175	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### 5.Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	78	°C/W

Note 2: When mounted on 1 inch square copper board  $t \leq 10$ sec The value in any given application depends on the user's specific board design.

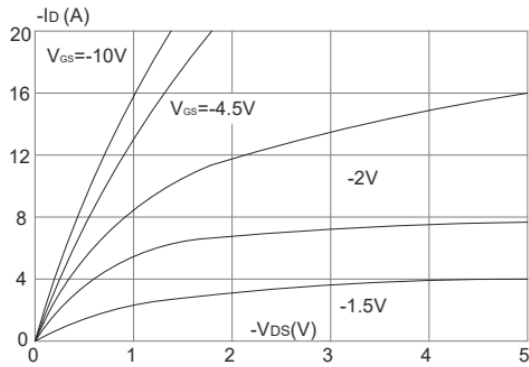
**6. Electrical Characteristics at Ta=25°C (Note 3)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -250\mu A, V_{GS} = 0V$	-20	-	-	V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -20V, V_{GS} = 0V$	-	-	-1	$\mu A$
Gate to Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 12V, V_{DS} = 0V$	-	-	$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-0.5	-0.7	-1.0	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = -3A, V_{GS} = -4.5V$	-	46	55	m $\Omega$
		$I_D = -2A, V_{GS} = -2.5V$	-	57	70	m $\Omega$
Input Capacitance	$C_{iss}$	$V_{GS}=0V,$ $V_{DS}=-10V,$ Frequency=1.0MHz	-	520	-	pF
Output Capacitance	$C_{oss}$		-	65	-	pF
Reverse Transfer Capacitance	$C_{rss}$		-	53	-	pF
Turn-ON Delay Time	$t_{d(on)}$		-	11	-	ns
Rise Time	$t_r$	$V_{DS} = -15V, I_D = -1A,$ $R_G = 6\Omega, V_{GS} = -10V$	-	52	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	16	-	ns
Fall Time	$t_f$		-	10	-	ns
Total Gate Charge	$Q_g$		$V_{DS} = -15V,$ $V_{GS} = -5V,$ $I_D = -5A$	-	4.1	-
	$Q_{gs}$	-		0.8	-	nC
	$Q_{gd}$	-		1.1	-	nC
Diode Forward Voltage	$V_{FSD}$	$I_S = -2.6A, V_{GS} = 0$	-	-	-1.3	V

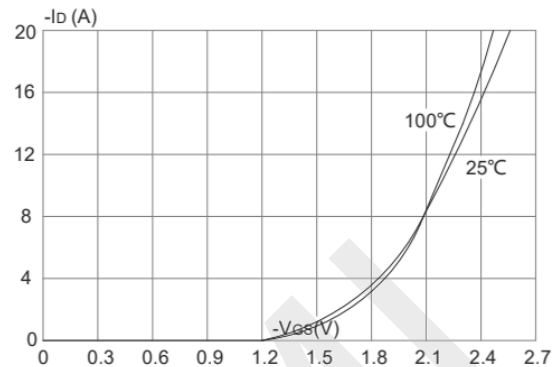
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



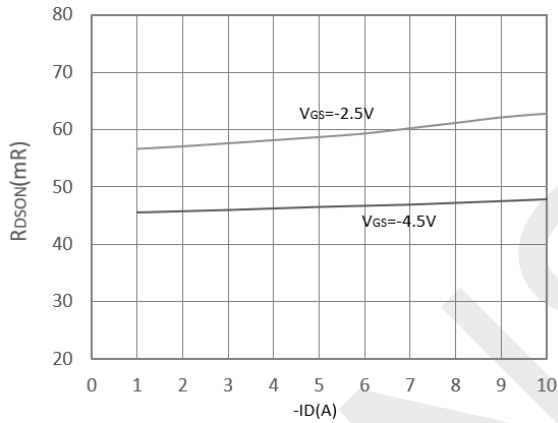
### 7. Typical Electrical and Thermal Characteristics



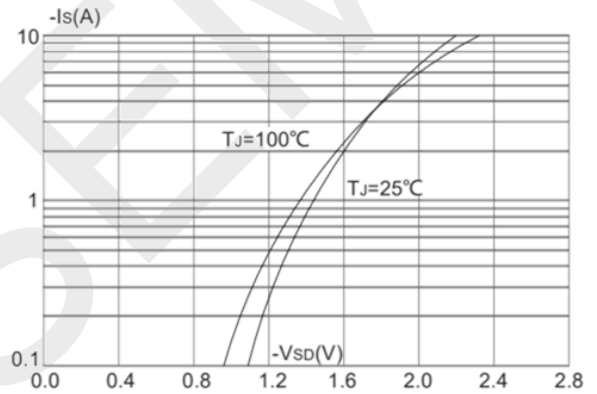
Output Characteristics



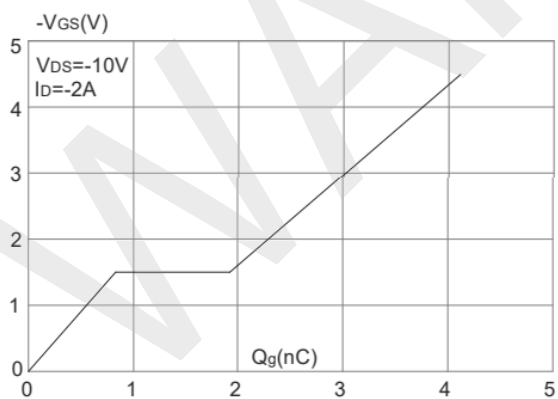
Transfer Characteristics



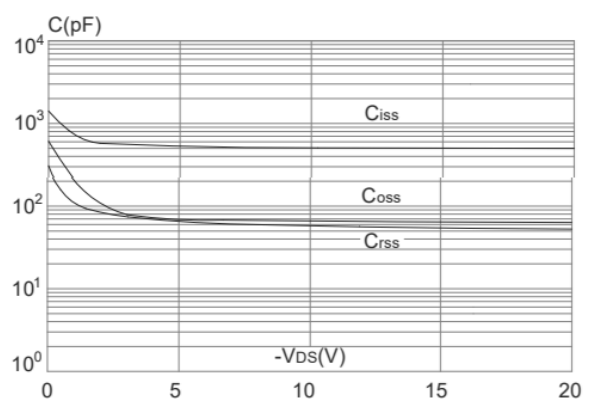
On-resistance vs. Drain Current



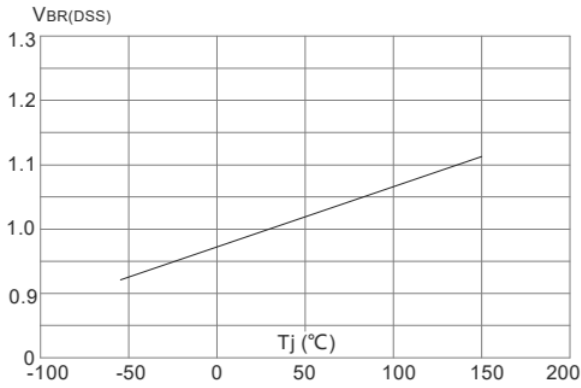
Body Diode Characteristics



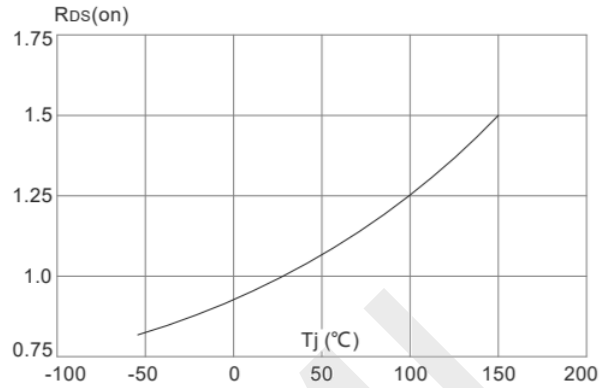
Gate Charge Characteristics



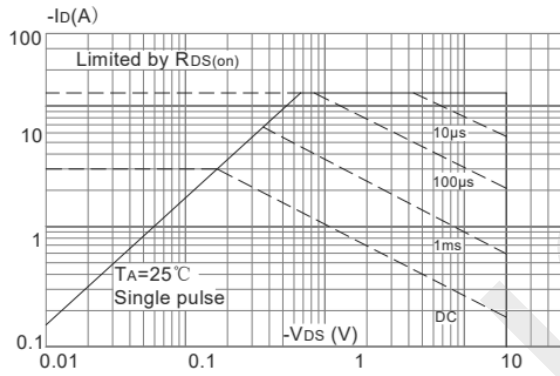
Capacitance Characteristics



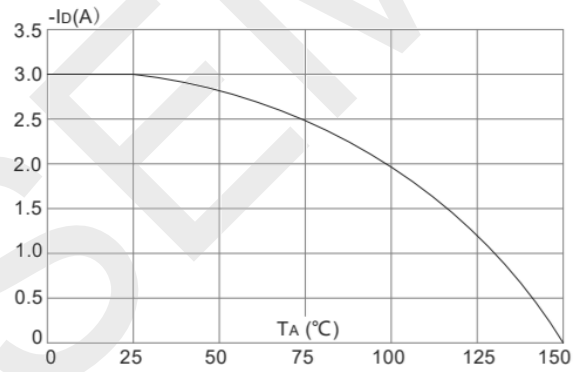
**Normalized Breakdown Voltage vs. Junction Temperature**



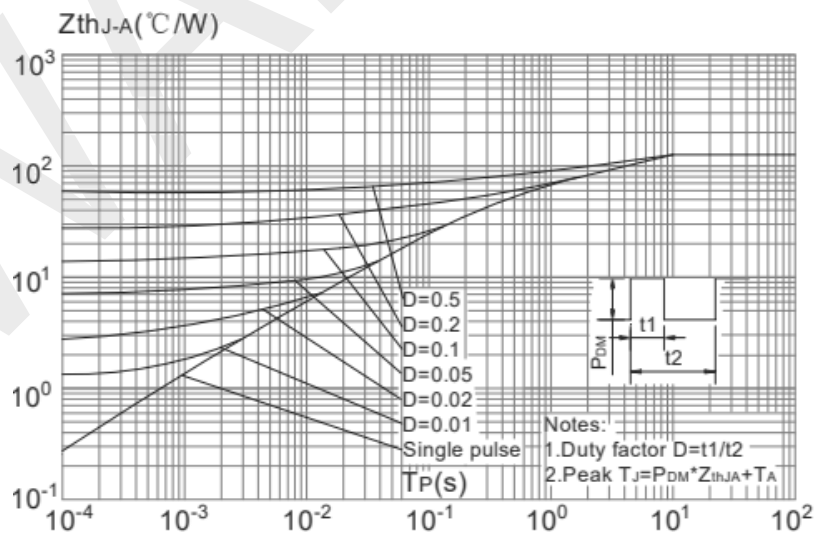
**Normalized on Resistance vs. Junction Temperature**



**Maximum Safe Operating Area**



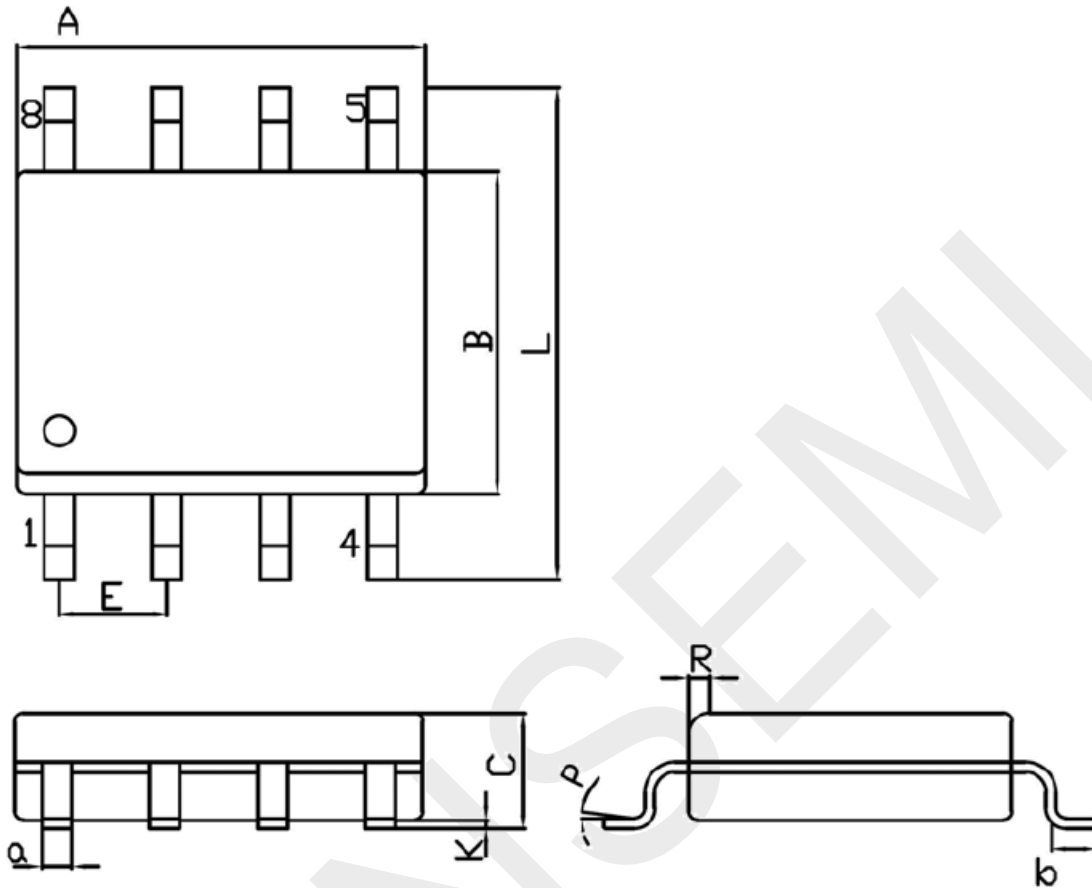
**Maximum Continuous Drain Current vs. Ambient Temperature**



**Maximum Effective Transient Thermal Impedance, Junction-to-Ambient**



## 8.Package Dimensions



Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
A	4.70	5.10	C	1.35	1.75
B	3.70	4.10	a	0.35	0.49
L	5.80	6.20	R	0.30	0.60
E	1.27BSC		P	0°	7°
K	0.12	0.22	b	0.40	1.25

## **8.Important Notice**

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