



**WANSEMI**  
万芯半导体

**WP100P30**

# **Enhancement Mode P-Channel Power MOSFET**

TO-252/PMOS/-100V/ $\pm 20V$ /-1.8V/-19A/75m $\Omega$

Rev1.3

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## -100V, 75mΩ, -19A, Single P-Channel

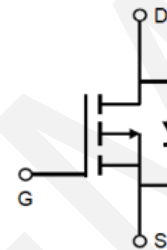
### 1.Features

- ◆ Super Low Gate Charge
- ◆ 100% EAS Guaranteed
- ◆ High Power and current handing capability
- ◆ Lead free product is acquired

$V_{DS}$	$R_{DS(on)}$ Typ.	$I_D$ Max.
-100V	75mΩ @ -10V	-19A
	85mΩ @ -4.5V	

### 2.Applications

- ◆ Load Switching



Schematic Diagram

### 3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP100P30	100P30	TO-252	2,500	25,000

### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	$V_{DSS}$	-100	V
Gate to Source Voltage	$V_{GSS}$	±20	V
Drain Current (DC)	$I_D$	-19	A
Drain Current (Pulse), $PW \leq 300\mu s$	$I_{DP}$	-76	A
Total Dissipation	$P_D$	79	W
Avalanche Energy, Single Pulsed	$E_{AS}$	156	mJ
Junction Temperature	$T_j$	175	°C
Storage Temperature	$T_{stg}$	-55 to +175	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

**5. Thermal Resistance Ratings (Note 2)**

Parameter	Symbol	Value	Unit
Junction to case	$R_{\theta JC}$	1.9	$^{\circ}C/W$

Note 2: When mounted on 1 inch square copper board  $t \leq 10$ sec The value in any given application depends on the user's specific board design.

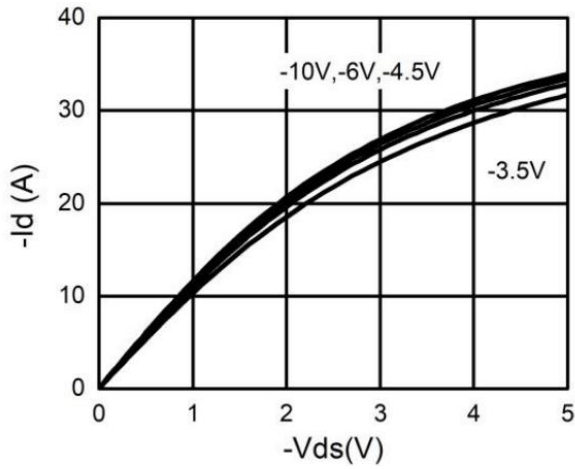
**6. Electrical Characteristics at  $T_a=25^{\circ}C$  (Note 3)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -250\mu A, V_{GS} = 0V$	-100	-121	-	V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -100V, V_{GS} = 0V$	-	-	-1	$\mu A$
Gate to Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-1.0	-1.8	-2.5	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = -10A, V_{GS} = -10V$	-	75	90	m $\Omega$
		$I_D = -8A, V_{GS} = -4.5V$	-	85	117	m $\Omega$
Input Capacitance	$C_{iss}$	$V_{GS}=0V,$	-	3700	-	pF
Output Capacitance	$C_{oss}$	$V_{DS}=-50V,$	-	90	-	pF
Reverse Transfer Capacitance	$C_{rss}$	Frequency=1.0MHz	-	32	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DS} = -50V, R_L = 5\Omega$ $V_{GS} = -10V, R_G = 9.1\Omega$	-	6	-	ns
Rise Time	$t_r$		-	29	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	17	-	ns
Fall Time	$t_f$		-	24	-	ns
Total Gate Charge	$Q_g$	$V_{DS} = -50V,$	-	72	-	nC
	$Q_{gs}$	$V_{GS} = -10V,$	-	8.4	-	nC
	$Q_{gd}$	$I_D = -10A$	-	17.3	-	nC
Diode Forward Voltage	$V_{FSD}$	$I_S = -10A, V_{GS} = 0V$	-	-	-1.2	V

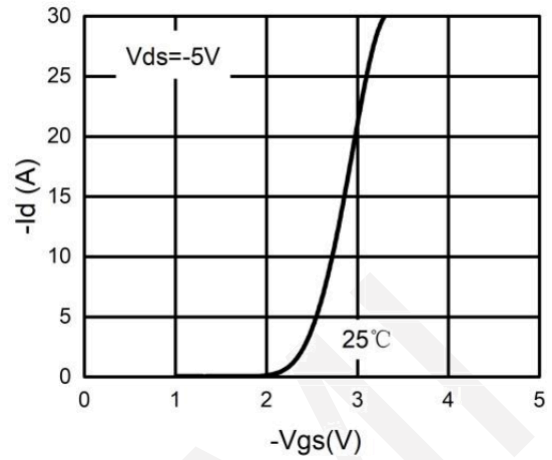
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



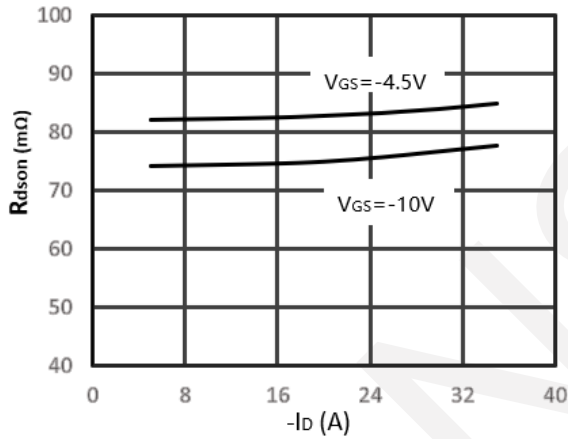
### 7. Typical electrical and thermal characteristics



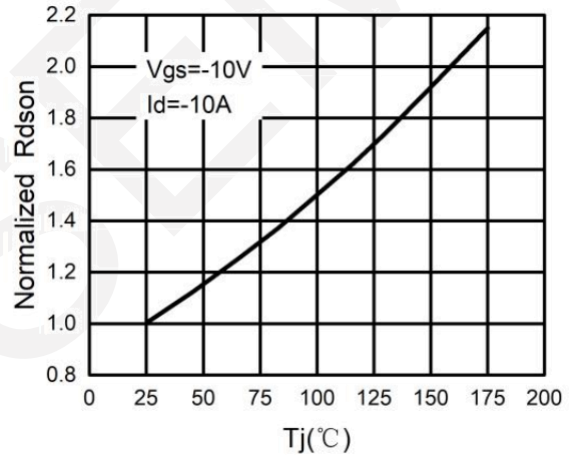
Output Characteristics



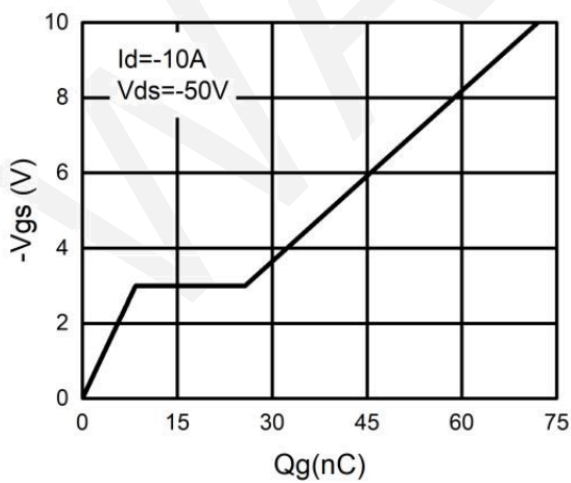
Transfer Characteristics



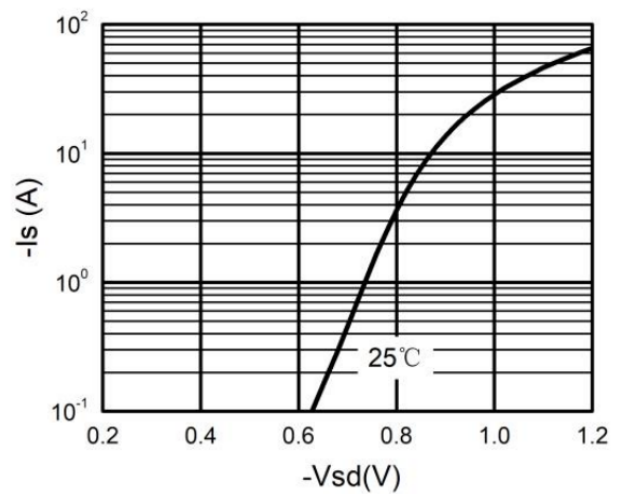
$R_{dson}$ -Drain Current



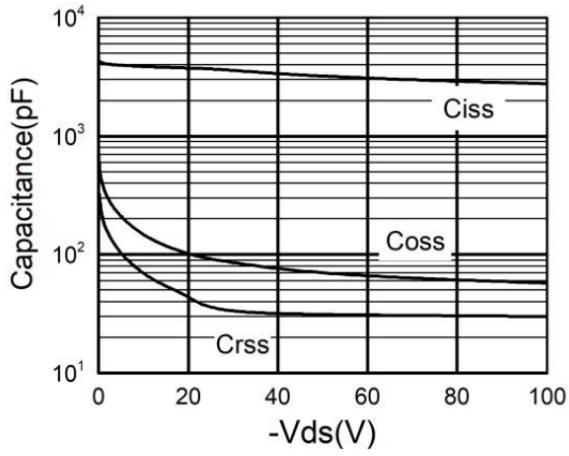
$R_{dson}$ -Junction Temperature



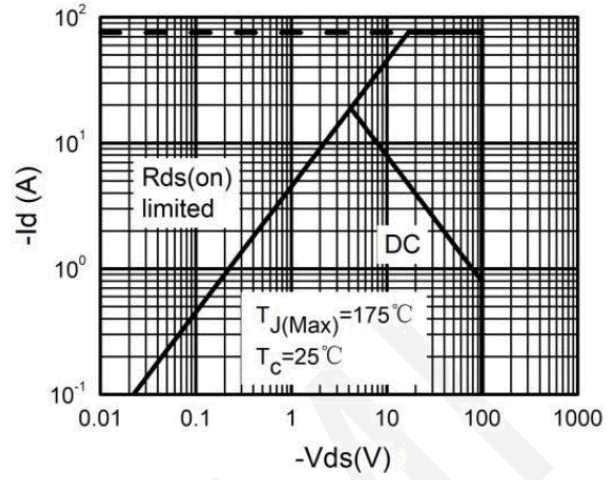
Gate Charge



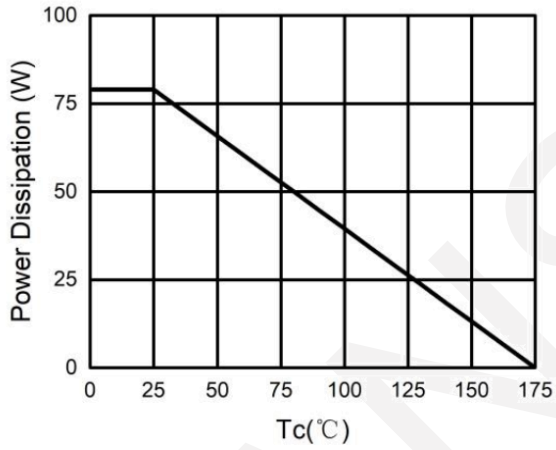
Source-Drain Diode Forward



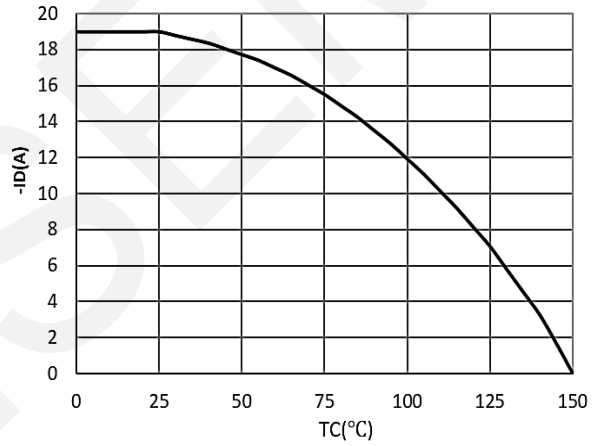
Capacitance vs Vds



Safe Operation Area



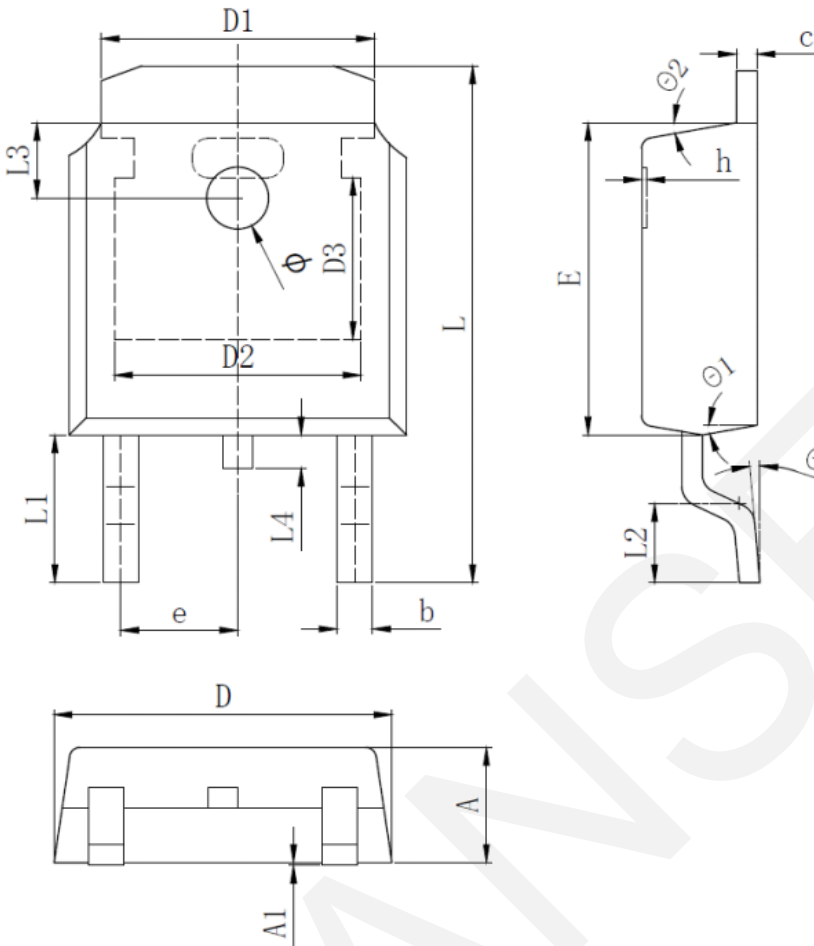
Power De-rating



ID Current Derating



**8.Package Dimensions**



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
e (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
phi	1.100	1.200	1.300
theta	0°		
theta 1	9° TYP		
theta 2	9° TYP		

## 9. Important Notice

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