



WANSEMI
万芯半导体

WX090P02SM

Enhancement Mode P-Channel Power MOSFET

SOT323/PMOS/-20V/ $\pm 12V$ /-0.7V/-2.2A/90m Ω

Rev0.1

-20V, 90mΩ, -2.2A, P-Channel MOSFET

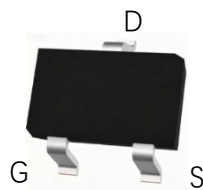
1.Features

- ◆ Advanced Trench Technology
- ◆ Surface mount package

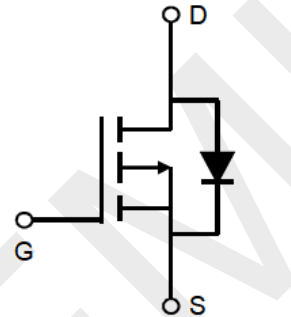
2.Applications

- ◆ Power Management
- ◆ Load Switching

V _{DS}	R _{DS(on)} Typ.	I _D
-20V	90mΩ @ -4.5V	-2.2A
	120mΩ @ -2.5V	



SOT323
Pin Description



Schematic Diagram

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX090P02SM	09P2 \times	SOT323	3,000	180,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	-20	V
Gate to Source Voltage	V _{GSS}	±12	V
Drain Current (DC)	I _D	-2.2	A
Drain Current (Pulse), PW≤300μs	I _{DP}	-8.8	A
Total Dissipation	P _D	1.0	W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5.Thermal Resistance Ratings(Note 2)

Parameter	Symbol	Value	Unit
Junction to Ambient	R _{θJA}	120	°C/W

Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

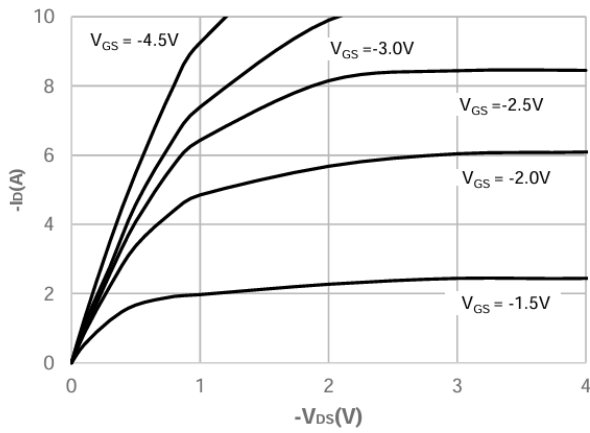
5. Electrical Characteristics at Ta=25°C (Note 2)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -250\mu A, V_{GS} = 0V$	-20	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20V, V_{GS} = 0V$	-	-	-1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-0.4	-0.7	-1.2	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = -2A, V_{GS} = -4.5V$	-	90	110	m Ω
		$I_D = -1A, V_{GS} = -2.5V$	-	120	160	m Ω
Input Capacitance	C_{iss}	$V_{GS}=0V,$	-	234	-	pF
Output Capacitance	C_{oss}	$V_{DS}=-10V,$	-	34	-	pF
Reverse Transfer Capacitance	C_{rss}	Frequency=1.0MHz	-	24	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DD} = -10V, I_D = -2A,$ $R_G = 3\Omega, V_{GS} = -4.5V$	-	5	-	ns
Rise Time	t_r		-	18	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	79	-	ns
Fall Time	t_f		-	42	-	ns
Total Gate Charge	Q_g		$V_{DS} = -10V,$	-	2.4	-
	Q_{gs}	$V_{GS} = 0 \text{ to } -4.5V,$	-	0.6	-	nC
	Q_{gd}	$I_D = -2A$	-	0.4	-	nC
Diode Forward Voltage	V_{FSD}	$I_{SD} = -2A, V_{GS} = 0$	-0.5	-	-1.2	V

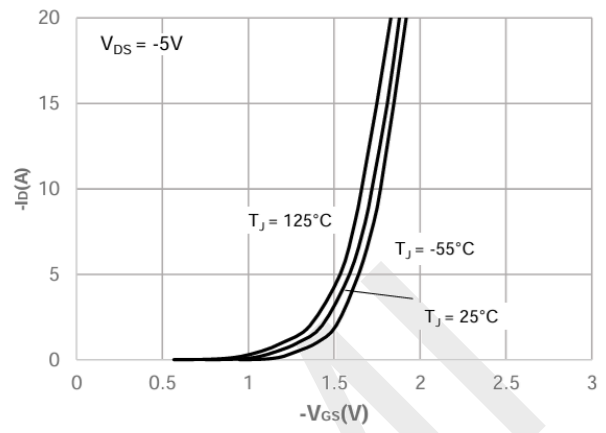
Note 2: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



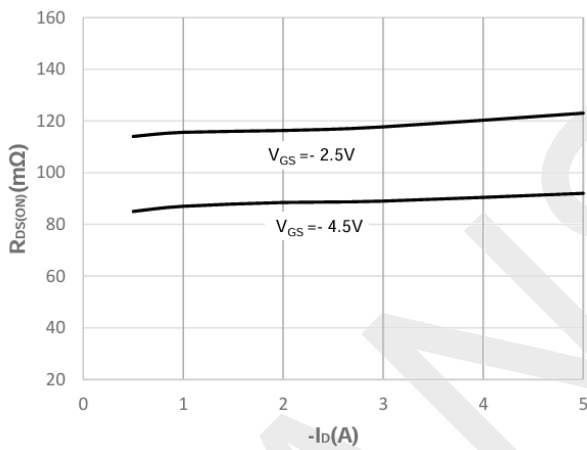
6. Typical Electrical and Thermal Characteristics



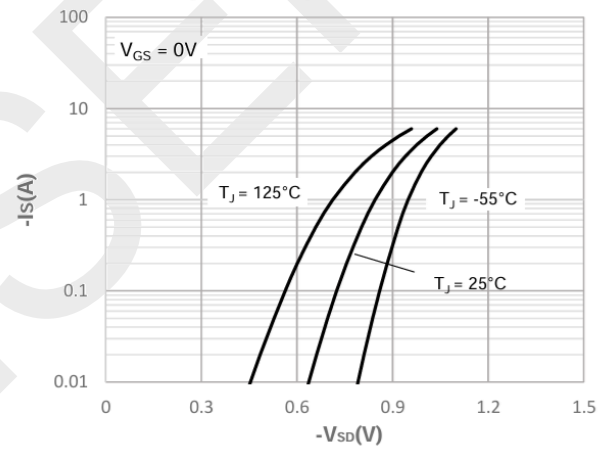
Output Characteristics



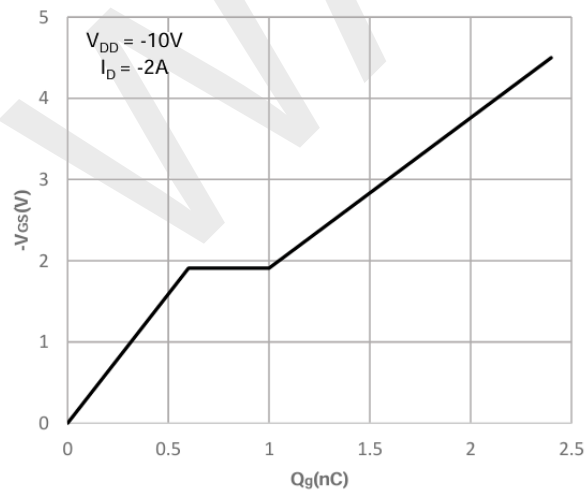
Typical Transfer Characteristics



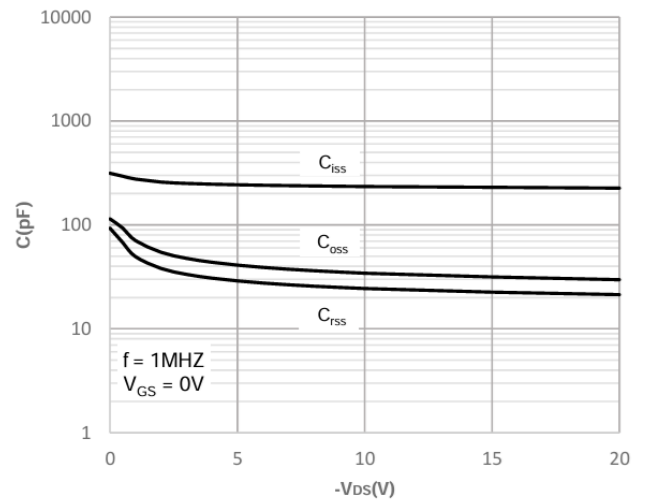
On-resistance vs. Drain Current



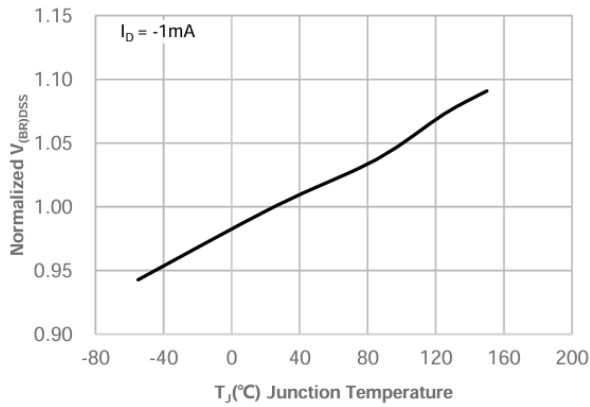
Body Diode Characteristics



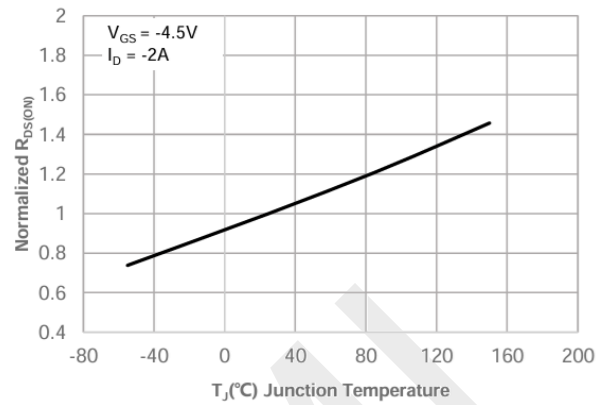
Gate Charge Characteristics



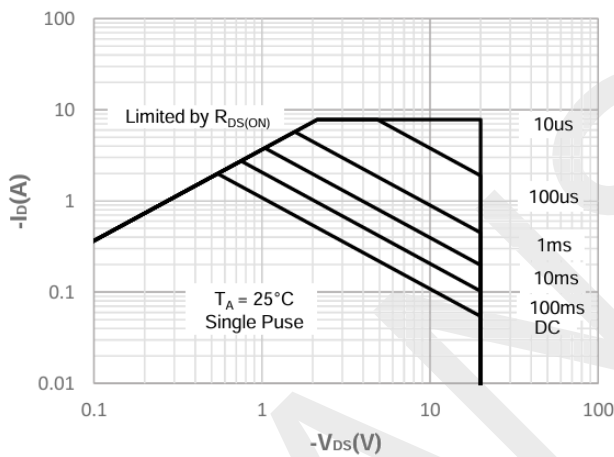
Capacitance Characteristics



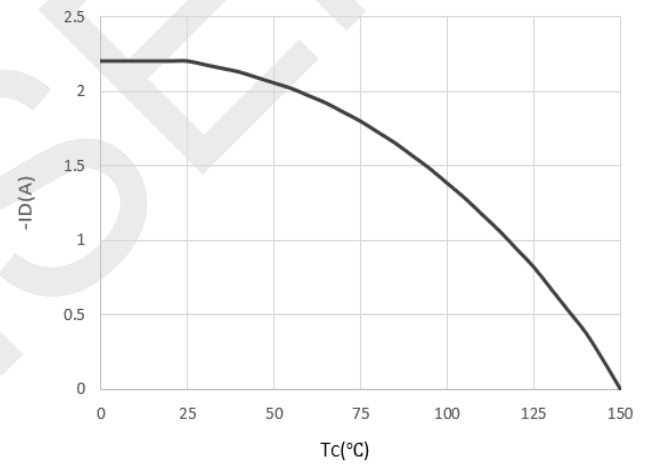
Normalized Breakdown Voltage vs.
Junction Temperature



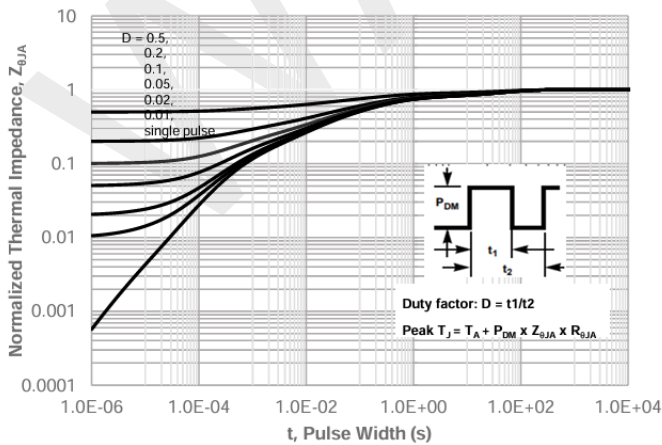
Normalized on Resistance vs.
Junction Temperature



Maximum Safe Operating Area



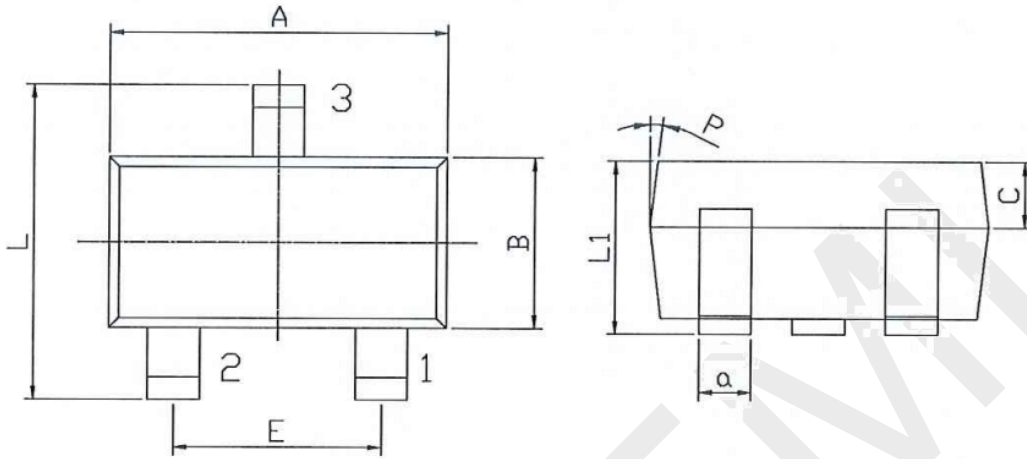
Maximum Continuous Drain Current vs.
Case Temperature



Normalized Maximum Transient
Thermal Impedance



7.Package Dimensions



Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
A	1.95	2.35	C	0.30	0.50
L	2.00	2.2	L1	0.85	1.15
E	1.20	1.40	a	0.20	0.40
B	1.15	1.35	P	7°	

8.Important Notice

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