



WANSEMI
万芯半导体

WP30P50P3

Enhancement Mode P-Channel Power MOSFET

PDFN3x3/PMOS/-30V/ $\pm 20V$ /-1.6V/-35A/7.6m Ω

Rev1.3

-30V, 7.6mΩ, -35A, Single P-Channel

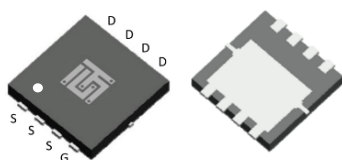
1.Features

- ◆ Advanced Trench Technology
- ◆ Surface mount package
- ◆ 100% RG Tested
- ◆ 100% UIS Tested

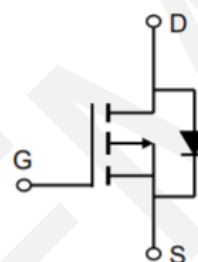
V_{DS}	$R_{DS(on)}$ Typ.	I_D Max.
-30V	7.6mΩ @ -10V	-35A
	10.2mΩ @ -4.5V	

2.Applications

- ◆ Power Management
- ◆ Load Switching



PDFN3X3
Pin Description



Schematic Diagram

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP30P50P3	30P50	PDFN3X3	5,000	50,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	-30	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I_D	-35	A
Drain Current (Pulse), $PW \leq 300\mu s$	I_{DP}	-140	A
Total Dissipation	P_D	5	W
Avalanche Energy, Single Pulsed	E_{AS}	126.5	mJ
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5. Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to ambient	$R_{\theta JA}$	62	$^{\circ}\text{C/W}$

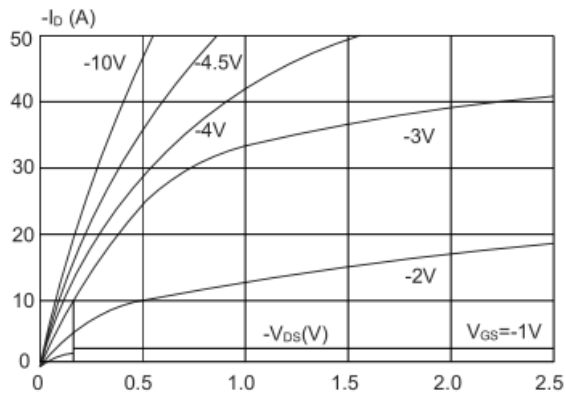
Note 2: When mounted on 1 inch square copper board $t \leq 10\text{sec}$ The value in any given application depends on the user's specific board design.

6. Electrical Characteristics at $T_a=25^{\circ}\text{C}$ (Note 3)

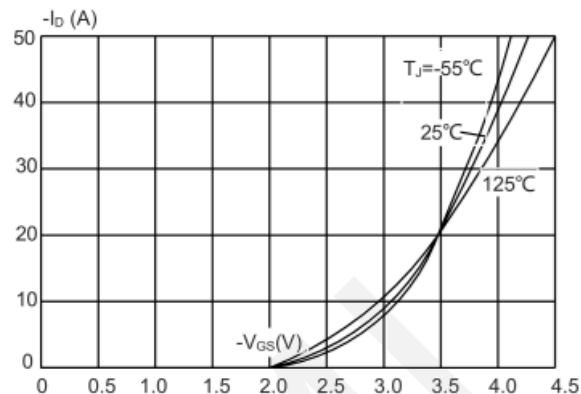
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -250\mu\text{A}$, $V_{GS} = 0\text{V}$	-30	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30\text{V}$, $V_{GS} = 0\text{V}$	-	-	-1	μA
Gate to Source Leakage Current	I_{GSS1}	$V_{GS} = \pm 20\text{V}$, $V_{SS} = 0\text{V}$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{DS}=-250\mu\text{A}$	-1.0	-1.6	-2.5	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = -12\text{A}$, $V_{GS} = -10\text{V}$	-	7.6	9.0	$\text{m}\Omega$
		$I_D = -8\text{A}$, $V_{GS} = -4.5\text{V}$	-	10.2	15	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}$, $V_{DS}=-15\text{V}$, Frequency=1.0MHz	-	2251	-	pF
Output Capacitance	C_{oss}		-	334	-	pF
Reverse Transfer Capacitance	C_{rss}		-	248	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DD} = -15\text{V}$, $I_D = -10\text{A}$, $V_{GS} = -10\text{V}$, $R_G = 2.5\Omega$	-	9	-	ns
Rise Time	t_r		-	13	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	48	-	ns
Fall Time	t_f		-	20	-	ns
Total Gate Charge	Q_g	$V_{DS} = -15\text{V}$, $V_{GS} = -10\text{V}$, $I_D = -50\text{A}$	-	22	-	nC
	Q_{gs}		-	1	-	nC
	Q_{gd}		-	1.8	-	nC
Diode Forward Voltage	V_{FSD}	$I_S = -12\text{A}$, $V_{GS} = 0$	-	-0.8	-1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

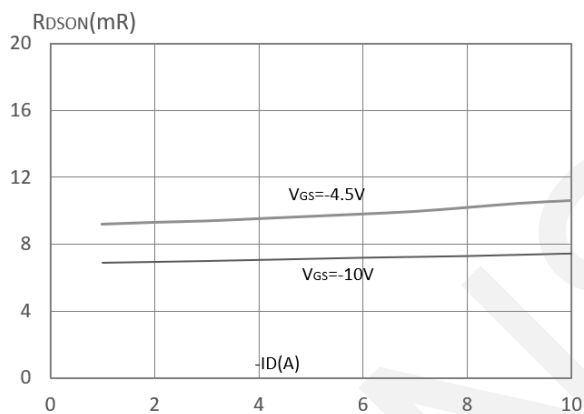
7. Typical electrical and thermal characteristics



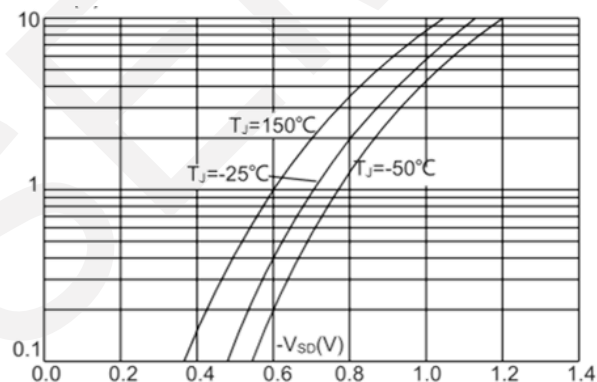
Output Characteristics



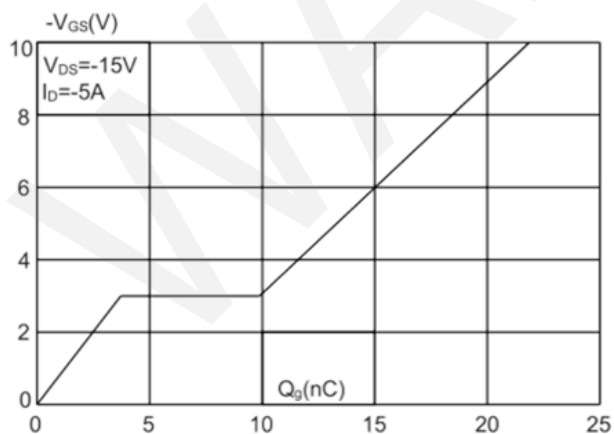
Typical Transfer Characteristics



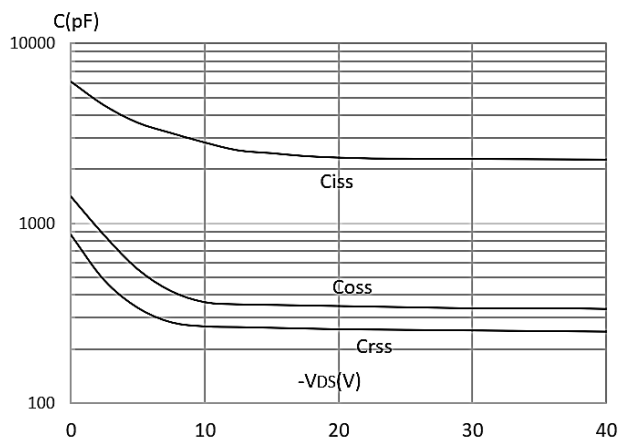
On-resistance vs. Drain Current



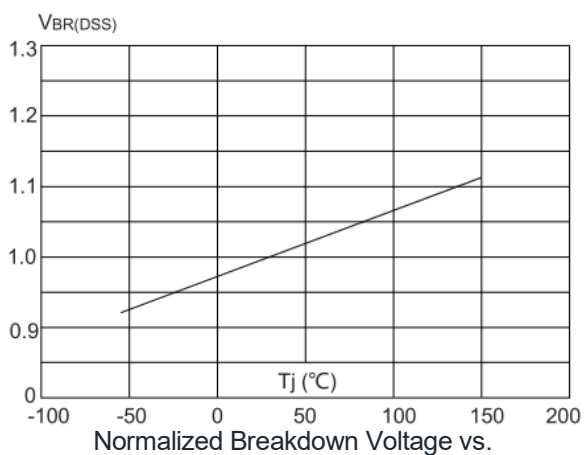
Body Diode Characteristics



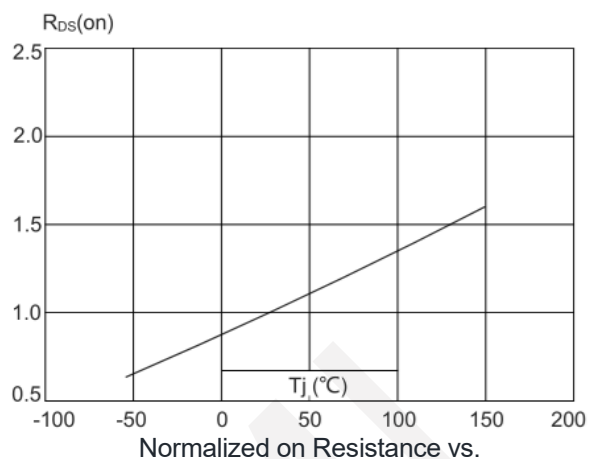
Gate Charge Characteristics



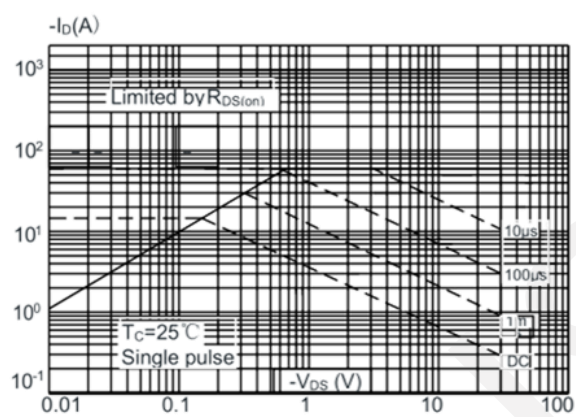
Capacitance Characteristics



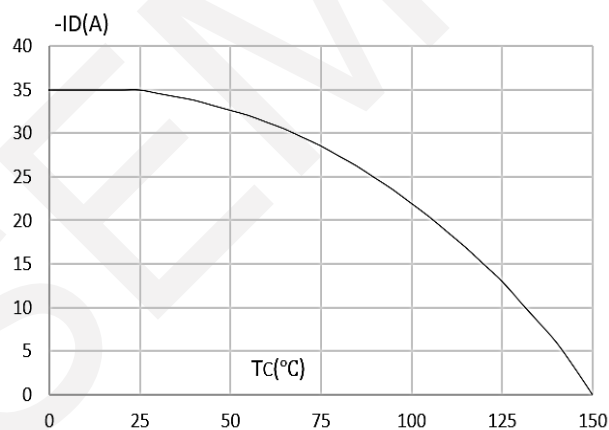
Junction Temperature



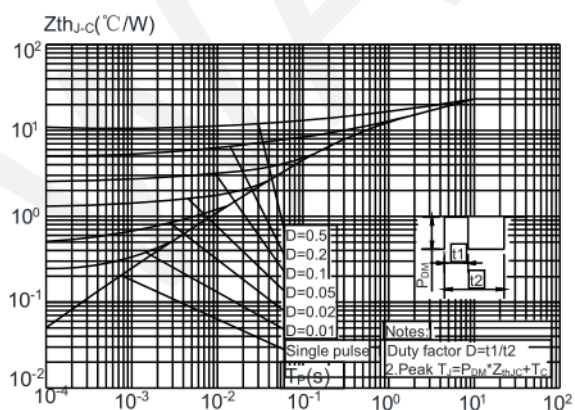
Junction Temperature



Maximum Safe Operating Area

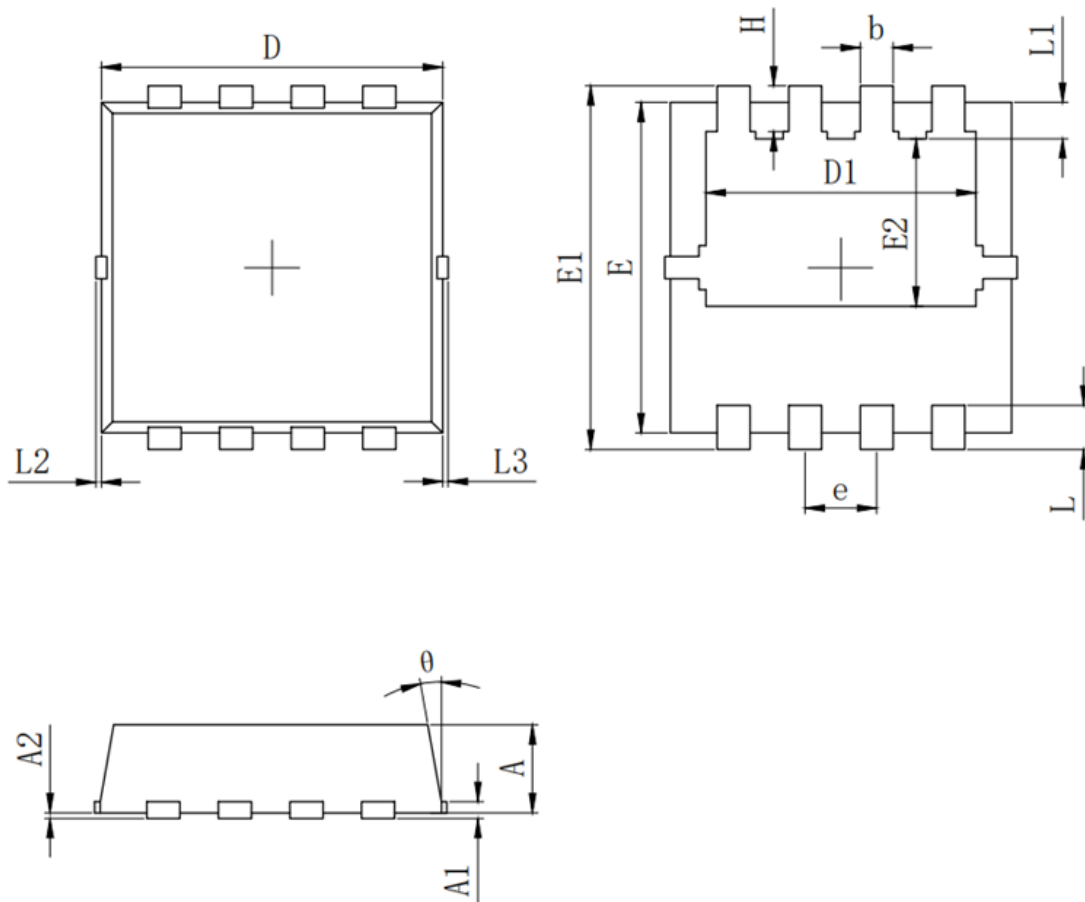


Maximum Continuous Drain Current vs.
Case Temperature



Normalized Maximum
Transient Thermal Impedance

8.Package Dimensions



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.700	0.800	0.900
A1	0.152 REF.		
A2	0~0.05		
D	3.000	3.100	3.200
D1	2.300	2.450	2.600
E	2.900	3.000	3.100
E1	3.150	3.300	3.450
E2	1.320	1.520	1.720
b	0.200	0.300	0.400
e	0.550	0.650	0.750
L	0.300	0.400	0.500
L1	0.180	0.330	0.480
L2	0~0.100		
L3	0~0.100		
H	0.315	0.415	0.515
θ	8°	10°	12°

9. Important Notice

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