



WANSEMI
万芯半导体

WP8205KT8

Enhancement Mode N-Channel Power MOSFET

TSSOP8/NMOS/20V/ ± 12 V/0.8V/5A/24m Ω

Rev0.7

20V, 24mΩ, 5A, N-Channel Enhancement Mode Power MOSFET

1.Features

- ◆ High Power and current handing capability
- ◆ Lead free product is acquired
- ◆ Surface Mount Package

V _{DS} Typ	R _{DS(on)} Typ.	I _D Max.
20V	24mΩ @ 4.5V	5A
	34mΩ @ 2.5V	

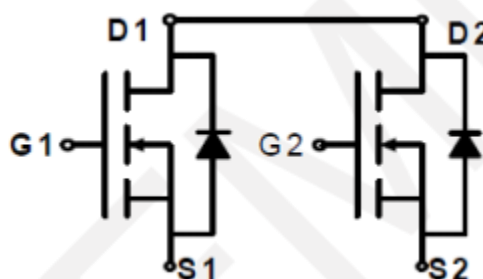
2.Applications

- ◆ Battery protection
- ◆ Load Switch
- ◆ Power management



TSSOP8

Pin Description



Schematic Diagram

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Tube	PCS/CTN.
WP8205KT8	8205A	TSSOP8	5,000	80,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	20	V
Gate to Source Voltage	V _{GSS}	±12	V
Drain Current- Continuous	I _D	5	A
Drain Current (Pulse)	I _{DM}	20	A
Maximum Power Dissipation	P _D	1.7	W
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5.Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient (Note 2)	R _{θJA}	75	°C/W

Note 2 : When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

6. Electrical Characteristics at Ta=25°C (Note 3)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250\mu A, V_{GS} = 0V$	20	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20V, V_{GS} = 0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{DS} = 250\mu A$	0.5	0.8	1.0	V
Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = 5A, V_{GS} = 4.5V$	-	24	30	m Ω
		$I_D = 2A, V_{GS} = 2.5V$	-	34	45	m Ω
Diode Forward Voltage	V_{SD}	$I_S = 5A, V_{GS} = 0$	-	-	1.0	V
Input Capacitance	C_{iss}	$V_{GS} = 0V,$	-	455	-	pF
Output Capacitance	C_{oss}	$V_{DS} = 10V,$	-	64	-	pF
Reverse Transfer Capacitance	C_{rss}	Frequency=1.0MHz	-	55	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DS} = 10V,$ $V_{GS} = 4.5V,$ $R_{GEN} = 3\Omega,$ $I_D = 2A$	-	4	-	ns
Turn-ON Rise Time	t_r		-	10	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	65	-	ns
Turn-ON Fall Time	t_f		-	33	-	ns
Total Gate Charge	Q_g	$V_{DS} = 10V,$	-	6	-	nC
Gate- Source Charge	Q_{gs}	$V_{GS} = 0 \text{ to } 4.5V,$	-	1	-	nC
Gate- Drain Charge	Q_{gd}	$I_D = 2A$	-	1.5	-	nC

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

7. Typical electrical and thermal characteristics

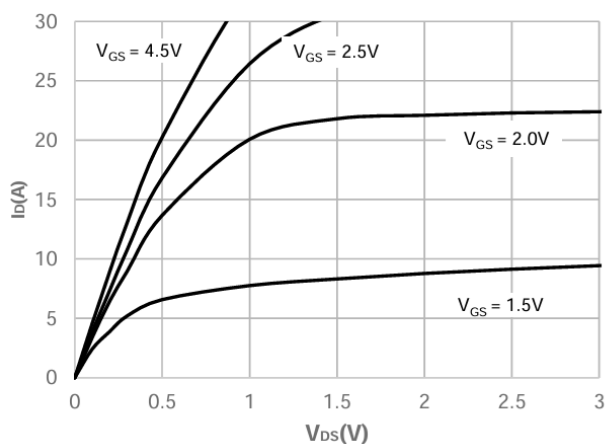


Figure 1 On-Region Characteristics

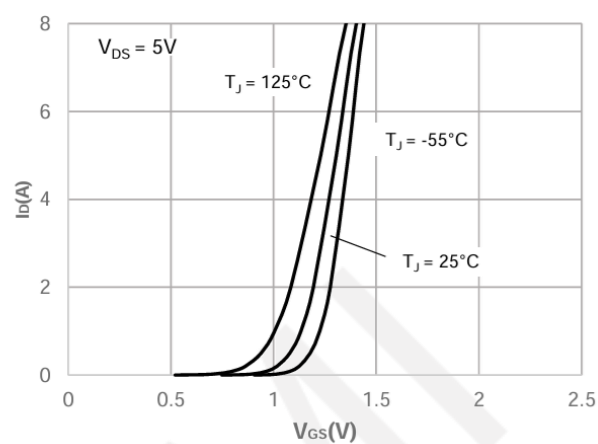


Figure 2 Transfer Characteristics

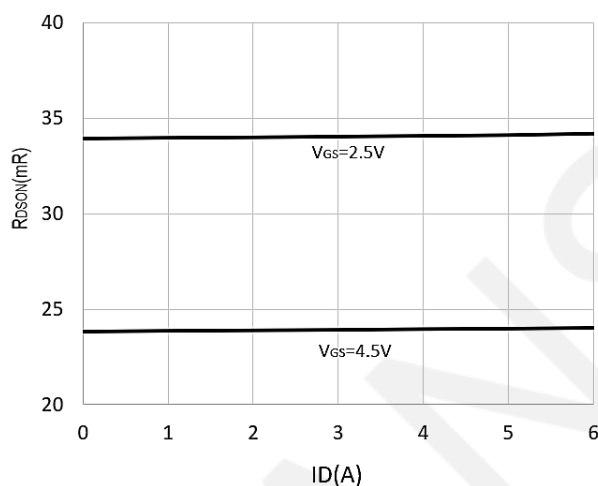


Figure 3 On-Resistance vs. Drain Current and Gate Voltage

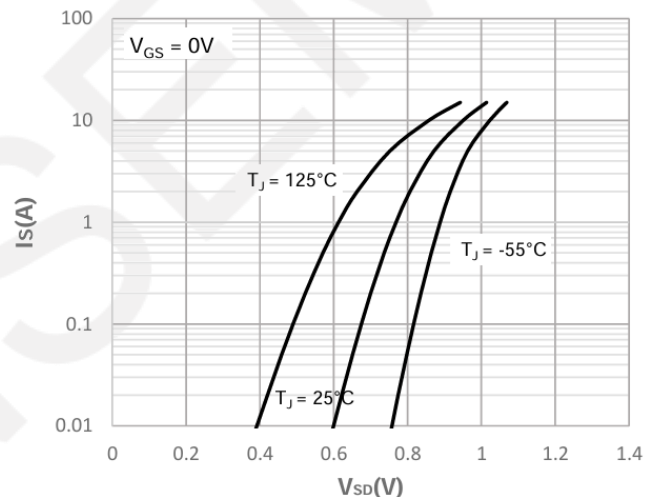


Figure 4 Body Diode Characteristics

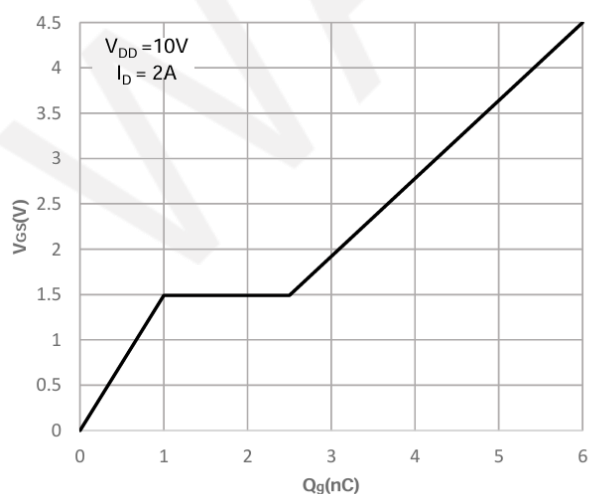


Figure 5 Gate Charge Characteristics

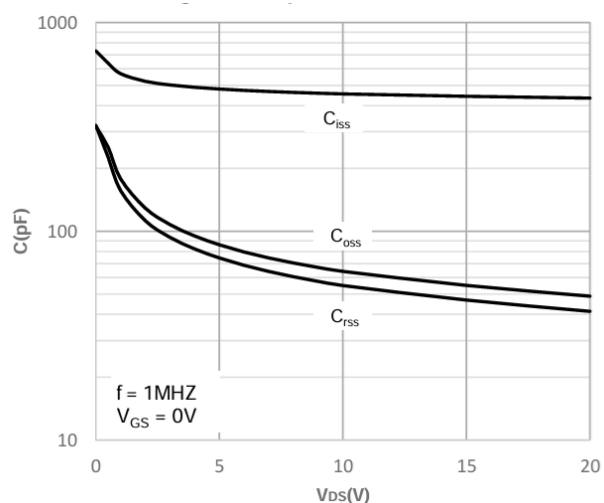


Figure 6 Capacitance Characteristics

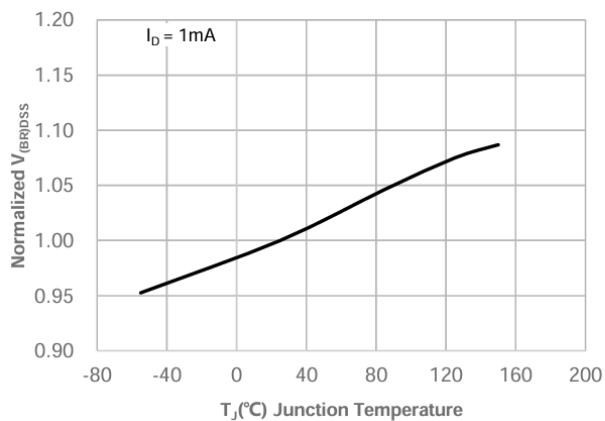


Figure 7 Normalized Breakdown voltage vs. Junction Temperature

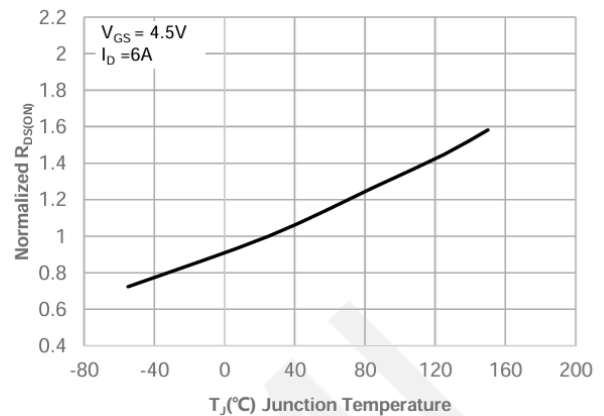


Figure 8 Normalized on Resistance vs. Junction Temperature

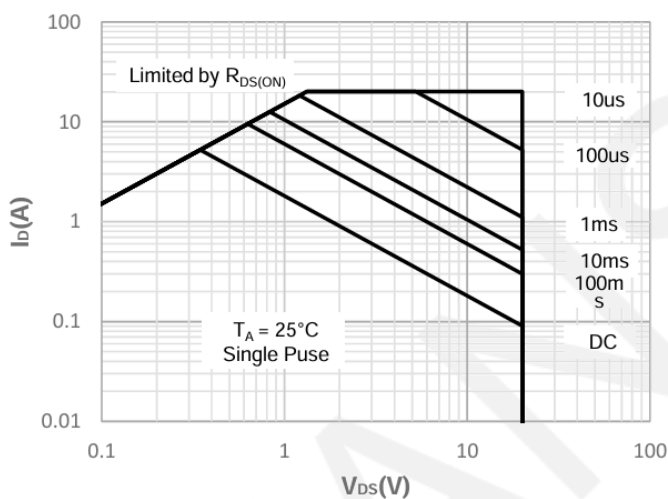


Figure 9 Maximum Forward Biased Safe Operating Area

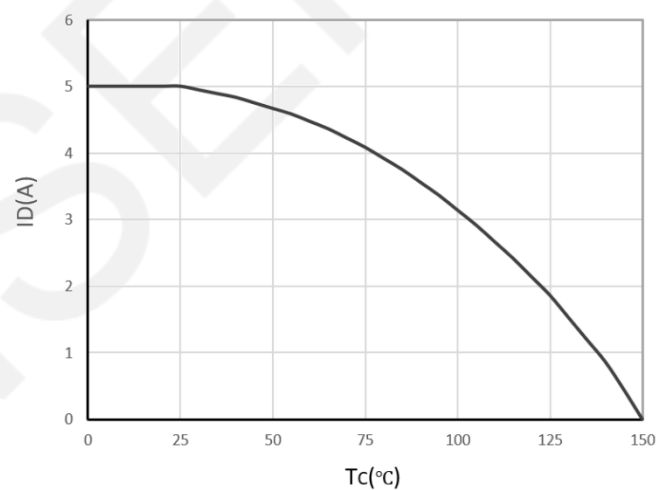


Figure 10 Maximum Continuous Drain Current vs. Ambient Temperature

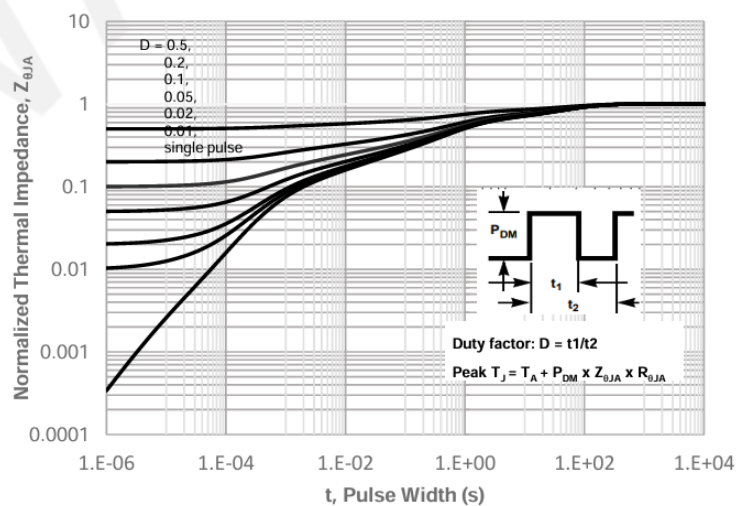
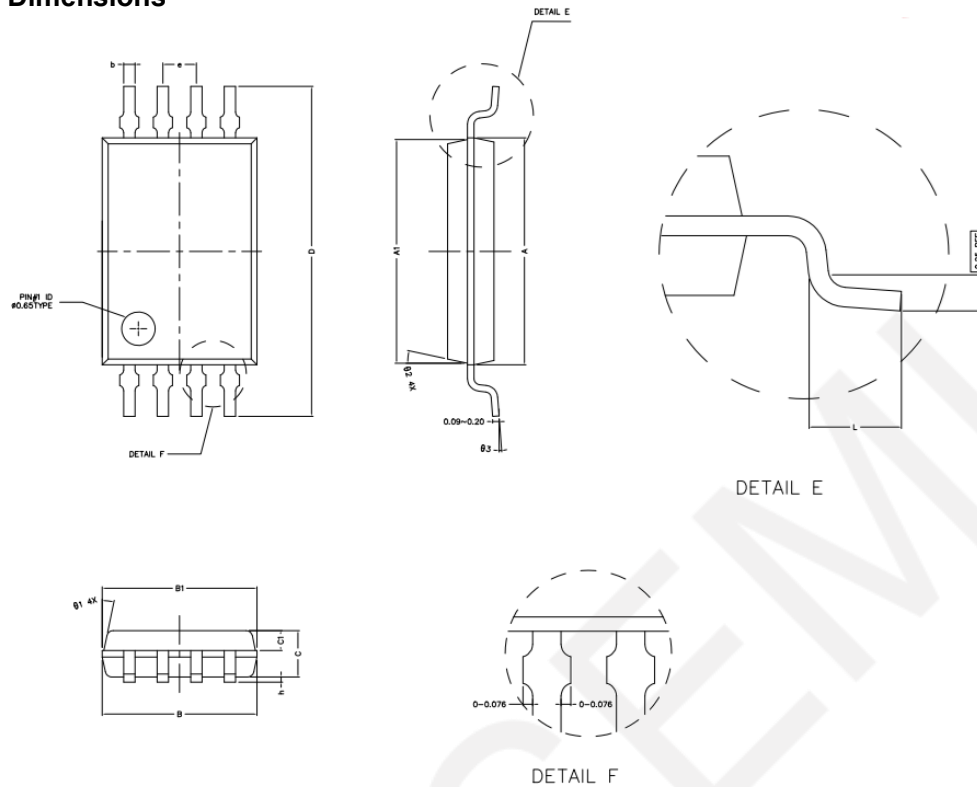


Figure 11 Normalized Maximum Transient Thermal Impedance
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8.Package Dimensions



COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	4.300	4.400	4.500
A1	4.240	4.340	4.440
B	3.050	3.150	3.250
B1	2.840	2.940	3.040
C	0.850	0.900	0.950
C1	0.337	0.387	0.437
D	6.250	6.400	6.550
L	0.450	0.600	0.750
b	0.170	0.220	0.300
h	0.050	0.100	0.150
e	0.650TYPE		
θ_1	12° TYPE		
θ_2	12° TYPE		
θ_3	0° ~ 7°		

9.Important Notice

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