



WANSEMI
万芯半导体

WP2N7002DW

Enhancement Mode N-Channel Power MOSFET

SOT363/NMOS/60V/ $\pm 20V$ /1.6V/0.35A/1.9 Ω

Rev0.1

60V, 1.9Ω, 0.35A, N-Channel MOSFET

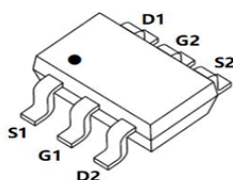
1.Features

- ◆ High Power and current handling capability
- ◆ Lead free product is acquired
- ◆ Surface Mount Package
- ◆ ESD Protected

V _{DS} Typ.	R _{DS(on)} Typ.	I _D Max.
60V	1.9Ω @ 10V	0.35A
	2.1Ω @ 4.5V	

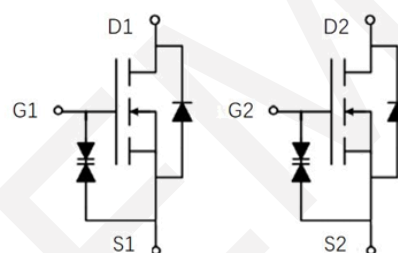
2.Applications

- ◆ Battery Protection
- ◆ Battery Powered Systems
- ◆ Power Management in Notebook Computer
- ◆ Portable Equipment



Pin Description

SOT363



Schematic Diagram

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP2N7002DW	7002	SOT363	3,000	120,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	60	V
Gate to Source Voltage	V _{GSS}	±20	V
Drain Current-Continuous	I _D	0.35	A
Drain Current (Pulse)	I _{DM}	1.4	A
Maximum Power Dissipation	P _D	1.9	W
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55 to +150	°C
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)	T _L	260	°C

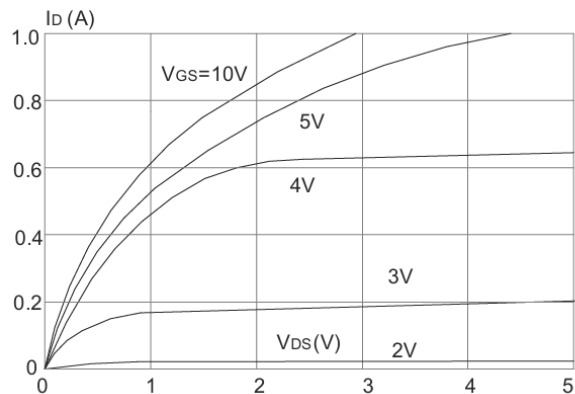
Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5. Electrical Characteristics at Ta=25°C (Note 2)

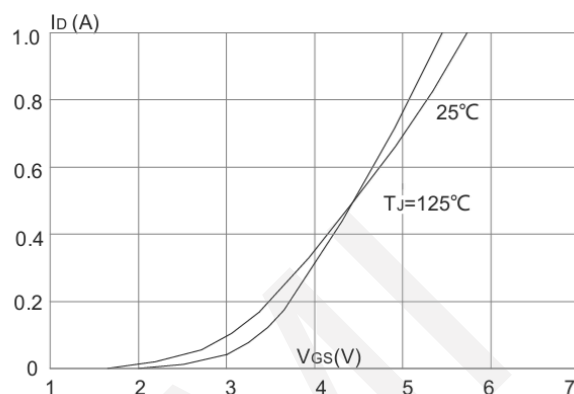
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250\mu A, V_{GS} = 0V$	60	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 10	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{DS} = 250\mu A$	1.0	1.6	2.5	V
Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = 0.3A, V_{GS} = 10V$	-	1.9	2.4	Ω
		$I_D = 0.2A, V_{GS} = 4.5V$	-	2.1	2.9	Ω
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = 30V,$ Frequency = 1.0MHz	-	28	-	pF
Output Capacitance	C_{oss}		-	11	-	pF
Reverse Transfer Capacitance	C_{rss}		-	4	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DD} = 10V, I_D = 0.2A,$ $V_{GS} = 10V, R_{GEN} = 10\Omega$	-	2	-	ns
Turn-ON Rise Time	t_r		-	15	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	7	-	ns
Turn-ON Fall Time	t_f		-	20	-	ns
Total Gate Charge	Q_g	$V_{DS} = 10V,$ $V_{GS} = 4.5V,$ $I_D = 0.3A$	-	1.7	-	nC
Gate-Source Charge	Q_{gs}		-	0.3	-	nC
Gate-Drain Charge	Q_{gd}		-	0.6	-	nC
Diode Forward Voltage	V_{SD}	$I_S = 0.3A, V_{GS} = 0V$	-	-	1.4	V

Note 2: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

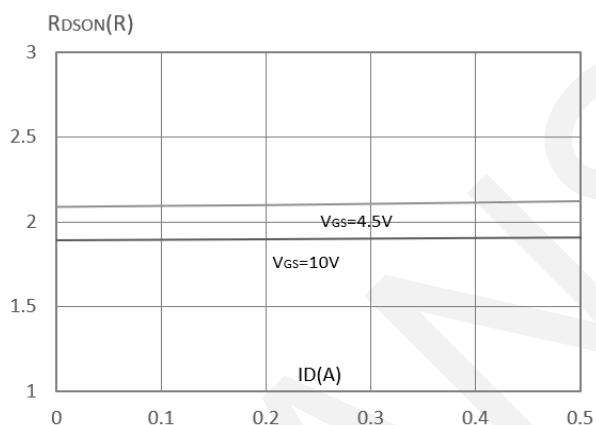
6. Typical electrical and thermal characteristics



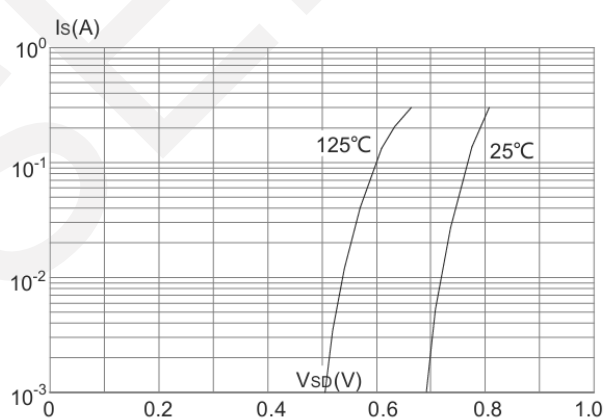
Output Characteristics



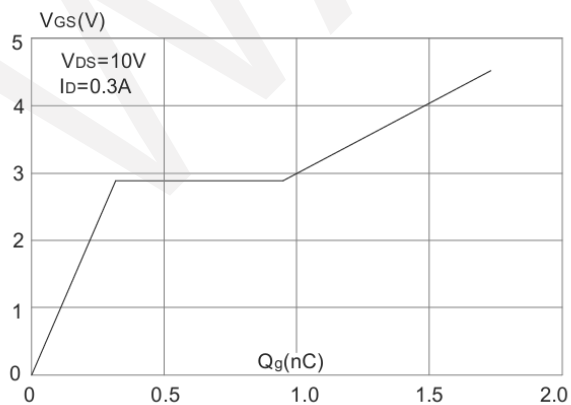
Typical Transfer Characteristics



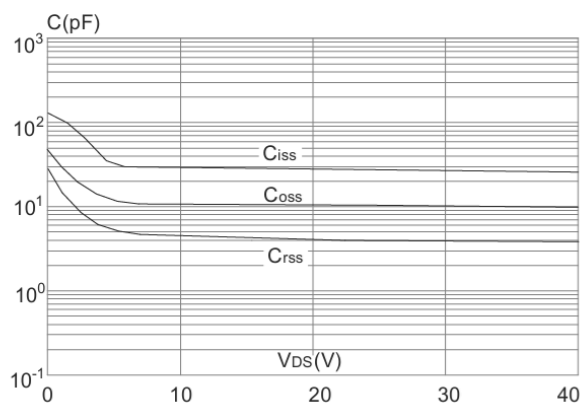
On-resistance vs. Drain Current



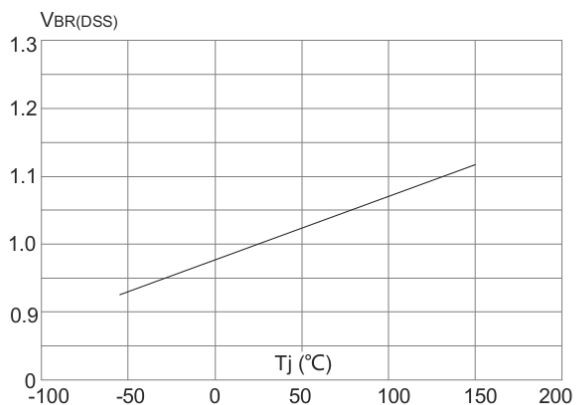
Body Diode Characteristics



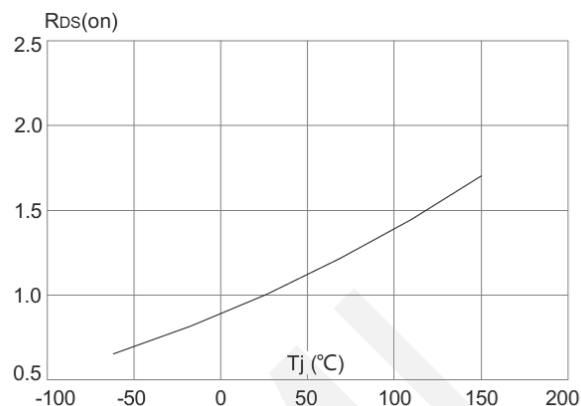
Gate Charge Characteristics



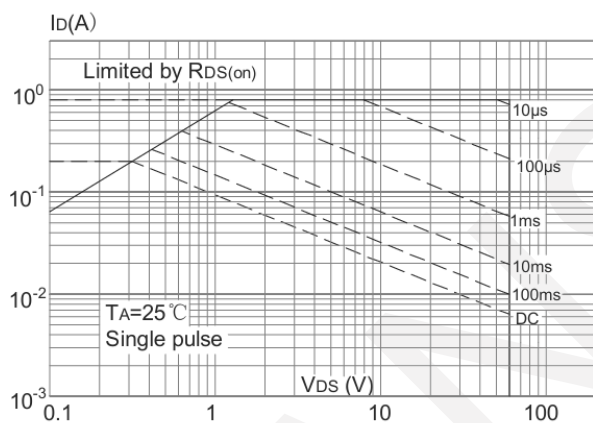
Capacitance Characteristics



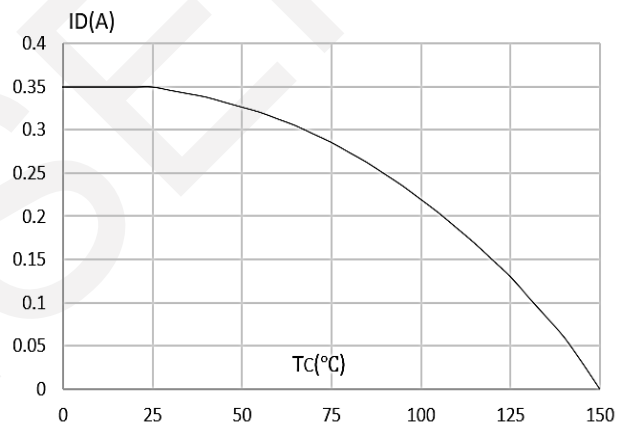
Normalized Breakdown Voltage vs.
Junction Temperature



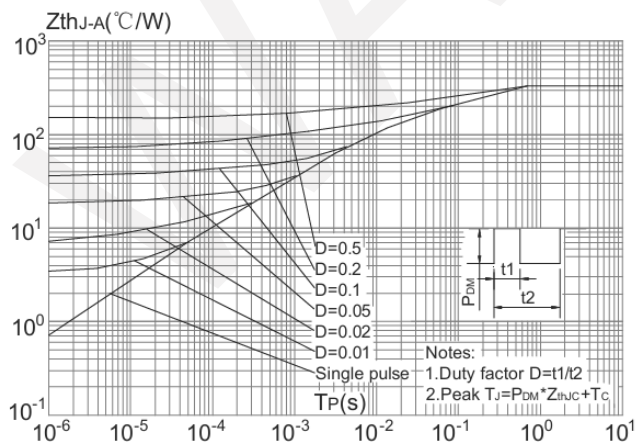
Normalized on Resistance vs.
Junction Temperature



Maximum Safe Operating Area

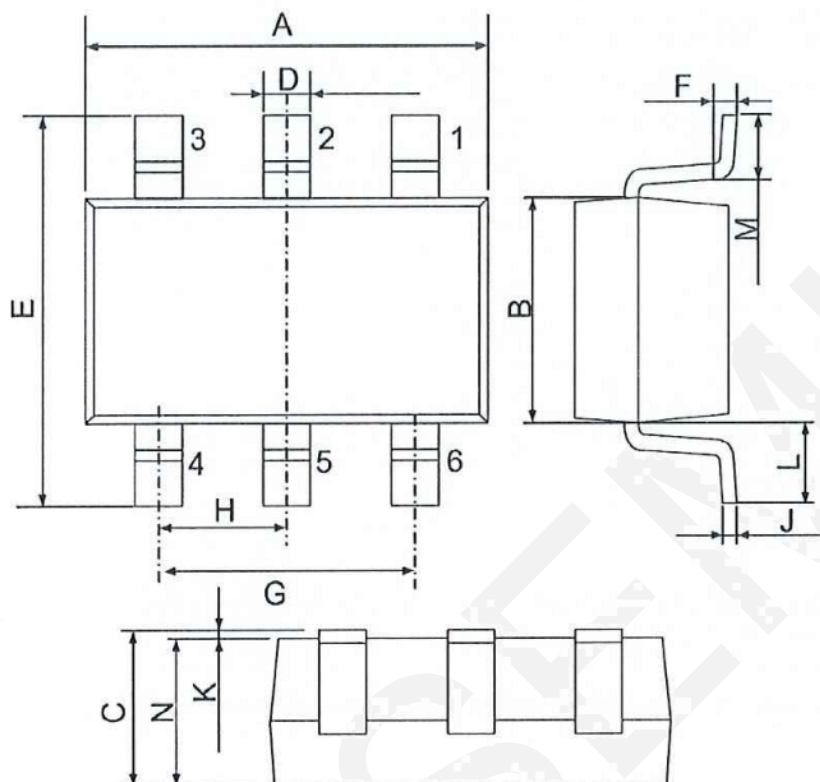


Maximum Continuous Drain Current vs.
Case Temperature



Normalized Maximum Transient
Thermal Impedance

7.Package Dimensions



UNIT: mm

DIM	MIN	MAX
A	2.00	2.20
B	1.15	1.35
C	0.90	1.10
D	0.15	0.35
E	1.95	2.25
F	0.20 Typ.	
G	1.20	1.40
H	0.65 Typ.	
J	0.08	0.15
K	0.00	0.10
L	0.525 Ref.	
M	0.26	0.46
N	0.90	1.10

8.Important Notice

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